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PowerBoot for CPCI-690/6190

Release Notes

P/N 6806800B69A
September 2006

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1 Introduction

This document refers to the PowerBoot for CPCI-690/6190 boards.

1.1 Purpose of this Document

This document describes the changes between Release 4.09 and Release 5.0 of the PowerBoot software for CPCI-690/6190 boards.

1.2 New Features of this Release

The following changes have been made in this release:

- Support for RoHS-compliant CPCI-6190 board
- Update of BIB driver to BIB version 2.1
- Support for 1 GB board variant

2 Known Limitations

None

3 Revision History

Order No.	Revision	Date	Description
220772	AA	April 2003	First release notes
220880	AA	May 2003	Release 4.03
220962	AA	May 2003	Corrected SDRAM configuration register settings
221201	AA	June 2003	Changes for release 4.04
221824	AA	October 2003	Changes for release 4.05
222190	AA	January 2004	Changes for release 4.06
223232	AA	April 2004	Changes for release 4.07
223458	AA	June 2004	Changes for release 4.08
226633	AA	June 2005	Changes for release 4.09
6806800B69	A	September 2006	Changes for release 5.0

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