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Find the *Abaco Systems / SBS / Greenspring IP-Digital482* at our website: *Click HERE*

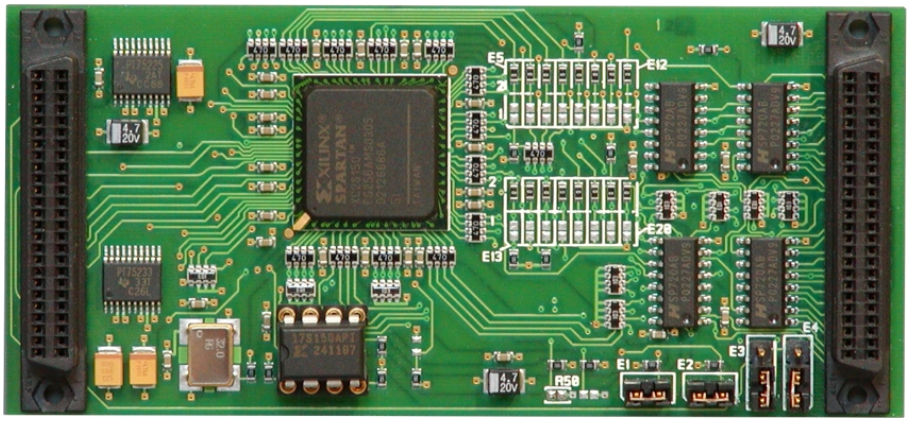
IP-DUALPIT2 &

IP-DIGITAL482

I/O & Communications

Features

- Single-wide IndustryPack
- IP-DUALPIT2 is a drop-in replacement for IP-DUAL-PIT
- IP-DIGITAL482 is a drop-in replacement for IP-DIGITAL-48
- IP-DUALPIT2 uses 32 lines for bit I/O, 8 bit or 16 bit (unidirectional, bi-directional, single or double buffered) plus 8 lines as programmable handshake lines or timer I/O with alternate ground wires
- IP-DIGITAL482 is configured for 48 general purpose lines of bit I/O. These lines may also be used for 8-bit and 16-bit I/O with handshake lines
- Two 24-bit synchronous counters
- 50-conductor flat cable interface connector



IP-DUALPIT2 & IP-DIGITAL482 IndustryPack® (IP) modules implement high density digital I/O. Both IPs are built from the same hardware but each has a different factory configuration and IDROM. Both emulate two Motorola MC68230 Programmable Interface/Timer (PI/T) ICs to implement a wide range of interface options.

IP-DUALPIT2

The IP-DUALPIT2 is configured to take advantage of the more sophisticated options of the ICs. These options include double buffered byte and 16-bit data transfers, bi-directional data transfer and hardware handshake. Timer and interrupt options are implemented.

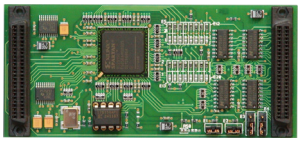
In Dual PI/T mode, 32 digital lines are available, plus timer I/O and hardware handshake lines that use alternate ground lines on the I/O cable to reduce noise. The digital lines have multiple programmable interface options, including bit I/O, byte I/O, double buffered input or output, bi-directional I/O and hardware handshake with integrated interrupts.

One or both of the two 24-bit timers may be used with other IPs to enhance functionality.

IP-DIGITAL482

The IP-DIGITAL482 is configured to maximize the number of digital I/O lines. The 48 lines are accessible as individual bits or as bytes.

One or both of the two 24-bit timers may be used with other IPs to enhance functionality.



IP-DUALPIT2 & IP-DIGITAL482

Specifications - Continued

Specifications

IP Interface

Interface
- Single-wide IndustryPack logic interface compliant to ANSI/VITA 4-1995

IDROM Data
- Format I

I/O Space
- Used

Memory Space
- Not used

Interrupts
- Int1 / Int2 used

DMA
- Not supported

Clock Rate
- 8 MHz

Module Type
- Type 1

Wait States
- ID reads: 0 wait states
- I/O reads and writes: 1 wait state
- Interrupt cycles: 1 wait state

I/O Interface

Interface Connector
- 50-connector flat cable

Termination
- 10 ohms resistor as pull up to 3.3V for each I/O line of port A and B, H2, H4, PIRQ#, TIRQ#

Input Voltage Range
- -0.5V to +5.5V (-2V or +7V for 11 ns)

Output High Voltage
- +3.3V

Output Low Current
- -2mA on port A and B, H2, H4, and port C (line 0-2, 4, 6, 7)
- -8mA on port C (line 3 and 5)

Output High Current
- +2mA on port A and B, H2, H4, and port C (line 0-2, 4, 6, 7)
- +8mA on port C (line 3 and 5)

Physical Data

Power Requirements
- 100 mA typical @ +5V DC, all lines are inputs

Operating Temperature Range
- -40° C to +85° C

Storage Temperature Range
- -65° C to +150° C

MTBF
- 823000h

Humidity
- 5% to 95% non-condensing

Ordering Information

IP-DUALPIT2	IndustryPack implements digital I/O with 32 TTL channels & timers
IP-DIGITAL482	IndustryPack implements digital I/O with 48 TTL channels

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