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Denali
HotSwap CompactPCI Single Board Computer

Features:

- PowerPC 603e@ 200MHz or PowerPC 740 @ 266MHz
- 64MB SDRAM with Parity
- 8.5MB FLASH
- 32KB NVRAM/RTC (Y2K Ready)
- 10BaseT/100BaseTX ETHERNET
- Two RS-232C Serial Ports
- ATM SAR with H&J *SoftATM*™ Software support
- Hot Swap per PICMG Spec. 2.1 R1.0
- Auto Sensing System/Non-System Functionality
- RTOS Support for VxWorks



Denali ™ is a 6U cPCI board with PowerPC 603e @ 200MHz processor (optional PowerPC 740 @ 266MHz). The board has 64 Mbyte of SDRAM, 8 Mbyte of flash memory, 512Kbyte socketed flash, 10BaseT/100BaseTX ETHERNET with interface on an RJ-45 connector, 32K byte NVRAM/RTC, two RS-232C ports with interface to DB-9 connectors, PMC interface, ATM SAR with dual J5 interfaces, PCI bridge to cPCI bus, watch dog timer, and interface to 8 digital I/O signals.

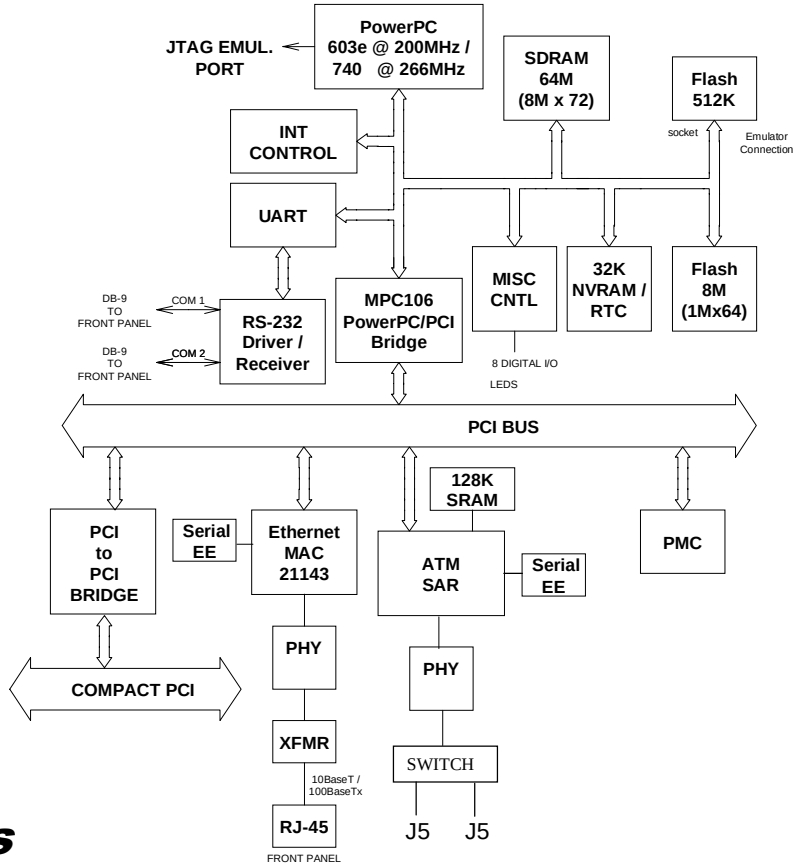
Denali introduces breakthrough technology that several customers have asked for: Hot Swap per the latest PICMG specification, System/Non-System Slot capability (a real benefit from a “spares” perspective), and ATM SAR functionality.

Denali provides an ATM SAR with 128K bytes of local SRAM data buffer. The SAR interfaces to an ATM PHY device via standard UTOPIA interface. The PHY devices handle Physical Media Dependent Sublayer functions for 25.6Mbps ATM networks. The PHY interfaces to two physical ATM connections on cPCI J5connector through a software controlled switch (high speed switching FET).

Real time kernels and ATM Software support is available off-the-shelf. Users can be up and running in hours with Denali as their ATM hardware partner.

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Denali Block Diagram



Denali Specifications

General	Model	Denali
	Form Factor	6U CompactPCI Single Board Computer
Processor	Clock Rate	266MHz (PowerPC 740) or 200MHz (PowerPC 603e)
	L1 Cache	32KB Data/ 32KB Instruction 16KB Data/16KB Instruction
cPCI Bus	Controller	DEC 21554
	Configuration	System/Non-system slot Auto-Sensing and “Hot Swappable”
PCIbus	Controller	MPC106
Memory	SDRAM	64MB Synchronous DRAM on Base Board
		Memory Timing 4-1-1-1 @ 66.6MHz bus (Parity Option)
	FLASH	512KB Boot Prom - 32 pin PLCC, socketed device
		8MB soldered on FLASH, 1M x 64 with 256KB sectors
	NVRAM/RTC	32K NVRAM and Real Time Clock with Battery Back Up (removable). Y2K Ready.
ETHERNET	Controller	DEC 21143 10BaseT/100BaseTX to Front Panel RJ-45
ATM	SAR	IDT77211 with 128KB SRAM buffer, UTOPIA Interface to ATM PHY (J5)
	Interface	J5 physical interfaces controlled by software switch
Serial Ports	Controller	National PC16552 UART
	Type	2 RS-232C (up to 115K baud.) Two Front Panel DB-9 connectors
Digital I/O		8 Digital I/O lines (J3)
PMC	1 PMC Slot	PMC connection allows IEEE 1386 compatible, single size PMC Module
		SBS/V•I Supported PMC’s include: FDDI, 1553, SCSI ,PCMCIA ,
		Graphics adaptors and more
JTAG		JTAG Emulator / Debug port on board
Software	BITe	Built In Test (optional) is available
	O/S Support	VxWorks available, LynxOS, pSOS+ and OpenBSD/RT planned
	ATM	H & J *SoftATM*™ Software Support
Electrical	Power	+5VDC @ 15 Watts, +3.3V @ 15 Watts
Environmental	Temperature	0 to +55C Inlet Air Operating, -40 to +85C Non-Operating
	Cooling	Forced Air 100 LFM Minimum Required
	Humidity	10 to 95% Relative Humidity, Non-Condensing

SBS Technologies, Inc.

SBS Embedded PPC Products (formerly V•I Computer)

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