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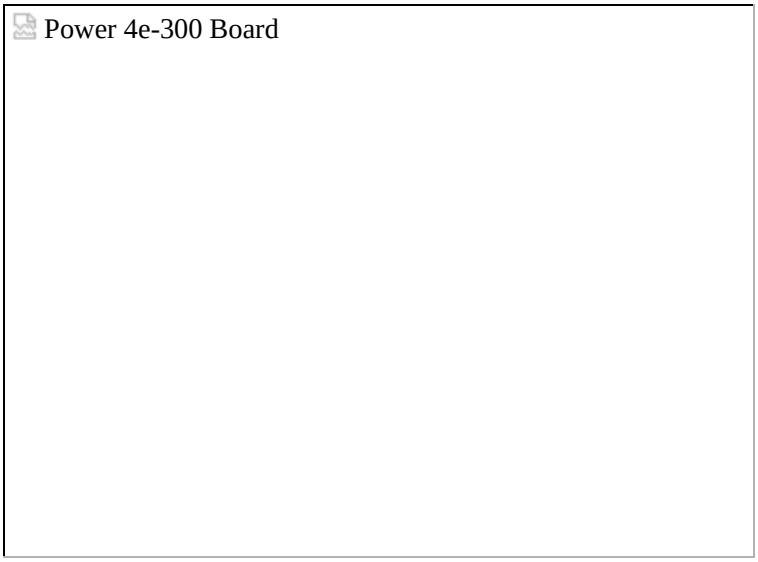
Find the *Abaco Systems / SBS / VI Computer Power-4E-200* at our website: *Click HERE*

VI Logo

PowerPC Logo

Power•4e-200

Very-High Performance PowerPC 604e Based VME Engine



Key Features

- 16 to 256 Mbytes of DRAM
- 512 Kbytes of Level 2 cache
- 8 Mbytes of FLASH Memory
- Motorola MPC106 PowerPC to PCI Bridge
- VME64 Interface
- 100Base TX and 10BaseT Ethernet
- Fast SCSI-20 Port
- Parallel Port
- 2 RS232 Serial Ports
- Real-Time Clock
- 8 Kbytes of Battery Backed NVRAM
- PMC Expansion Connector

The Power•4e-200 is a 200MHz PowerPC 604e based 6U VME Engine designed for extremely high performance embedded and real-time compute applications.

PowerPC **604e Microprocessor**

The IBM PowerPC 604e Microprocessor runs at **200MHz** internally and interfaces to the external memory subsystem at 66 MHz. The PowerPC 604e has a 32Kbyte Level 1 data cache and a 32 Kbyte instruction cache.

Features List

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16 to 256 Mbytes of DRAM

Supporting up to 2 Mezzanine Memory boards, the Power 4e supports up to 256 MBytes of EDO DRAM, with either parity or ECC support. The cycle time of the DRAM is 7-2-2-2 without ECC support, and 9-4-4-4 with ECC

Fast Ethernet

A DEC 21140 Ethernet controller provides 100 Base TX and 10Base-T Ethernet capability. The Ethernet Port is accessible via an RJ-45 connector on the front panel, and AUI Ethernet is available on P2.

Features List

Fast SCSI-20 Port

A Symbios/NCR 53C860 chip is used to provide the SCSI interface. The PCI interface allows the SCSI port to achieve a 20 Mbyte/Sec transfer rate. The SCSI port is available on the P2 connector and on the front panel.

Features List

Parallel Port

support. Individual mezzanine boards are available in 16, 32, 64, or 128 Mbyte configurations.

Features List

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512 Kbytes of Level 2 Cache

Organized as 64K x 72, the 9 nsec Synchronous SRAM achieves 2-1-1-1 access time from the 604e.

Features List

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8 Mbytes of FLASH Memory

Organized as 1 Mbyte x 64, with 256 Kbyte sectors, the on-board FLASH provides 8 Mbytes of non-volatile memory. Support for ROM emulation is provided via a standard 32 pin PLCC socket

Features List

Motorola MPC106 PowerPC to PCI Bridge

The Motorola PowerPC to PCI Bridge chip has a fully integrated system memory and cache controller to provide maximum PCI to memory throughput.

Features List

VME Interface

The Tundra Semiconductor Universe chip provides a single stage interface between the on board PCI bus and the VME bus. Extensive use of FIFOs allows this chip to decouple the two buses for increased throughput. A flexible mailbox-type interrupt mechanism, including both broadcast and unique addressing modes, enhances the standard VME interrupt structure.

Features List

A National Semiconductor PC87332 (Super I/O chip) provides an industry standard bi-directional parallel port.

Features List

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2 RS-232 Serial Ports

The National Super I/O chip contains two independent 16550 compatible UARTs. These ports all supports hardware handshaking. All via front-panel connectors.

Features List

Real-Time Clock

The SGS/Thomson M48T18 provides time-of-day clock and one-hundred year calendar.

Features List

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8 Kbytes of Battery-Backed NVRAM

The SGS-Thomson M48T18 also provides 8K x 8 of NVRAM backed by a 10 year lithium battery.

Features List

PMC Expansion Connector

The PMC connector allows for future expansion using an IEEE 1386 compatible, single size, PMC expansion module.

Features List

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