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Find the *Abaco Systems / Radstone PMC1553E-530* at our website: *Click HERE*

PMC 1553E

RADSTONE MEZZANINE



- Dual-channel, dual-redundant MIL-STD-1553B mezzanine
- Enhanced Mini-ACE
- 66 MHz, 32-bit PCI rev 2.2 interface
- Direct and Transformer Coupled
- Low power consumption
- 16 Mbytes FLASH On-board
- 8-bit TTL digital interface
- Air and Conduction-cooled ruggedization levels

Product Overview

PMC1553E is a single or dual channel MIL-STD-1553B mezzanine suitable for use in a wide range of embedded applications in both air and conduction cooled environments. Performance has been significantly enhanced over previous generations, with 66 MHz PCI bridging providing improved access to the 1553 bus. This also ensures greater compatibility with the latest generation PowerPC processor boards and CompactPCI single board computers.

Typical applications for PMC1553E include: mission electronics, navigational aids, target acquisition, UAV's, electronic countermeasures and stores management.

The module is compliant with MIL-STD-1553A, MIL-STD-1553B Notice 2 and STANAG 3838 protocols. Each channel may be independently selected as Bus Controller (BC), Remote Terminal (RT) or Bus Monitor (BM).

Direct and Transformer Coupled

Both direct and transformer-coupled signaling is routed to the P14 rear connector for maximum flexibility. Addressing for each channel on the PMC1553E may be set either by software or hard-wired from the backplane, via the P14 connector.

Low Power

Offering a typical thermal load of less than 4.5W, the mezzanine is designed for temperature extremes from -40°C to +85°C at the coldwall.

Software Support

Built-In-Test (BIT) firmware for the mezzanine can be supplied resident in FLASH. For ease of integration, Radstone also offers a high-level 1553 software library suitable for VxWorks, Greenhills Integrity, LynxOS and other real-time operating systems.

RADSTONE TECHNOLOGY

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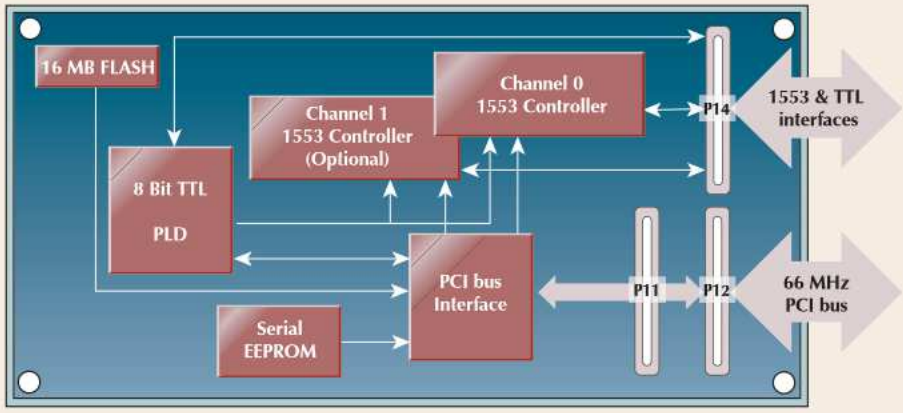


Figure1: PMC 1553E Block Diagram

Specification

Standards	**MIL-STD-1553A, MIL-STD-1553B Notice 2 and STANAG 3838**
Bus Interface	BU-61864 Single or Dual Channel
RAM (per channel)	64Kx17-bit RAM with parity
Digital I/O Interface	8-bit TTL, each line independently programmable as input or output
FLASH	16 MB (Megabytes)
EEPROM	32 KB (Kilobytes)
Host Interface	66 MHz, 32-bit PCI Local Bus specification Rev 2.2
Mean Time Between Failures	1,161,000 hours, Ground Benign 30°C
Form Factor	IEEE P1386.1 PMC specification. Draft 2.0 IEEE P1386 CMC specification. Draft 2.0 ANSI VITA20-2001 Conduction-cooled PMC Specification
Power	5V ±0.25V @ 0.85A typical (1.6 A max)
Dimensions	143.75 x 74 mm, stacking height 10 mm
Environmental	Ruggedization levels 1-5. See Radstone's ruggedization data sheet (RT184)

Standard Ordering Information

Sales Code	Description
PMC1553E-150	Single channel MIL-STD-1553B interface, ruggedization level 1
PMC1553E-250	Air-cooled level 2 as above with conformal coating
PMC1553E-350	Air-cooled level 3 as above with conformal coating
PMC1553E-450	Conduction-cooled level 4 as above with conformal coating
PMC1553E-550	Conduction-cooled level 5 as above with conformal coating
PMC1553E-155	Dual channel MIL-STD-1553B interface, ruggedization level 1
PMC1553E-255	Air-cooled level 2 as above with conformal coating
PMC1553E-355	Air-cooled level 3 as above with conformal coating
PMC1553E-455	Conduction-cooled level 4 as above with conformal coating
PMC1553E-555	Conduction-cooled level 5 as above with conformal coating
1553EP2-11	PMC1553E P2 adapter
C-CBL000039-412	PMC1553E P0 cable
1553-LIBMS	1553 Library Software Maintenance on CDROM

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