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GENERAL MICRO SYSTEMS, INC.

4740 Brooks Street
Montclair, California 91763
Phone: (714) 625-5475 Fax: (714) 621-4400
Marketing: Scott Bowman Technical: Scott Bowman

OS-9 SYSTEMS

GMS-SYSTEM 10-059 **Software Development**

Software	Professional OS-9		
On Board	CPU: 68020	FPCP: 68881	RAM: 4M
Ports	Serial: 2	Parallel: 16	
Storage	Hard Disk: 1	Size: 20M	Interface: SCSI
	Floppy Disk: 1	Size: 5 1/4"	
Features	19" Rack mount, desktop or portable.		

OS-9 BOARD-LEVEL PRODUCTS

GMSV07: CPU

68020 CPU, 16, 20 or 25 MHz, optional 68881 FPCP, 128K - 1M SRAM, four 32-bit EPROM, up to 4M DRAM, Z8536 programmable dynamic configuration controller with 3 timers, 68562 multi-protocol SCC, RS 232/422/485, 68155 VMEbus interrupt manager, 3 separate mailboxes.

GMSV06: CPU

68000 or 68010 CPU, 10 or 12.5 MHz, optional 68881 FPCP, up to 2M dual-ported DRAM, two 28-pin EPROM, Z8536 configuration controller/timer, 2 RS232, two 8-bit parallel ports, one buffered Centronics compatible, SCSI, 68450 DAM, battery-backed real-time clock/calendar, VMEbus interrupt.

SAM-VSB-1: I/O

Master/slave subsystem module allows memory access over the VSBbus (P2 connector) and message passing and memory sharing between CPU's.

SAM-VSB-2: I/O

VSB master-only subsystem module with floppy controller, SCSI with FIFO buffer for high-speed data transfer, real-time clock.

SAM-MIL-STD/1553: I/O

Two-channel differential serial communications interface, fully isolated I/O for daisy chain between communications modes.

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OS-9 BOARD-LEVEL PRODUCTS

SAM-SYS-F: I/O
Parallel I/O, floppy controller, SCSI asynchronous or synchronous, on-board DMA, battery-backed real-time clock.

SAM-SYS-G: I/O
Parallel I/O, GPIB interface, SCSI asynchronous or synchronous, on-board DMA, battery-backed real-time clock.

SAM-SCSI: I/O
SCSI controller, asynchronous or synchronous, FIFO buffer, floppy controller, real-time clock.

SAM-SERIAL I/O1: I/O
Intelligent 8-channel multi-protocol serial communications controller module, on-board 68020, mailbox communication between the on-board processor and host VO7 CPU of I/O overhead, all channels interrupt driven, duplex serial communications under DMA.

SAM-PARALLEL I/O: I/O
Parallel I/O, 40 bi-directional, I/O lines mated to FIFO buffer, 8 lines reserved for control functions other lines for byte, word or long word communication, direct download from FIFO to CPU permitted by on-board DMA.

VO5: Mass Storage
DRAM board, up to 32M, direct access via CPU bus, provides triple porting via SAM, VMEbus and VSB buses, external access to VO5 with concurrent CPU operation possible.

DRAM-04: Mass Storage
4M DRAM add-on memory device with parity, zero wait state, dual access to the VMEbus.

DRAM-01: Mass Storage
1M DRAM add-on memory device with parity, zero wait state, dual access to the VMEbus.

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OS-9 BOARD-LEVEL PRODUCTS

SRAM 256K: Mass Storage
High-speed memory module, 256K CMOS zero wait state RAM, twelve 32-pin socket array with 4 sockets reserved for up to 512K EPROM.

SRAM 1M: Mass Storage
High-speed memory module, 1M CMOS RAM, twelve 32-pin socket array with 4 sockets reserved for up to 512K EPROM.

OS-9 BOARD-LEVEL SYSTEMS

CPU	SERIAL PORT	MASS STORAGE	OTHER
VO6	(VO6)	(VO6)	
VO7	(VO7) SAM-SYS Modules	(VO7) SAM-SYS Modules	SAM-SYS Modules

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