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PMC QSIO: 'Commander'

Quad Synchronous / Asynchronous Serial I/O

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- **Key Features**
- **General Description**
- **Functional Description**
- **Specifications**
- **Ordering Information**



Data Sheet PDF



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Key Features:

- Quad Synchronous/Asynchronous Serial I/O
- Each Channel may be Configured as RS232/RS422/RS485 in Balanced or Unbalanced Modes
- Up to **10**MBit/Sec on Each Channel in Synch. Mode, Up to 153.6KBit/Sec in Async. Mode
- 32 Byte FIFO on Receive & Transmit for Each Channel
- Supported for HDLC, SLDC, Bisync and Byte-Oriented Protocols
- Internal or External Clocking for Each Channel on Transmit & Receive
- I/O via 68 Pin Front Panel Connector and/or PMC P4
- Support for Vx-Works®, and Phar Lap® Operating Systems
- Easy, Jumper-Less Configuration Setting

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General Description:

The Quad Serial I/O module the "Commander" is the industry's first quad multi-protocol/multi-interface standard, PMC module. The "Commander" provides up to four fully independent channels of asynchronous and/or synchronous communications protocols with EIA-232C or EIA-422/485 line interfaces in balanced or unbalanced modes. Each channel can communicate up to 153.6KB/S in asynchronous mode or up to **10**Mb/S in synchronous mode. For ease of configuration, each channel can be configured to any of the line interface options simply by removing a resistor pack from a high reliability machine screw socket and plugging it into the adjacent socket. This prevents the use of small jumpers, which can be lost or even dropped in the system, and can cause major damage. Each channel on the Commander is connected to the PMC front panel via a 68 Pin high-density connector and to the PMC P4 connector which is then brought to the VME P2. When the PMC QSIO is used in a synchronous mode, the source of clock can be internal clock via the 3.6864MHz oscillator or a custom oscillator which can be installed on the QSIO or the clock can be provided from an external source.

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Functional Description:

The GMS PMC QSIO takes advantages of the PCI bus with its fast transfers and support for resource management. The PCI bus is connected to the Tundra's QSpan device, which converts the PCI bus to a Motorola-style local bus. The local bus then is connected to two, industry standard Zilog Z16C30 Multi Protocol Serial Controllers. Each device is capable of supporting a variety of asynchronous and synchronous protocols. In asynchronous mode, data rates up to 153.6KB/Sec on all of four channels can be sustained and in synchronous mode data rates of up to 3.6Mbits/Sec can be sustained on all four channels. The synchronous protocols include HDLC, SDLC, Bisync, and many byte oriented protocols. Each Z16C30 device is clocked via an on-board 3.6864MHz oscillator, or provisions have been provided for a user oscillator for custom operations. Further, each device may be clocked by the external source, for remote sync operations. The serial devices are connected to two sets of buffers, EIA-232C and EIA-422/485. In the EIA-422 mode, each channel can operate in a balanced mode or in an unbalanced mode. All the board configurations are made via a resistor network array, instead of jumpers and straps. Software drivers for Wind River VxWorks®, and Phar Lap® are available in binary and source from General Micro Systems, Inc.

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Specifications:

Operating Specs		
Characteristic	Minimum	Maximum
Power Consumption	5.9 W (Typical)	
Supply Voltage	4.87 VDC	5.25 VDC
Temperature	0 °C	60 °C
Humidity (non-condensing)	5%	95%
Altitude Range	0 feet (0 meters)	10,000 feet (3,408 m)
Shock	5 g pk, 10 msec, 1/2 sine wave	
Non-Operating Storage		
Characteristic	Minimum	Maximum
Temperature	-55 °C	150 °C
Humidity (non-condensing)	5%	95%

Altitude Range	0 feet (0 meters)	40,000 ft (12,192 m)
Shock	20g pk, 30msec	
Physical Specifications		
Dimension	Measurement	
Height	0.650 in	
Length	5.865 in	
Width	2.912 in	

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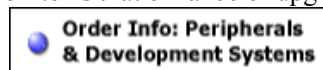
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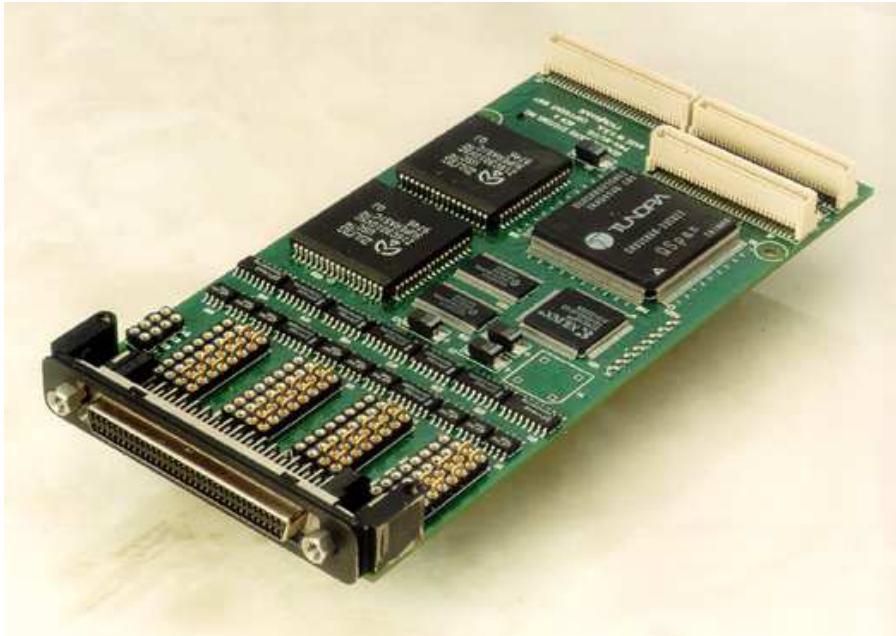
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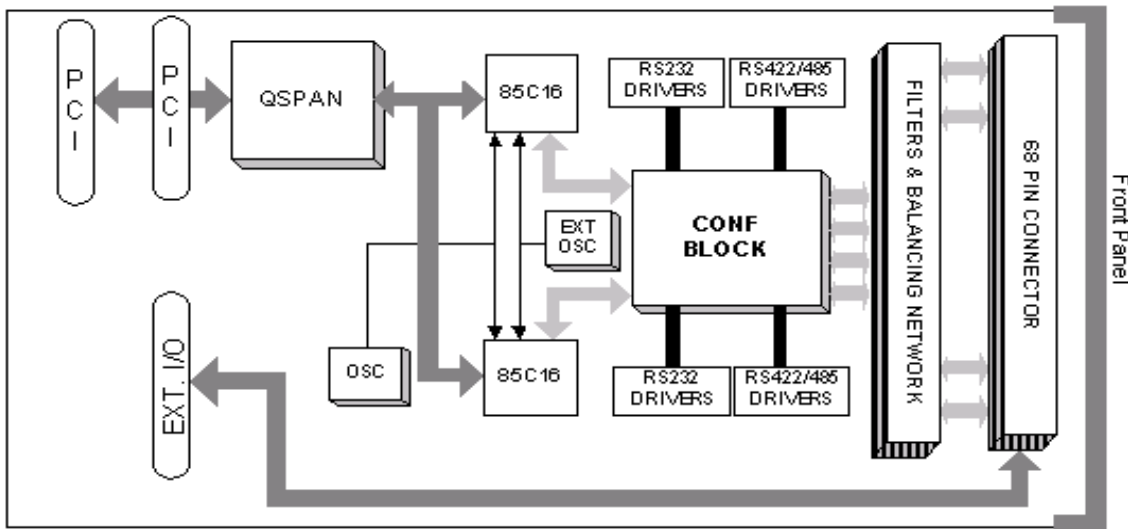
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