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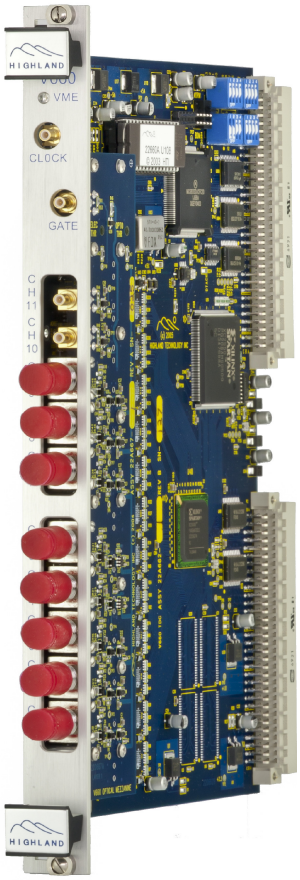
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HIGHLAND TECHNOLOGY

V660 12-channel VME picosecond resolution time-interval measurement module

Features



- 12-channel, 32-bit resolution timestamper
- Measures time intervals up to 6.71 seconds
- Each of 12 channels is an independent timestamper
- 256-sample FIFO memory for each channel
- Jitter typically below 75 picoseconds RMS
- Includes option of up to 8 M RAM for event buffering
- 32-bit VME module
- Available with electrical or optical inputs

The V660 is a 12-channel time-to-digital converter used to record the time of occurrence of twelve independent electrical pulse inputs, each independently timestamped and buffered in FIFO memory.

The V660 may be used in streaming timestamp or single-shot event modes. Each channel will timestamp events separated by 150 nanoseconds or more.

18 Otis Street San Francisco, CA www.HighlandTechnology.com tel: 415 551-1700 fax: 415 551-5129

Specifications : V660 12-channel VME picosecond resolution time-interval measurement module

FUNCTION	12-channel VME picosecond resolution time-interval measurement module
DEVICE TYPE	Register based slave: A16:A24:D32:D16:D08 (EO)
INPUTS	With V664 standard mezzanine board: 12 logic-level differential ECL front-panel inputs, T0 - T11 One differential ECL Gate Input One TTL Gate Input One general-purpose TTL logic I/O
INPUT LEVELS	Accepts differential ECL positive-edge triggers Optional LVDS or coaxial inputs with programmable trigger level and polarity
RESOLUTION	User selectable 24.4 / 97.6 / 390.6 / 1562.5 ps
RANGE	32 bits User selectable 0.105 / 0.419 / 1.67 / 6.71 sec
JITTER	<(65ps + 0.75 LSB + timebase) RMS
LINEARITY	<0.5 LSB
MEMORY	256-sample FIFO memory per channel with associated control/status register
TIMEBASE	Internal temperature compensated crystal oscillator Phase-lockable to external 10 MHz reference
POWER	+5V 1.3A ; +12V 300 mA; -12V 250 mA typical
CONNECTORS	Mezzanine options: SCSI-II ECL SCSI-II LVDS Optical (10 inputs maximum) type ST TTL (12 inputs maximum) type SMB SMB aux gate, clock
INDICATORS	LED indicates module VME access
PACKAGING	6U single-wide VME module
CONFORMANCE	ANSI/IEEE 1014-1987 VMEbus specification

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