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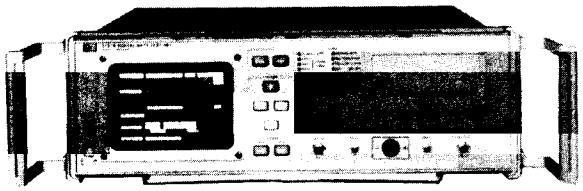
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TELECOMMUNICATIONS TEST EQUIPMENT

A total Solution for Digital Data Services and Equipment Testing

Model 3787B

- ISDN 'B' or 'D' channel Protocol Analyzer interface
- Comprehensive bit error measurements and analysis at DS1C, DS1, DS0 and DDS interfaces.
- Sub-rate drop and insert capability.
- Built-in data logging capability.
- DS1 Jitter analysis.



HP 3787B

DESIGNED FOR HP-IB SYSTEMS

RS-232-C

HP 3787B Digital Data Test Set

Description

The HP 3787B Digital Data Test Set is a Bit Error Rate Tester (BERT) designed for use on T1 leased, Digital Data System (DDS), 56kbit/s switched and Packet Switched services transmission line and multiplexing equipment.

Digital data circuits are sold with a guaranteed Quality-of-Service (eg DDS circuits offer >99.5% Error Free Seconds in a 24 hour period and 99.95% Availability). To maintain this high quality of service, Service Providers need test equipment that will provide in-service network monitoring and fast out-of-service testing.

The HP 3787B has a comprehensive range of in-service features (eg. Frame, CRC, BPV and Jitter analysis, alarm monitoring, internal printer for data logging etc) which maximizes circuit availability by detecting circuit deteriorations and intermittents, before they seriously affect the service.

The HP 3787B also provides comprehensive out-of-service features (eg Logic (binary) test patterns, Frame Slips, full range of T1/DDS loopback codes etc) which are used to provide qualitative results for circuit troubleshooting and end-to-end test results which relate to actual in-service performance.

Signaling System No. 7 and DMI testing

For the development or maintenance of DMI (Digital Multiplexed Interface) and Signaling Systems No. 7, you can connect a protocol analyzer to the HP 3787B and substitute the BER test pattern with one from the protocol analyzer. In this mode, the HP 3787B acts like a DS1 Channel Access Unit - enabling any 'B' or 'D' channel to be accessed for protocol analysis.

Who needs an HP 3787B?

If you are involved in manufacturing, installing, commissioning or maintaining combined DS1/1C/DDS systems you have the problem of buying test gear to match your test requirements. You need DS1/1C testers, DDS testers and DS1/1C channel access equipment etc.

The HP 3787B solves this problem by satisfying all of these test requirements and more in a single unit. The combination of three test sets in one is not the end of our total solution - we also have a built-in printer, many methods for presenting the error information (e.g. Count, BER, ES, EFS etc) and error analysis (e.g. % Availability, %EFS, %Severely Errored Seconds, Consecutive SES etc.).

Specifications

For details, ask your local HP sales Office for an HP 3787B data sheet and specification booklet.

Drop and Insert

If the Transmitter/receiver signal is suitably framed (eg a DS1 ESF signal), test patterns or control codes can be inserted into/extracted from the following:

An individual customer timeslot (64k and 56kbit/s).

DDS 2.4k, 4.8k, 9.6k, 19.2k and 56 kbit/s primary and secondary channels (DS0A and DS0B).

4kbit/s Datalink (DS1 ESF).

4kbit/s Fs channel (DS1 Ft).

8kbit/s R-channel (DS1 T1DM).

Measurement Capability

Operating Frequency: DS0 (64kbit/s), DS1 (1.544Mbit/s), DS1C (3.152Mbit/s).
Framing: DS1 (SF, ESF, T1DM and Ft only), DS1C and DS0B.
Line Code: B8ZS, AMI.
Error Types: Logic (Binary), Bipolar Violations, Frame Word, CRC-6 Word.
Error Results: Error Count, Error Ratio, Error Seconds, Error Free Seconds, % Error Free Seconds.
Error Analysis: % Availability, % Unavailability, % Severely Errored Seconds, % Error Seconds, % Degraded Minutes, Count Consecutive SES, Count SES, Count ES, Count Deg Min.
Alarm Seconds: Instrument Power Loss Seconds, Signal Loss Seconds, AIS Seconds, Frame Loss Seconds (ie DS1C, DS1 or DS0B), Test Pattern Loss Seconds.
Frame Slips (Controlled): Duplicated frames are indicated as positive frame slips. Deleted frames are indicated as negative frame slips.
Protocol Analyzer Interface: RS-232-C 4-wire synchronous interface. When interfacing at DS1 or DS0, the following channels can be accessed: DDS primary and secondary channels, ISDN 'B' or 'D' channels (64/56 kbit/s), ESF datalink or D4 Fs channel.
DS1/DS1C Signal Voltage: Positive and Negative peak voltage displayed.
DS0 Bit Monitor: Selected received customer bytes displayed.
Signalling Bits: A,B (SF) or A,B,C,D (ESF) signalling bits can be set and displayed when 56kbit/s circuit switched is selected.

DS1 Jitter Measurement (Option 001)

Peak-to-Peak Jitter: Range 0.00 to 10.00 Unit Intervals (UI) pk-to-pk, in 0.01 UI steps.
Jitter Filters: LP=2Hz to 40kHz, HP1+LP=10Hz to 40kHz, HP2+LP=8kHz to 40kHz.
Jitter Hit Threshold: 0.05 to 10.00 UI pk-to-pk in 0.01 UI pk-to-pk increments.
Jitter Hit Measurements: Jitter Hit Count, Jitter Hit Bit Count, Jitter Hit Bit Ratio, Jitter Hit seconds, Jitter Hit Free Seconds.

Ordering Information	Prices
HP 3787B Digital Data Test Set.	$8,990

HP 3787B Options

001: DS1 Jitter.	$925
002: DC Capability.	$615
909: 19-inch Rackmount.	$154
910: Extra Operating and Service Manuals.	$103

Accessories available

HP 15668A: Front Panel DDS Clock Cable.	$128
HP 15669A: Rear Panel DDS Clock Cable.	$128

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