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Find the *Intel / Wind River VisionPROBE II* at our website: *Click HERE*

WindRiver

visionPROBE II Datasheet

Introducing visionPROBE II, a hardware-assisted tool that utilizes on-chip debugging services embedded in the microprocessor. It comes with a software debugger for a fully-integrated hardware and software solution, and provides the industry´s leading capability for board bring-up, flash programming, and production/testing.

visionPROBE II provides a high-speed parallel connection between a host debugger and target microprocessor for PC-based development. It is the second-generation release of Wind River´s proven high-performance emulation system, and it offers industry-leading download speeds, overall debug through-put, and unprecedented debug efficiency.

visionPROBE II allows developers to perform common source-level debug activities, such as watching memory and controlling large numbers of registers in a matter of seconds. Developers are able to shorten edit/compile/download/debug cycles and make more turns, even with the most complex of applications.

High-performance JTAG
visionPROBE II eliminates slow download times and run control when developing with OCD (on-chip debug) microprocessors. With new hardware logic that caches common JTAG scan chains, visionPROBE II delivers dramatic performance when developing with OCD microprocessors.

Feature summary

- ***High-speed parallel download:*** Download at 750KB/second with certain targets. Typically *4-10 times faster* than other products.
- ***Modular design and target versatility:*** visionPROBE II features a modular design that allows developers to expand and intergrate with visionTRACE logic analyzers, and to move from one target to another using the same debugging hardware.
- ***RTOS awareness:*** Obtain detailed information on any RTOS object.
- ***On-chip debug target control:*** Start/stop target, set internal hardware/software breakpoints, take a target snapshot, reset the target, step on statement/instruction into function calls, step over or out of a function.
- ***Built-in hardware diagnostics:*** Includes a comprehensive suite of RAM tests, scope loops, and CRC checks.
- ***Flash memory programming:*** A complete library of turnkey algorithms for programming flash devices.
- ***External triggering:*** A trigger in/out port and cable for triggering connected external devices.
- ***Source-level debugging:*** visionCLICK visionXD, and SingleStep C/C++ source code debuggers. Integrated with Tornado, VxWorks, and other third-party operating systems.
- ***Internal register configuration:*** An automated internal register configuration utility with bit-level detail and online help.
- ***Additional custom registers:*** Supports 32 groups, a total of 960 custom registers.

Embedded Development Solutions

Fully integrated solutions
Wind River offers fully integrated hardware and software solutions designed for all phases of

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RTOS independent
Wind River provides its extensive line of embedded development tools for all embedded real-time operating systems (RTOSs) including In-house Proprietary RTOS, Wind River RTOSs, and other commercial embedded operating systems. Published APIs are provided to support easy integration of Wind River tools with other embedded operating systems.

Tornado integration
visionPROBE is tightly integrated with Tornado and enables Tornado Tools to communicate with the target through JTAG. This provides board bring-up and debug support, crash diagnosis support, as well as a download and debug channel, if the Ethernet and serial channels are used by the application.

The visionWARE developers kit
visionWARE development kit is specifically designed for engineers developing software that interacts with, and controls hardware. The visionWARE development kit provides components in a source code format which accelerates and simplifies writing target diagnostics, initialization code, device drivers, and BSPs. Additionally, the visionWARE run-time component is included and provides bootloading, flash support, http server, and other components.

Features

- High-speed parallel download
- Target versatility
- RTOS awareness
- On-chip debug target control
- Built-in hardware diagnostics
- Flash memory programming
- External triggering
- Source-level debugging
- Internal register configuration
- Statistical Performance Analysis
- Additional custom registers

Supported targets*

- Motorola/IBM PowerPC architecture family
  - Motorola MPC5xx/74xx/8xx/82xx
  - Motorola 68K CPU32
  - Motorola/IBM PPC6xx/7xx
  - IBM PPC4xx
- Motorola ColdFire architecture family
  - Motorola MCF520x/530x/540x
- Hitachi SuperH architecture family
  - SH4/SH7750/SH7751
  - SH3/SH7709A/SH7729
- MIPS32 architecture family
  - 4Kc/4Kp/4Km
  - IDTs RC32332/334/364/355
  - Philips PR3940
  - Alchemy Semiconductor's AU1000
  - Broadcom's BCM3350, BCM1100
  - Philips PNX852X, PNX832X
- ARM architecture family
  - ARM7TDMI, ARM710, ARM720T, ARM740T
  - ARM9TDMI, ARM920T, ARM922T, ARM940T
- INTEL XScale
  - 80200, with 80312 memory controller

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