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LP5000

LASAR Post Processor and Runtime Software



- Conversion of LSRTAP files into Digital Test Programs
- Compatible with SR2500 and SR5000 Digital Test Subsystems
- Graphical User Interface
- Post Processors for Simulation Vectors, Fault Dictionary and Guided Probe Nodal Data
- Perform GO/NOGO Testing, Fault Dictionary Fault Isolation Testing and Guided Probe Fault Isolation Testing
- WIN Framework VXI Plug & Play Interface
- Runs Under Windows NT

The LP5000 is a software Post Processor that converts Teradyne LASAR logic simulation, fault simulation, and guided probe data into test programs for use by Interface Technology SR2500 and SR5000 VXI Digital Test Subsystems (ITDTS), and the associated runtime controls for loading and executing test programs. The LP5000 was developed specifically as a direct LASAR to ITDTS link, and takes full advantage of all of the high speed timing, pin formatting, and programmable features of these instruments. The software is available as Microsoft Windows NT DLLs, which may be called from other Windows applications, such as a test executive. As such, the user interface consists of graphical icons, buttons, and displays to simplify the operation of post processing test data, running tests on a UUT, reporting failures back to the operator, and performing various diagnostic routines on a failed UUT.

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Post Processor

The LP5000 Post Processor converts Teradyne LASAR logic simulation data into test programs for use by Interface Technology SR2500 and SR5000. If present, the LASAR fault simulations and guided probe data are also converted into formats compatible with the LP5000 runtime controls.

The Post Processor uses LASAR simulation output files (LSRTAP) and other instrument dependent files to create test program files and if enabled, UUT fault dictionary and guided probe information files.

GO/NOGO Testing

GO/NOGO testing is performed by the GO/NOGO Runtime Control. The GO/NOGO function is initiated via a DLL function call. The function call may be embedded within a Test Executive, or from within other Windows applications. When called, the DLL function loads the translated test program into the digital instrument, executes the test program, and returns a pass/fail status to the calling test executive system.

If the circuit card passes the GO/NOGO test, a thumbs up message will be returned to the calling application. If the circuit card indicates a failure, the test will terminate on the first failed pattern and report back which pattern and which pin failed.

Fault Dictionary Fault Isolation

Fault Dictionary fault location is performed by the Fault Dictionary browser, which uses the fault simulation data extracted from the LSRTAP file by the post processor. Along with the UUT edge I/O pin states and nodal information, the dictionary is used to match simulated failure signatures with those actually produced by a failed board. To accomplish this, the application initiates the browser via a DLL function call if the GO/NOGO test returns a fail status. The test will execute from end to end and collect every failed pattern and bit. The Fault Dictionary browser then compares the actual fault signature with those extracted from the LASAR Fault Dictionary. The nearest 10 matches will be reported back to the calling application, which can display the failure information to the user.

Guided Probe Fault Isolation

If enabled by the Post Processor, Guided Probe fault isolation is performed automatically by the probe back-trace algorithms and run-time engine if the Fault Dictionary cannot resolve a failure to only one exact match. The probe backtrace algorithm uses nodal pattern data and topological circuit information extracted from the LSRTAP files by the post processor. Two options for guided probing are available, depending on whether the Fault Dictionary is enabled by the Post Processor:

- Start probing from a failed circuit card output pin and trace back to the failure.
- Start probing from a node suggested by the Fault Dictionary.

If a trace back loop is encountered during probing, an appropriate message will be issued and all components in the loop will be listed. An option is available to reprobe nodes along the faulty path.

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