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Find the *Interface Technology SR2520GP* at our website: *Click HERE*

SR2520

SR2500 Expansion Module with Guided Probe Option



- DC to 25 MHz Operation
- Dual Threshold Comparators
- 64K or 256K Vector Depth
- Initiate Button Located on Probe Body
- User Replaceable Probe Tip
- Enable/Disable Probe Testing Per Vector
- "Learns" Known Good Responses From UUT
- Active Input Buffer
- 100K Ohm Input Impedance
- Expect, Mask, and Record Memories
- Record High, Low, Indeterminate and Error Information per Vector
- Record Vector Count/Time Tag
- 24-Bit Continuous Cycle Counter
- Record Probe Contact per Vector
- Detect Logic High Input with LED Indicator
- Detect Logic Low Input with LED Indicator
- Detect Indeterminate / Float / Tristate Logic Input
- Programmable Input Thresholds
- 40.0V Overvoltage Protection
- Hardware Signature Generation (Polynomial CRC)
- Edge Sample / Compare or Window Compare Modes
- Programmable Sample Strobe/Window
- Minimum Pulse Width 10.0 ns
- Static Analog Measure, 12-Bit Resolution, ± 10.0V Range

The SR2500GP Guided Probe Module provides the capability to probe nodes on the UUT, measure the state response, and determine pass / fail conditions. Adding the Guided Probe to the SR2500 Digital Test Subsystem does not diminish the available I/O pin count. Instead, the Guided Probe actually adds 20 additional clocks, triggers and sync to the SR2500.

The Guided Probe is capable of testing and detecting high, low and indeterminate states. It can also calculate a CRC checksum based on the input stream, detect pulses and make static measurements of analog voltages. Upon determination of the pass / fail state, the Guided Probe will store the information from the node, the results of the real-time compare between the measured response and the expected response, and information indicating if the node is actively driven or tristated. The Guided Probe will also store the state of a 24-bit vector counter (time tag) and a 4-bit, user defined tag.

The probe itself uses an active input to reduce circuit loading on the test node and to condition the UUT signal prior to routing it to the Guided Probe logic within the SR2520 Expansion Module. An activation switch is located on the probe to trigger or continue test execution. The SR2500GP Guided Probe is supplied as an option to the SR2520 Expansion Module.

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SR2520 Guided Probe SPECIFICATIONS*

Frequency Range: DC to 25 MHz

Minimum Pulse Width: 10.0 ns

Modes: Edge sample / compare
Window compare

Vector Depth:

Standard	64K
Optional	256K

Input Impedance: 100k ohms

Resolution: 12-bits, standard analog measure, ±10.0 volt range

Memory Types:

Stimulus	Output, Tristate
Response	Expect, Mask
Record	

Indicators:

Detect Logic High	LED (green)
Detect Logic Low	LED (red)

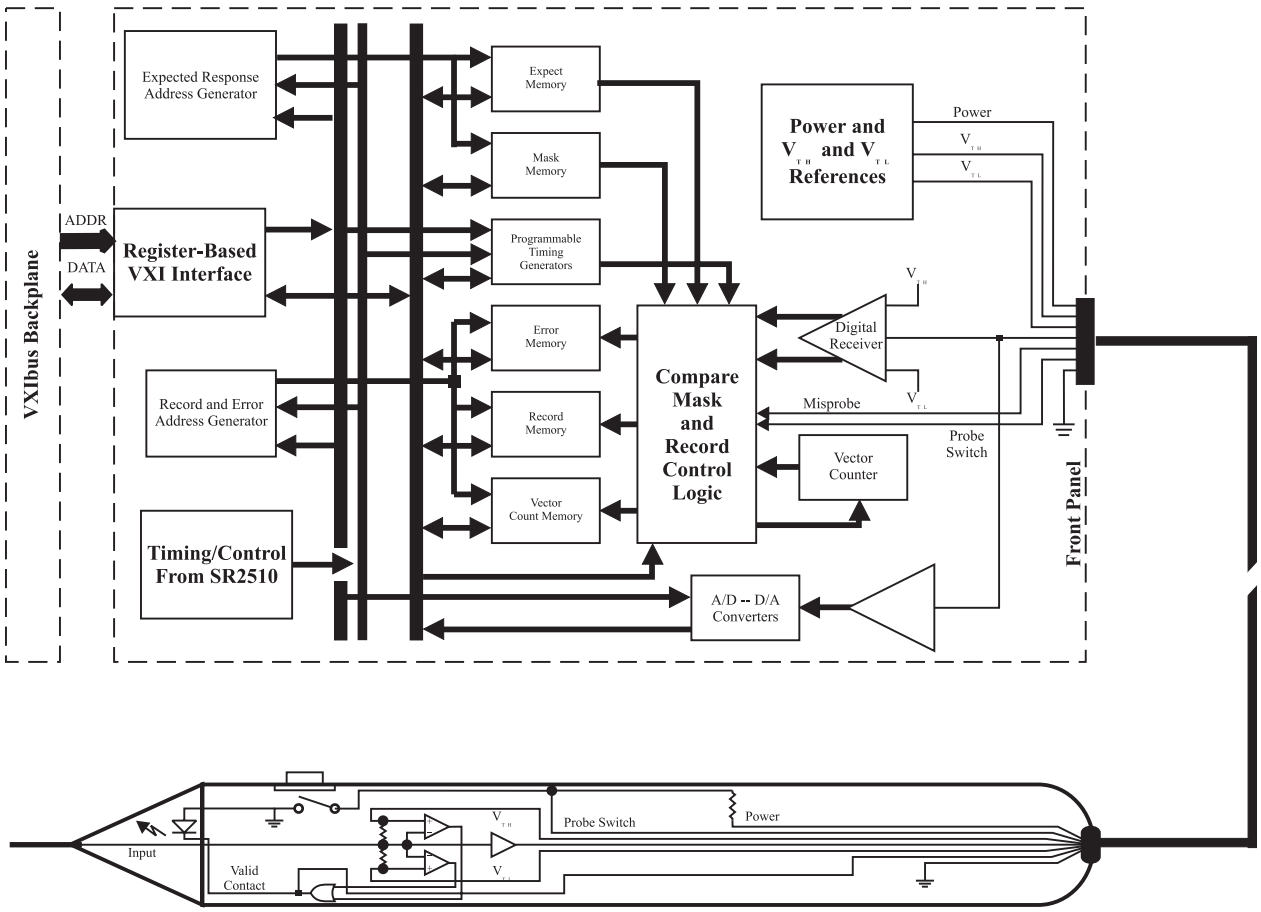
Overvoltage Protection: 40.0 volts

Environmental Specifications:

Temperature	Storage = -40°C to +75°C Operating = 0°C to +45°C
Humidity	5% to 95% relative, noncondensing

* Specifications subject to change without notice.

Note: The above specifications are for the guided probe only and do not include the specifications for the SR2520 itself.



Block Diagram, Guided Probe Electronics.

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