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V/Ethernet 4207 Eagle

User's Guide

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V/Ethernet 4207 Eagle

Ethernet 32-bit
Protocol Platform
User's Guide

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TABLE OF CONTENTS

CHAPTER 1
INTRODUCTION 1-1
SCOPE 1-1
HOW TO USE THIS MANUAL 1-1
RELATED PUBLICATIONS AND STANDARDS 1-2
PRODUCT OVERVIEW 1-3
FULL LINK-LEVEL SUPPORT, PLUS THE ABILITY TO EXECUTE ISO/OSI LEVELS 3 & 4 1-4
EXTENSIVE MULTITASKING CAPABILITIES 1-4
ARCHITECTURE DESIGNED TO MAXIMIZE BANDWIDTH 1-4
LANCE PIPELINE REGISTERS 1-4
TWO 32-BIT-WIDE RAM ARRAYS 1-4
128 KBYTE SCRATCHPAD RAM. 1-5
512 KBYTE COMMUNICATIONS BUFFER 1-5
BUSpacket INTERFACEsm. 1-5
MULTIPLE OPTIONS FOR TRANSFERRING DATA OVER THE VMEbus 1-5
DMA MODE 1-5
SLAVE MODE. 1-5
HARDWARE DRIVERS AND COMMAND-QUEUING SOFTWARE IN . . . . .ONBOARD EPROM 1-5
MACSI SOFTWARE INTERFACE 1-6
FASTER COMMAND RESPONSE AND PARSING TIMES 1-6
SUMMARY OF FEATURES 1-7
ARCHITECTURAL CONSIDERATIONS 1-7
VMEbus CONSIDERATIONS 1-7
ETHERNET CONSIDERATIONS 1-7
CONVENTIONS USED IN THIS MANUAL 1-8
CHANGES FROM PREVIOUS REVISION 1-8

CHAPTER 2
INSTALLATION 2-1
OVERVIEW 2-1
SECTION I
PB-0760-A08-010 EAGLE MOTHERBOARD INSTALLATION 2-4
INSTALLATION PROCEDURE 2-5
STEP 1. VISUAL INSPECTION 2-5
STEP 2. SET ONBOARD JUMPERS 2-5
MOTHERBOARD JUMPER SETTINGS 2-5
STEP 3. SET ONBOARD SWITCHES 2-7
SETTING BASE ADDRESS OF SHORT I/O SPACE RAM 2-7
SETTING BASE ADDESSS OF COMMUNICATIONS BUFFER 2-9
DAUGHTER CARD JUMPER SETTINGS 2-10
SETTING ADDRESS MODIFIERS ALLOWED IN EXTENDED, STANDARD, AND SHORT I/O ADDRESS SPACE 2-11
STEP 4. POWER OFF SYSTEM 2-11
STEP 5. CABLING PROCEDURE 2-11
VMEbus P1 and P2 CONNECTOR CONFIGURATIONS 2-12
INSTALLING THE CABLE AND BOARD 2-12
STEP 6. POWER ON SYSTEM 2-13
SECTION II
PB04207-000,REVD EAGLE MOTHERBOARD INSTALLATION 2-14
INSTALLATION PROCEDURE 2-15
STEP 1. VISUAL INSPECTION 2-15
STEP 2. SET ONBOARD JUMPERS 2-15

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TABLE OF CONTENTS

(continued)

MOTHERBOARD JUMPER SETTINGS 2-15
SETTING BASE ADDRESS OF SHORT I/O SPACE RAM 2-17
SETTING BASE ADDRESS OF COMMUNICATIONS BUFFER 2-19
SETTING ADDRESS MODIFIERS ALLOWED IN EXTENDED, STANDARD, AND SHORT I/O ADDRESS SPACE 2-19
Daughter Card Jumper Settings 2-20
STEP 4. POWER OFF SYSTEM 2-21
STEP 5. CABLING PROCEDURE 2-21
VMEbus P1 AND P2 CONNECTOR CONFIGURATIONS 2-22
INSTALLING THE CABLE AND BOARD 2-22
STEP 6. POWER ON SYSTEM 2-22
SECTION III
PB04207-001,REVA EAGLE MOTHERBOARD INSTALLATION 2-23
INSTALLATION PROCEDURE 2-24
STEP 1. VISUAL INSPECTION 2-24
STEP 2. SET ONBOARD JUMPERS 2-24
MOTHERBOARD JUMPER SETTINGS 2-24
SETTING BASE ADDRESS OF SHORT I/O SPACE RAM 2-26
SETTING BASE ADDRESS OF COMMUNICATIONS BUFFER 2-28
SETTING ADDRESS MODIFIERS ALLOWED IN EXTENDED, STANDARD, AND SHORT I/O ADDRESS SPACE 2-28
Daughter Card Jumper Settings 2-29
STEP 4. POWER OFF SYSTEM 2-30
STEP 5. CABLING PROCEDURE 2-30
VMEbus P1 AND P2 CONNECTOR CONFIGURATIONS 2-31
INSTALLING THE CABLE AND BOARD 2-31
STEP 6. POWER ON SYSTEM 2-31

CHAPTER 3
FUNCTIONAL OVERVIEW 3-1
OVERVIEW OF SYSTEM INTERFACE 3-1
MACSI ORGANIZATION 3-2
EAGLE COMMAND SET 3-2
MASTER COMMAND ENTRY AND COMMAND QUEUE 3-3
ISSUING A COMMAND VIA THE MASTER COMMAND ENTRY 3-3
ISSUING COMMANDS THROUGH THE COMMAND QUEUE 3-4
HANDLING DATA TRANSMISSIONS 3-5
512K COMMUNICATIONS BUFFER 3-5
TRANSMITTING AND RECEIVING DATA OVER THE NETWORK 3-6
GATHER OPERATIONS 3-6
VMEbus INTERRUPTS 3-9
PERFORMING DIAGNOSTICS 3-9
TYPES OF AVAILABLE DIAGNOSTICS 3-9
POWER UP DIAGNOSTICS 3-9
EPROM TEST 3-10
ONBOARD CONTROL REGISTER TEST 3-11
QUICK STATIC RAM TEST 3-11
QUICK BUFFER RAM TEST 3-11
TIMER TEST 3-11
Quick Buffer RAM FIFO Test 3-11
Quick Event RAM Test 3-11
QUICK EVENT RAM FIFO TEST 3-11
LANCE INTERNAL CONTROL/STATUS REGISTER TEST 3-12
LANCE INTERRUPT TEST 3-12

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TABLE OF CONTENTS

(continued)

LANCE LOOPBACK TEST 3-12

CHAPTER 4
MACSI SYSTEM INTERFACE 4-1
OVERVIEW 4-1
MASTER CONTROL/STATUS BLOCK 4-1
MASTER STATUS REGISTER 4-2
MASTER CONTROL REGISTER 4-3
QUEUE HEAD POINTER 4-4
MASTER COMMAND ENTRY 4-4
MASTER COMMAND ENTRY CONTROL REGISTER 4-4
IOPB ADDRESS 4-5
COMMAND TAG 4-5
COMMAND QUEUE 4-5
COMMAND QUEUE ENTRY 4-5
QUEUE ENTRY CONTROL REGISTER. 4-5
IOPB ADDRESS 4-6
COMMAND TAG 4-6
WORK QUEUE NUMBER 4-6
HOST USABLE SPACE 4-6
COMMAND RESPONSE BLOCK / RETURNED IOPB 4-7
COMMAND RESPONSE STATUS WORD. 4-8
COMMAND TAG 4-9
WORK QUEUE NUMBER 4-10
IOPB LENGTH 4-10
RETURNED IOPB 4-10
CONFIGURATION STATUS BLOCK 4-12
CONTROLLER STATISTICS BLOCK 4-13

CHAPTER 5
EAGLE COMMAND SET 5-1
OVERVIEW 5-1
PERFORM EXTENDED DIAGNOSTICS (0x40) 5-2
HOST-PROVIDED IOPB FIELDS 5-3
EAGLE-PROVIDED IOPB FIELDS 5-8
EXTENDED DIAGNOSTICS TEST DESCRIPTIONS 5-9
Static RAM Address Test (Test Code # 0x01) 5-9
Static RAM Extended Test (Test Code # 0x02) 5-10
Buffer RAM Address Test (Test Code # 0x03) 5-11
Buffer RAM Extended Test (Test Code # 0x04) 5-12
Extended Buffer RAM FIFO Test (Test Code # 0x05) 5-13
Buffer RAM Refresh Test (Test Code # 0x06). 5-14
Event RAM Address Test (Test Code # 0x07) 5-14
Event RAM Extended Test (Test Code # 0x08) 5-15
Event RAM Extended FIFO Test (Test Code # 0x09) 5-16
Event RAM Refresh Test (Test Code # 0x0A) 5-16
NOVRAM Test (Test Code # 0x0B) 5-17
Extended LANCE Loopback Test (Test Code # 0x0C) 5-18
VMEbus Master Test (Test Code # 0x0D) 5-23
VMEbus Slave Test (Test Code # 0x0E) 5-24
VMEbus ACFAIL* Test (Test Code # 0x10). 5-25
INITIALIZE CONTROLLER (0x41) 5-27
HOST-PROVIDED IOPB FIELDS 5-27
CONTROLLER INITIALIZATION BLOCK FORMAT 5-31

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TABLE OF CONTENTS

(continued)

EAGLE-PROVIDED IOPB FIELDS 5-34
CHANGE DEFAULT NODE ADDRESS (0x45) 5-35
HOST-PROVIDED IOPB FIELDS 5-35
EAGLE-PROVIDED IOPB FIELDS 5-38
CHANGE LOGICAL ADDRESS FILTER (0x46) 5-39
HOST-PROVIDED IOPB FIELDS 5-39
EAGLE-PROVIDED IOPB FIELDS 5-42
DOWNLOAD SOFTWARE (0x47) 5-43
HOST-PROVIDED IOPB FIELDS 5-44
EAGLE-PROVIDED IOPB FIELDS 5-47
EXECUTE DOWNLOADED SOFTWARE (0x48) 5-48
HOST-PROVIDED IOPB FIELDS 5-48
EAGLE-PROVIDED IOPB FIELDS 5-50
TRANSMIT (0x50) 5-51
HOST-PROVIDED IOPB FIELDS 5-51
EAGLE-PROVIDED IOPB FIELDS 5-55
RECEIVE (0x60) 5-56
HOST-PROVIDED IOPB FIELDS 5-57
EAGLE-PROVIDED IOPB FIELDS 5-60
DMA (0x70) 5-62
HOST-PROVIDED IOPB FIELDS 5-62
EAGLE-PROVIDED IOPB FIELDS 5-65
INITIALIZE MULTIPLE COMPLETIONS PER INTERRUPT (0x80) 5-66
HOST-PROVIDED IOPB FIELDS 5-67
EAGLE-PROVIDED IOPB FIELDS 5-70

CHAPTER 6
SPECIFICATIONS 6-1
ENVIRONMENTAL SPECIFICATIONS 6-1
ELECTRICAL SPECIFICATIONS 6-1
PHYSICAL SPECIFICATIONS 6-1
LED 6-1

CHAPTER 7
ERROR CODES 7-1
CONTROLLER ERRORS: 7-1
QUEUE AND COMMAND ERRORS: 7-1
ERRORS ASSOCIATED WITH MULTIPLE COMPLETIONS PER INTERRUPT IOPB: 7-1
GENERAL ERRORS: 7-1
VMEbus ERRORS: 7-1
ETHERNET ERRORS: 7-2
MISCELLANEOUS ERRORS: 7-2
POWER UP DIAGNOSTICS — PROCESSOR ERRORS: 7-2
POWER UP DIAGNOSTICS — EPROM ERRORS: 7-3
POWER UP DIAGNOSTICS — RAM SIZE ERRORS: 7-3
POWER UP DIAGNOSTICS — RAM ADDRESS ERRORS: 7-3
POWER UP DIAGNOSTICS — RAM VALUE ERRORS: 7-3
POWER UP DIAGNOSTICS — LANCE CSR ERRORS: 7-4
EXTENDED DIAGNOSTICS — MISCELLANEOUS ERRORS: 7-4
EXTENDED DIAGNOSTICS — MEMORY ERRORS: 7-4
EXTENDED DIAGNOSTICS — RAM ERRORS: 7-5
EXTENDED DIAGNOSTICS — EVENT RAM ERRORS: 7-5
EXTENDED DIAGNOSTICS — DMA-RELATED ERRORS: 7-5
EXTENDED DIAGNOSTICS — LANCE LOOPBACK ERRORS: 7-5

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

TABLE OF CONTENTS

(continued)

APPENDIX A
MACSI ACRONYMS **A-1**

APPENDIX B
MULTICAST PROGRAM **B-1**

APPENDIX C
VMEbus TIMING CONSIDERATIONS **C-1**
THE FIFO BUFFER C-1
THE ASYNCHRONOUS STATE MACHINE C-1
BUS PROPAGATION DELAY C-3
OPTIMIZING DMA OPERATIONS C-3

APPENDIX D
COMMUNICATIONS BUFFER BASE ADDRESS SWITCH SETTINGS **D-1**

APPENDIX E
COMMUNICATIONS BUFFER BASE ADDRESS JUMPER SETTINGS **E-1**

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TABLE OF CONTENTS

(continued)

Artisan Technology Group - Quality Instrumentation ... Guaranteed | (888) 88-SOURCE | www.artisantg.com

LIST OF FIGURES

Figure 1-1. V/Ethernet 4207 Block Diagram 1-3
Figure 2-1. V/Ethernet 4207 Eagle Board Layout (PB-0760-A08-010) 2-4
Figure 2-2. VMEbus Request Priority Jumper Settings 2-6
Figure 2-3. Eagle Daughter Card 2-10
Figure 2-4. Eagle P2/DB15 Adapter 2-12
Figure 2-5. V/Ethernet 4207 Eagle Board Layout 2-14
Figure 2-6. VMEbus Request Priority Jumper Settings 2-16
Figure 2-7. Eagle Daughter Card 2-20
Figure 2-8. Eagle P2/DB15 Adapter 2-21
Figure 2-9. V/Ethernet 4207 Eagle Board Layout 2-23
Figure 2-10. VMEbus Request Priority Jumper Settings 2-25
Figure 2-11. Eagle Daughter Card 2-29
Figure 2-12. Eagle P2/DB15 Adapter 2-30
Figure 3-1. Communications Buffer Memory Map 3-5
Figure 3-2. VMEbus Transfer Options for Gather 3-8
Figure 4-11. Master Status Register 4-2
Figure 4-12. Master Control Register 4-3
Figure 4-13. Master Command Entry Control Register 4-4
Figure 4-14. Queue Entry Control Register 4-5
Figure 4-15. Command Response Status Word 4-8
Figure 5-12. Perform Extended Diagnostics Command Options 5-3
Figure 5-13. Perform Extended Diagnostics Normal Completion Fields 5-4
Figure 5-14. VMEbus Transfer Options for Perform Extended Diagnostics Command 5-5
Figure 5-15. Maximum Transfer Size/Memory Size Fields For Extended LANCE Loopback Test 5-19
Figure 5-16. Upper Word Of Failing Address Field For Extended LANCE Loopback Test 5-21
Figure 5-17. Lower Word of Failing Address Field for Extended Diagnostics
LANCE Interrupt Handler Flags 5-21
Figure 5-18. Field 1 (LANCE RMD1 & RMD3 Registers) 5-22
Figure 5-19. Field 2 (LANCE TMD1 & TMD3 Registers) 5-22
Figure 5-20. LANCE Control Status Register 0 (CSR0) 5-23
Figure 5-21. Initialize Controller Command Options 5-28
Figure 5-22. Initialize Controller Normal Completion Fields 5-30
Figure 5-23. Initialize Controller Network Options 5-32
Figure 5-24. Initialize Controller Command Completion Fields 5-33
Figure 5-25. Change Default Node Address Command Options 5-36
Figure 5-26. Change Default Node Address Normal Completion Fields 5-37
Figure 5-27. Change Logical Address Filter Command Options 5-40
Figure 5-28. Change Logical Address Filter Normal Completion Fields 5-41
Figure 5-29. Download Software Command Options 5-44
Figure 5-30. Download Software Normal Completion Fields 5-45
Figure 5-31. VMEbus Transfer Options for Download Software Command 5-46
Figure 5-32. Execute Downloaded Software Command Option 5-49
Figure 5-33. Download Software Normal Completion Fields 5-49
Figure 5-34. Transmit Command Options 5-52
Figure 5-35. Transmit Command Normal Completion Fields 5-53
Figure 5-36. Transmit Command VMEbus Transfer Options 5-54
Figure 5-37. Receive Command Options 5-57
Figure 5-38. Receive Command Normal Completion Fields 5-58
Figure 5-39. Receive Command VMEbus Transfer Options 5-59
Figure 5-40. DMA Command Options 5-63
Figure 5-41. DMA Command Normal Completion Fields 5-63
Figure 5-42. VMEbus Transfer Options for DMA Command 5-64
Figure 5-43. Command Options For Initialize Multiple Completions Per Interrupt 5-68
Figure 5-44. Normal Completion Fields For Initialize Multiple Completions Per Interrupt 5-68
Figure 5-45. Control Flags For Multiple Completions Per Interrupt IOPB 5-69
Figure C-46. Eagle-to-VMEbus Hardware Interface C-1
Figure C-47. VMEbus Timing Diagram C-2

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LIST OF TABLES

Table 2-2. Switches Used for Short I/O Base Address 2-7
Table 2-3. Base Address for Short I/O RAM. 2-8
Table 2-4. Switches Used for Communications Buffer Base Address 2-9
Table 2-5. Jumpers Used for Short I/O Base Address 2-17
Table 2-6. Base Addresses for Short I/O RAM. 2-18
Table 2-7. Jumpers Used For Communications Buffer Base Address. 2-19
Table 2-8. Jumpers Used for Short I/O Base Address 2-26
Table 2-9. Base Addresses for Short I/O RAM. 2-27
Table 2-10. Jumpers Used For Communications Buffer Base Address. 2-28
Table 3-13. Format Of A Gather List Entry. 3-7
Table 4-3. Location Of MACSI Functional Blocks In Short I/O Space 4-1
Table 4-4. Master Control/Status Block. 4-2
Table 4-5. Master Command Entry 4-4
Table 4-6. Command Queue Entry 4-5
Table 4-7. Work Queue Numbers for Use in Command Queue Entries. 4-6
Table 4-8. Command Response Block/Returned IOPB 4-7
Table 4-9. List Format of Multiple IOPB Completions. 4-11
Table 4-10. Configuration Status Block. 4-12
Table 4-11. Controller Statistics Block 4-14
Table 5-16. Eagle Command Set 5-1
Table 5-17. Perform Extended Diagnostics IOPB 5-2
Table 5-18. Test-Specific IOPB Fields for Static RAM Address Test 5-9
Table 5-19. Test-Specific IOPB Fields for Static RAM Extended Test 5-11
Table 5-20. Test-Specific IOPB Fields for Buffer RAM Address Test. 5-12
Table 5-21. Test-Specific IOPB Fields for Buffer RAM Extended Test. 5-13
Table 5-22. Test-Specific IOPB Fields for Extended Buffer RAM FIFO Test 5-13
Table 5-23. Test-Specific IOPB Fields for Buffer RAM Refresh Test 5-14
Table 5-24. Test-Specific IOPB Fields for Event RAM Address Test 5-15
Table 5-25. Test-Specific IOPB Fields for Event RAM Extended Test 5-15
Table 5-26. Test-Specific IOPB Fields for Event RAM Extended FIFO Test 5-16
Table 5-27. Test-Specific IOPB Fields for Event RAM Refresh Test. 5-17
Table 5-28. Test-Specific IOPB Fields for NOVRAM Test 5-17
Table 5-29. Test-Specific IOPB Fields for Extended LANCE Loopback Test 5-18
Table 5-30. Test-Specific IOPB Fields for VMEbus Master Test 5-24
Table 5-31. Test-Specific IOPB Fields for VMEbus Slave Test 5-25
Table 5-32. Test-Specific IOPB Fields for VMEbus ACFAIL Test 5-26
Table 5-33. Initialize Controller IOPB. 5-27
Table 5-34. Controller Initialization Block 5-31
Table 5-35. Change Default Node Address IOPB 5-35
Table 5-36. Change Default Logical Address Filter IOPB 5-39
Table 5-37. Download Software IOPB 5-43
Table 5-38. Execute Downloaded Software IOPB. 5-48
Table 5-39. Transmit IOPB 5-51
Table 5-40. Receive IOPB 5-56
Table 5-41. DMA IOPB 5-62
Table 5-42. Multiple Completions per Interrupt IOPB 5-67
Table 6-43. Transceiver Connector Signal Description (Front Panel I/O Only) 6-2
Table 6-44. P1 Connector Signal Descriptions (P2 - B Row Only and P2 - Full versions). 6-3
Table 6-45. VMEbus P2 Connector Signal Descriptions (P2 - B Row Only version only) 6-4
Table 6-46. VMEbus P2 Connector Signal Descriptions (P2 - Full version only) 6-5

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CHAPTER 1
INTRODUCTION

SCOPE

Interphase wrote this manual for its customers. It is primarily intended for users who need to write their own software drivers. It can also be used by individuals who need to install and/or test the V/Ethernet 4207 Eagle.

Readers are assumed to have a good technical background in the following areas.

- In-depth understanding of:
  - The target system (CPU, memory, VMEbus, peripherals)
  - The Eagle's system interface (as documented in this manual)
  - The AMD LANCE/SIA chipset
  - The higher-level protocol layers to be implemented on the target system

- General knowledge of:
  - Ethernet (IEEE 802.3) specifications
  - The 68020 microprocessor

This manual's organization allows you to focus on your specific areas of interest, without giving you more information than needed. Specifically, it contains guidelines on: 1) installing the 4207, and 2) issuing commands to and obtaining responses from the board

HOW TO USE THIS MANUAL

You will find it very useful to read this chapter completely. It contains information that will clarify many of your questions later. The conventions section can be especially useful for later reference since it defines how certain topics will be presented to you.

Be sure to perform the installation of the product using Chapter 2. Read this chapter thoroughly **before** attempting the installation.

Chapter 3 provides an overview of how the 4207's system interface works. It gives procedures for submitting commands, as well as specific facts about the VMEbus that affect board operation.

A detailed description of the 4207's system interface and command set appears in Chapters 4 and 5.

Chapters 6 and 7 contain information on the 4207's specifications, connector pinouts, and error messages.

The four appendices at the end of the manual provide miscellaneous information about the 4207. This includes such topics as a sample multicasting program and VMEbus timing information.

Interphase can supply you with an example driver. If your system only requires minor modifications of this driver, then the source code provided gives a good base from which to start. If your system is radically different, then the example driver at least gives you ideas on what must functionally be contained in a driver.

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RELATED PUBLICATIONS AND STANDARDS

The following publications are sources of information when working with this product.

- *The Ethernet Data Link Layer and Physical Layer Specifications*
  Version 1.0
  September 30, 1980

- *Am7990 Local Area Network Controller for Ethernet (LANCE)*
  Advanced Micro Devices
  Publication # 05698, Rev. B (October 1986)
  Specifications for AMD's LANCE chip.

- *The VMEbus Specification*
  Micrology pbt.
  Printex Publishing Inc.
  Revision C.1 (October 1985)
  VMEbus specifications and terminology.

- *Writing a Unix® Device Driver*
  Janet I. Egan/Thomas J Teixeira
  John Wiley and Sons, Inc.
  1988
  (This document is an excellent introduction to writing Unix drivers. It shows how Unix performs input/output, how device drivers relate to the hardware I/O architecture, and how to incorporate the device driver into the Unix kernel. It also contains an excellent discussion of how to test drivers.)

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PRODUCT OVERVIEW

The Eagle V/4207 Ethernet 32-bit protocol platform is a state-of-the-art VMEbus LAN controller for Ethernet and other IEEE 802.3-compatible networks. Featuring an advanced set of tools in hardware and firmware - including a 68020 microprocessor running at 16 MHz and a powerful command-queuing system interface - the Eagle is designed to minimize or eliminate the LAN bottleneck in demanding, high-traffic environments. Typical applications for the Eagle include acting as the Ethernet port in file servers or superminicomputers.

Figure 1-1 is a block diagram of the V/Ethernet 4207. Later sections contain descriptions of the various points of interest.

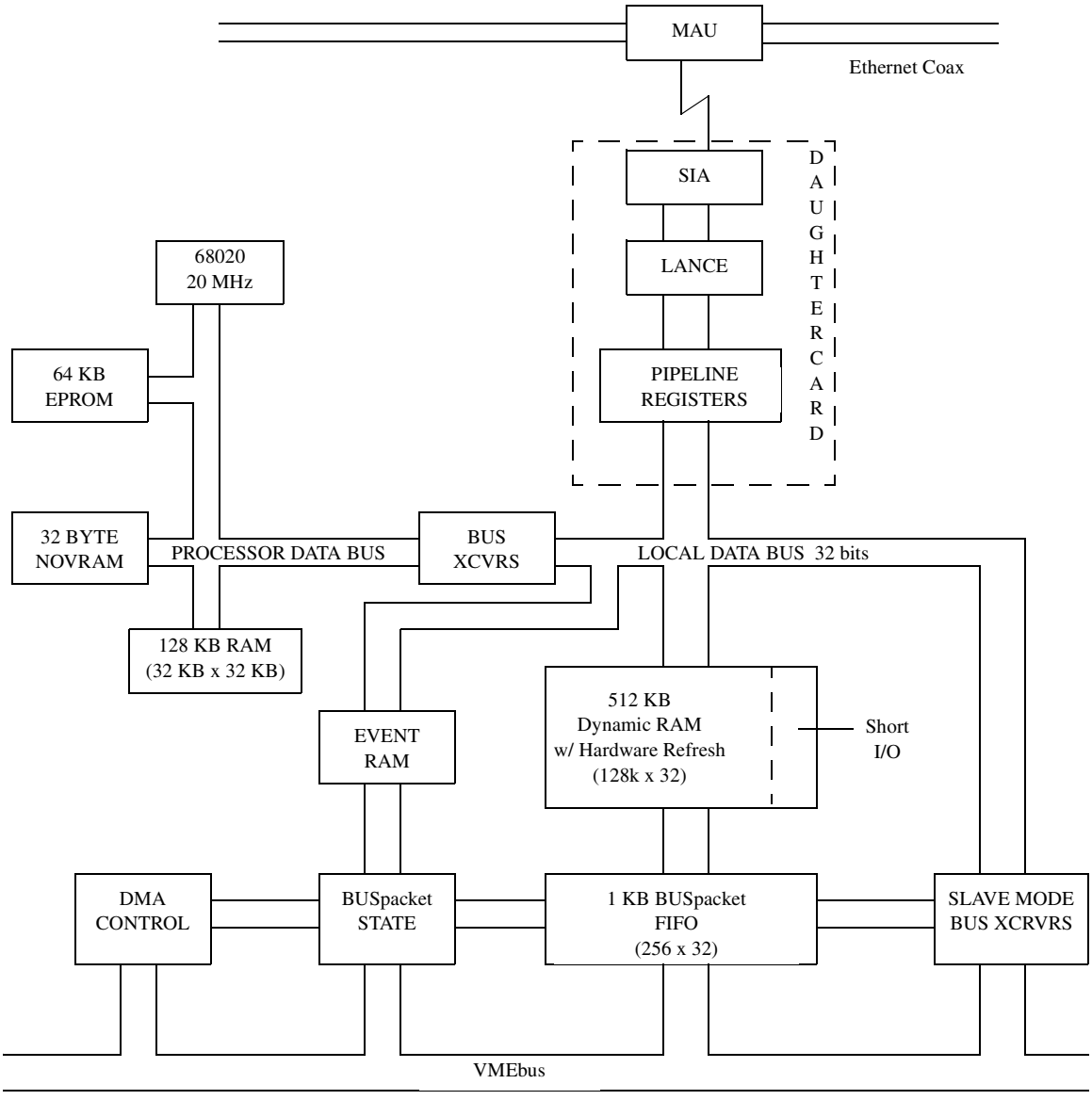


Figure 1-1. V/Ethernet 4207 Block Diagram

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FULL LINK-LEVEL SUPPORT, PLUS THE ABILITY TO EXECUTE ISO/OSI LEVELS 3 & 4

The Eagle uses the AMD Am7990 LANCE and Am7992 SIA chipset to implement Ethernet protocol levels 1 and 2 on board. In addition, it has the onboard intelligence, memory, and other capabilities needed to download and execute layers 3 and 4 of the 7-layer ISO/OSI model of data communications.

EXTENSIVE MULTITASKING CAPABILITIES

The Eagle is designed using a cleanly partitioned architecture that allows the 68020, LANCE, and VMEbus interface to function simultaneously and independently. For example, it can perform all of the following duties at the same time:

- Transmit or receive an Ethernet packet.
- Transfer data to or from system memory over the VMEbus.
- Execute downloaded and/or firmware-resident programs, such as protocol software.

ARCHITECTURE DESIGNED TO MAXIMIZE BANDWIDTH

The Eagle's architecture features an advanced set of support circuits which enhance Ethernet and VMEbus throughput by significantly reducing contention for resources such as RAM.

In a typical Ethernet system utilizing intelligent node controllers, the node microprocessor is idle whenever the VMEbus downloads data to the communications buffer or the LANCE transmits or receives a packet. In the latter case, the time penalty can run into the microseconds.

The Eagle addresses these and other problems affecting local and VMEbus bandwidth by partitioning the board into functional blocks designed to maximize the speed and intelligence of the 68020. These include:

- LANCE pipeline registers
- Two independent 32-bit-wide RAM arrays
    - 128 Kbyte dedicated RAM for 68020 (512 Kbyte optional)
    - 512 Kbyte communications buffer
- Interphase's proprietary BUSpacket Interface^{sm}

LANCE PIPELINE REGISTERS

As noted previously, the LANCE's inability to relinquish control of the bus while it is transmitting or receiving a packet adversely affects the performance of many Ethernet node controllers.

The Eagle solves this problem by interposing pipeline registers between the LANCE and the local bus. By providing fast interleaved access to the 512 Kbyte communications buffer, these registers prevent the LANCE from monopolizing the communications buffer or local bus. The result is significantly improved controller throughput.

TWO 32-BIT-WIDE RAM ARRAYS

The Eagle has two independent 32-bit-wide RAM arrays — a 128 Kbyte RAM (512 Kbyte optional) for the exclusive use of the 68020 and a half-megabyte, quad-ported communications buffer.

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128 KBYTE SCRATCHPAD RAM

Used exclusively by the 68020 as an execution RAM and scratchpad, the 32-bit-wide 128 Kbyte RAM (optionally 512K) supports zero wait state operation. Unlike other node controllers that force the host, microprocessor, and LANCE to contend for the same RAM, this dedicated RAM enables the 68020 to continue executing instructions, even when access to the communications buffer RAM is limited because of network or VMEbus activity.

512 KBYTE COMMUNICATIONS BUFFER

The quad-ported communications buffer serves to decouple network activity from both VMEbus activity and the execution of protocol firmware. It can be accessed by:

- the Ethernet interface
- the 68020
- the VMEbus Master interface

— and —

- the VMEbus Slave interface

BUSpacket INTERFACE^{sm}

Implemented using a bipolar, delay-line-timed state machine and a 1 Kbyte FIFO, the BUSpacket Interface supports DMA transfers to/from the Eagle at rates in excess of 30 Mbytes/s. The Eagle thus conserves VMEbus bandwidth for use by other system processes.

MULTIPLE OPTIONS FOR TRANSFERRING DATA OVER THE VMEbus

Unlike most Ethernet node controllers, which only support one method for transferring data over the VMEbus, the Eagle supports two modes: DMA and slave modes. These multiple data-transfer options provide added flexibility for optimizing the movement of data in your specific application.

DMA MODE

In DMA mode, the Eagle operates as a VMEbus master. Under command from the host, it can move data over the VMEbus to and from its onboard 512 Kbyte communications buffer at rates in excess of 30 Mbytes/s. The Eagle can also optimize the performance of the system's memory management scheme by performing a gather function during these DMA transfers.

SLAVE MODE

In slave mode, the 512 Kbyte communications buffer is accessible over the VMEbus by any VMEbus Master. In this mode it appears as fast (300 nsec) system memory on the VMEbus.

HARDWARE DRIVERS AND COMMAND-QUEUING SOFTWARE IN ONBOARD EPROM

The Eagle contains a command-queuing system interface, as well as a simple monitor and drivers for all onboard hardware.

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MACSI SOFTWARE INTERFACE

The command-queuing interface, referred to as "MACSI" (for Multiple Active Command Software Interface), provides an advanced system interface that:

- Easily interfaces to UNIX® drivers.
- Executes commands without the need for off-board parameter fetches.
- Minimizes the time that elapses between issuing a command and having the specified action take place on the network.
- Eliminates host/controller contention and timing windows.
- Reduces or eliminates "queue full" time and queue lockup.

MACSI uses the Eagle's 2 Kbyte short I/O space as an interface to the host system. For each command to send or receive data over the network, the host writes the following data to this dual-ported memory:

1) An IOPB describing the transmit or receive command.
2) An entry in a circular queue (called the Command Queue) pointing to the IOPB.

The Eagle services the Command Queue as quickly as possible. Rather than actually executing commands as they are encountered in the queue, it simply stores them away internally — freeing up both the queue location and the IOPB space. This allows a relatively small dual-ported memory to support a large number of concurrent commands.

FASTER COMMAND RESPONSE AND PARSING TIMES

MACSI allows the host to overlap command response and command parsing tasks with network operations. The Eagle's internal Command Queue enables the host to issue a data transmission command while the Eagle is still processing a previously issued command.

This boosts system performance in several respects. First, it allows the command response time of data transmission commands to be overlapped with actual network activity. It also decouples activities taking place between the 68020, LANCE, and BUSpacket Interface, allowing the 68020 to parse commands before they are actually needed by the LANCE or BUSpacket Interface. In addition, it reduces timing window contention by keeping interrupt response times and context switch times out of the critical path of the Eagle's operation.

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SUMMARY OF FEATURES

ARCHITECTURAL CONSIDERATIONS

- Onboard 68020 microprocessor operating at 16 MHz
- 128 Kbytes of zero wait state scratchpad RAM (512 Kbytes optional)
- 512 Kbytes quad-ported communication data buffer RAM
- 64 Kbyte EPROM program storage
- 32 Bytes nonvolatile RAM for Ethernet node address
- Proprietary real-time executive and a command queuing software interface in EPROM
- Drivers for onboard hardware resident in EPROM
- BUSpacket Interface to the VMEbus with DMA data rates of over 30 Mbytes/s
- Gather capability

VMEbus CONSIDERATIONS

- Double-high VMEbus card
- VMEbus bus master data transfers:
  - 8-, 16-, and 32-bit data transfers
  - 24- and 32-bit data addressing
- Supports block mode transfers
- Software-programmable address modifiers
- Programmable burst rates from 1 to 256 transfers per burst
- VMEbus slave data transfers:
  - 8-, 16-, 32-bit data transfers
  - 24- and 32-bit data addressing
  - Address modifiers: supervisor or non-privileged, standard or extended data access (0x3D, 0x39, 0x0D, 0x09)
- Control/status transfers:
  - 16- and 32-bit data transfers
  - 16-bit data addressing
  - Address modifiers: supervisor or non-privileged short access (0x29, 0x2D)
- Software programmable interrupt levels (1-7)
- Software programmable interrupt vectors

ETHERNET CONSIDERATIONS

- Compatible with Ethernet (Rev 1.0 or 2.0) and IEEE 802.3 (Rev. D).
- Unique node address assigned by IEEE.
- Onboard Ethernet/Cheapernet transceiver cable interface.

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CONVENTIONS USED IN THIS MANUAL

When numeric values are used, number base 10 may be assumed unless preceded by the characters "0x" for base 16 (hexadecimal).

Binary numbers are represented as "1" and "0".

Signal names followed by the character "*" are active low (i.e., 0 = true, 1 = false).

In tables which show the format of specific IOPBs, boldface type is used to indicate returned values (i.e. status information which is written by the Eagle to the IOPB at the completion of a command). Values which are not printed in boldface are host-specified values.

When individual bits are discussed in the text, "set" means "1" and "clear" means "0".

CHANGES FROM PREVIOUS REVISION

This section applies to users who are familiar with the previous revision of this manual (UG04207-000,REVD). The following list summarizes how the current revision of the manual differs from the previous one. Each item in the list includes a reference to page(s) affected by the change.

1. Correct typographical error in the V/Ethernet 4207 Block Diagram (page 1-3).

2. Change references of versions -0 and -1 of the Eagle to P2 - B Row Only and P2 - Full (throughout document).

3. Add section to Chapter 2 - Installation that describes an SMT version of the Eagle (pages 2-23 through 2-32).

4. Orient the figures for the VMEbus Request Priority Jumper Settings to match the orientation of the motherboard figure layout (pages 2-6, 2-16, and 2-25).

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CHAPTER 2
INSTALLATION

OVERVIEW

Before attempting installation, read this chapter thoroughly to insure the safe installation of the Eagle into your system. If you have any questions regarding installation which are not answered in this chapter, please contact Interphase Customer Service.

The Eagle is installed into the VMEbus system using the following five steps:

- Visual Inspection
- Set Onboard Jumpers
- Set Onboard Switches
- Power Off System
- Cabling Procedure
- Power System On

When installing the Eagle, the following **CAUTION** must be adhered to.

CAUTION

1. *Catastrophic DAMAGE can result if improper connections are made. Therefore, those planning to connect power sources to the VMEbus for the purpose of feeding the user-defined 96 pins of P2 (Rows A and C) should FIRST CHECK to ensure that all boards installed are compatible with those connections.*

2. *The Eagle is extremely sensitive to electrostatic discharge (ESD), and the board could be damaged if handled improperly. Interphase ships the board enclosed in a special anti-static bag. Upon receipt of the board, take the proper measures to eliminate board damage due to ESD (i.e., wear a wrist ground strap or other grounding device).*

3. *Do NOT install or apply power to a damaged board. Failure to observe this warning could result in extensive damage to the board and/or system.*

There are two versions of the Eagle that dictate the cabling options that are available:

1. The **P2 - B Row only** is the standard configuration. It allows Ethernet I/O signals to be directed out of the Eagle front panel only. This version has no connections to the VMEbus P2 rows A and C.

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The specific hardware configuration of the **P2 - B Row only** version is as follows:

- Supports front panel Ethernet I/O only.
- VMEbus connector utilizes row B only.
- Transformer must be seated in the T1 position.
- Jumper JA1 on daughter card must be in position 2 ↔ 3.

2. The **P2 - Full** is the nonstandard configuration. It allows Ethernet I/O signals to be directed out of either the front panel or off of the VMEbus P2 connector. The specific hardware configuration is as follows:

   - Supports both front panel and VMEbus P2 Ethernet I/O.
   - VMEbus P2 connector utilizes rows A, B, and C.

   In order to implement front panel I/O:
   — The transformer must be seated in the T1 position (refer to figure 2-3).
   — Jumper JA1 on the daughter card must be in position 2 ↔ 3.

   In order to implement VMEbus P2 I/O:
   — The transformer must be seated in position T2.
   — Jumper JA1 on the daughter card must be in position 1 ↔ 2.

Both versions of the Eagle are shipped with: 1) the transformer seated in the T1 position and 2) jumper JA1 on daughter card in position 2 ↔ 3.

NOTE

If Ethernet version 1.0 support is required, you must replace the T1/T2 transformer with a 16-pin DIP jumper platform. On this jumper platform, connect jumpers between pins 1↔16, 2↔15, 3↔14, 4↔13, 5↔12, 6↔11, 7↔10, and 8↔9. Note that the jumper platform must **not** be physically higher than the transformer it replaces. For front panel I/O, plug the platform into the T1 socket. For P2 I/O, plug it into the T2 socket.

This chapter contains the following 2 sections:

- Section I illustrates and describes the installation of the PB-0760-A08-010 Eagle motherboard.
- Section II illustrates and describes the installation of the PB04207-000, REVD Eagle motherboard.
- Section III illustrates and describes the installation of the PB04207-001, REVA Eagle motherboard.

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NOTE

If your board has switches (S1, S2, S3), you have **PB-0760-A08-010**, refer to Section I for installation procedures.

If your board has all jumpers, you have **PB04207-000 REVD**, refer to Section II for installation procedures.

If your board has all surface mount components, you have **PB4207-001 REVA**, refer to Section III for installation procedures.

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SECTION I
PB-0760-A08-010 EAGLE MOTHERBOARD INSTALLATION

The figure below shows the location of the jumpers, and switches on the Eagle PB-0760-A08-010 Motherboard.

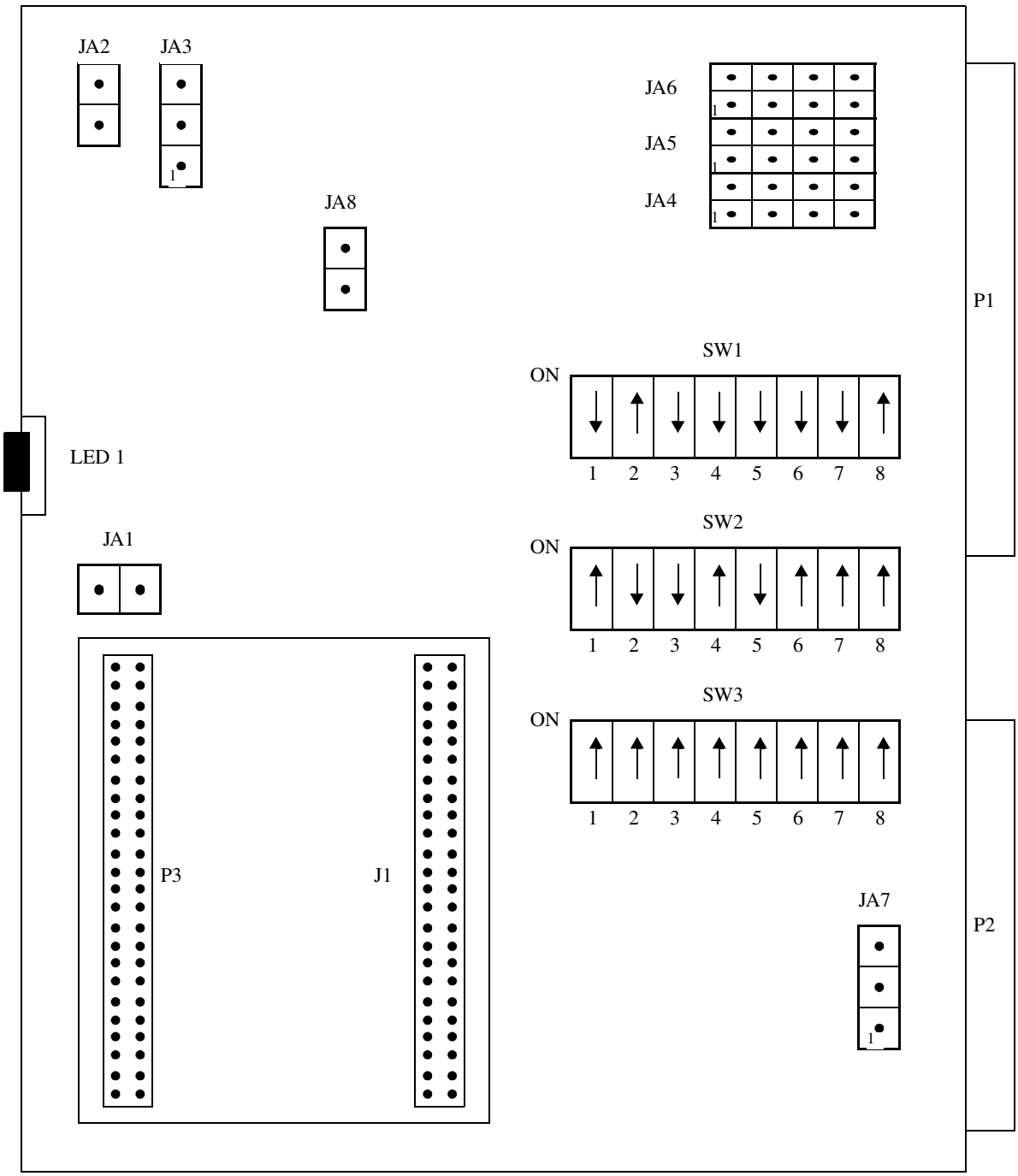


Figure 2-1. V/Ethernet 4207 Eagle Board Layout (PB-0760-A08-010)

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INSTALLATION PROCEDURE

For proper installation, it is imperative that you follow the steps below:

STEP 1. VISUAL INSPECTION

Before attempting the installation of this board, make sure you are wearing an anti-static or grounding device. Remove the Eagle board from the anti-static bag, and visually inspect it to ensure no damage has occurred during shipment. A visual inspection usually is sufficient, since each board is thoroughly checked at Interphase just prior to shipment.

If the board is undamaged and all parts are accounted for, proceed with the installation.

STEP 2. SET ONBOARD JUMPERS

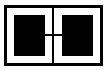
Set all onboard jumpers so that the Eagle is properly configured for operation within your system. The board layout on page 2-5 shows the location of each jumper on the Eagle.

Note that some of the jumper settings must not be altered.

MOTHERBOARD JUMPER SETTINGS

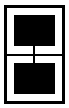
Set all onboard jumpers so that the Eagle is properly configured for operation within your system. The board layout as illustrated in figure 2-1 shows the location of the jumpers.

JA1



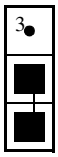
IN = **FACTORY DEFAULT - Do not alter.**
OUT = Deselect clock-to-memory circuitry.

JA2



IN = **FACTORY DEFAULT - Do not alter.**
OUT = Deselect clock-to-CPU circuitry.

JA3



1 ↔ 2 A 27512 EPROM is installed **FACTORY DEFAULT - Do not alter.**

2 ↔ 3 A 27256 EPROM is installed.

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Figure 2-2 shows all possible valid configurations for this jumper block. Sets the Eagle's VMEbus request priority level from 0 (lowest) to 3 (highest).

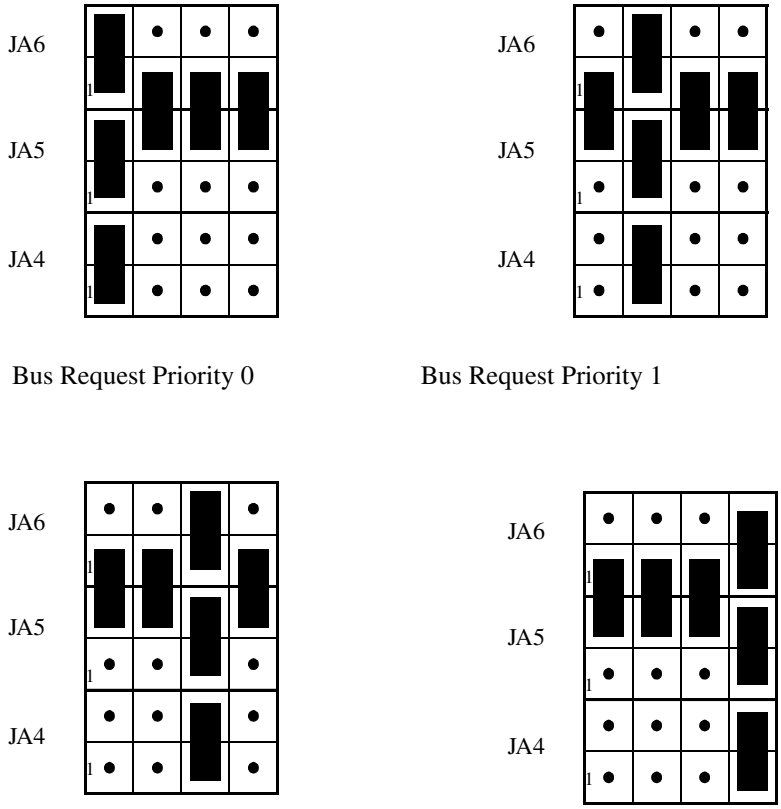
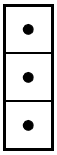


Figure 2-2. VMEbus Request Priority Jumper Settings

The JA7 jumper block determines whether the Eagle will use late or early release VMEbus arbitration when it is the bus master. If the jumper is set for late release, the VMEbus signal BBSY* is released **after** the last cycle is completely finished. If the jumper is set for early release, BBSY* is released **at the start** of the last cycle to allow for rearbitration during the last cycle. The possible settings are as follows:

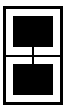
JA7



1 ↔ 2 Early release of BBSY*.
2 ↔ 3 Late release of BBSY*.

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JA8



IN FACTORY DEFAULT - Do Not alter.
OUT Disable tri-state drivers for testing.

STEP 3. SET ONBOARD SWITCHES

The Eagle has three switch blocks (SW1, SW2, and SW3) that contain eight switches (numbered 1-8) each. These switches are used to select:

— the VMEbus base address of the 512 Kbyte communications buffer RAM
— the base address of the 2 Kbyte short I/O RAM
— the allowable address modifiers

Refer to the board layout on page 2-5 to determine the location of the switch blocks.

The following sections describe how to set the switches to meet the requirements of your specific installation.

SETTING BASE ADDRESS OF SHORT I/O SPACE RAM

Switches 1 — 5 of SW1 are used to set the base address of the 2 Kbytes of short I/O space RAM on the Eagle. As discussed in the Functional Overview, all interaction between the host and the 4207 takes place in this 2K space.

The short I/O space is mapped into the uppermost 2K of the Eagle's 512K communications buffer. The switches correspond to VMEbus address lines A15-A11, respectively, as shown in Table 2-1.

Table 2-2. Switches Used for Short I/O Base Address

Switch #	VMEbus Address Bit
SW1 - 1	A15
SW1 - 2	A14
SW1 - 3	A13
SW1 - 4	A12
SW1 - 5	A11

An OFF switch has a value of '1' and an ON switch has a value of '0'. To determine the ON vs. OFF setting of the switches, refer to the board layout on page 2-4.

The following table shows all possible base address settings for the short I/O space.

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Table 2-3. Base Address for Short I/O RAM

Address	Switch Settings ON/OFF				
	1	2	3	4	5
0000	O	O	O	O	O
0800	O	O	O	O	F
1000	O	O	O	F	O
1800	O	O	O	F	F
2000	O	O	F	O	O
2800	O	O	F	O	F
3000	O	O	F	F	O
3800	O	O	F	F	F
4000	O	F	O	O	O
4800	O	F	O	O	F
5000	O	F	O	F	O
5800	O	F	O	F	F
6000	O	F	F	O	O
6800	O	F	F	O	F
7000	O	F	F	F	O
7800	O	F	F	F	F
8000	F	O	O	O	O
8800	F	O	O	O	F
9000	F	O	O	F	O
9800	F	O	O	F	F
A000	F	O	F	O	O
A800	F	O	F	O	F
B000	F	O	F	F	O
B800	F	O	F	F	F
C000	F	F	O	O	O
C800	F	F	O	O	F
D000	F	F	O	F	O
D800	F	F	O	F	F
E000	F	F	F	O	O
E800	F	F	F	O	F
F000	F	F	F	F	O
F800	F	F	F	F	F

NOTE: O = ON, F = OFFBase Addresses for Short I/O RAM

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SETTING BASE ADDESSS OF COMMUNICATIONS BUFFER

Switches 1 - 5 on Switch Block SW2 and switches 1 — 8 on SW3 are used to set the base address of the 512 Kbyte communications buffer, as shown in the following table:

Table 2-4. Switches Used for Communications Buffer Base Address

Switch #	VMEbus Address Bit
SW3-1	A31
SW3-2	A30
SW3-3	A29
SW3-4	A28
SW3-5	A27
SW3-6	A26
SW3-7	A25
SW3-8	A24
SW2-1	A23
SW2-2	A22
SW2-3	A21
SW2-4	A20
SW2-5	A19

An OFF switch has a value of '1' and an ON switch has a value of '0'. To determine the ON vs. OFF setting of the switches, refer to the board layout on page 2-4.

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DAUGHTER CARD JUMPER SETTINGS

COMPONENT SIDE

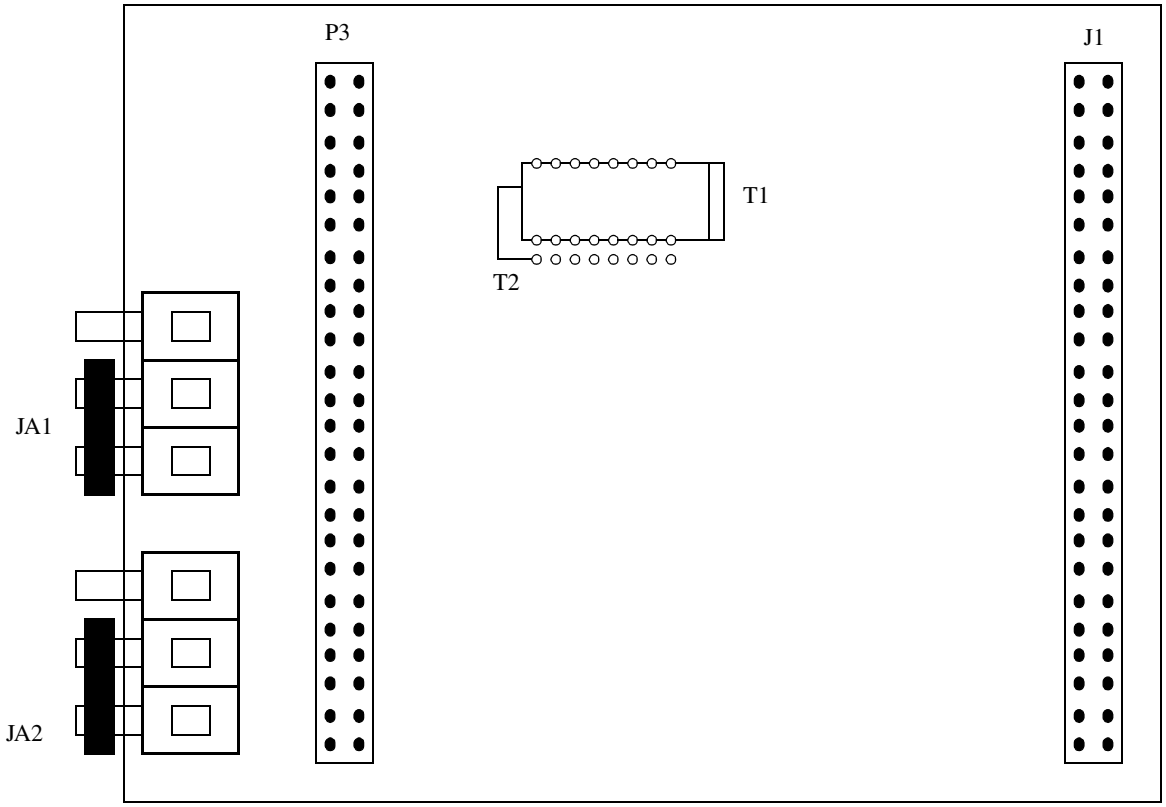


Figure 2-3. Eagle Daughter Card

Jumper Number	**Configuration**	**Use**
JA1	1 ⇔ 2	+12V (fused) transceiver power to AUI connection at VMEbus P2 connector. This setting is only valid for the P2 - Full version of the Eagle.
	2 ⇔ 3	+12V (fused) transceiver power to AUI connection at front panel connector. **These pins must always be jumpered together on the P2 - B Row Only version of the Eagle.**
JA2	1 ⇔ 2	Selects full-step signalling AUI interface (for Ethernet version 1.0 applications).
	2 ⇔ 3	Selects half-step signalling AUI interface (for Ethernet version 2.0 and IEEE 802.3 applications).

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SETTING ADDRESS MODIFIERS ALLOWED IN EXTENDED, STANDARD, AND SHORT I/O ADDRESS SPACE

Switch 7 in SW2 is used to select the address modifiers that are permitted in extended (32-bit) address space. If switch 7 is on, only supervisor accesses are permitted in extended address space (address modifier 0x0D only). If the switch is off, then both 0x0D and 0x09 address modifiers are selected.

Switch 8 in SW2 selects the address modifiers permitted in the standard (24-bit) address space. If the switch is on, only supervisor accesses are permitted in standard address space (address modifier 0x3D only). If it is off, then both 0x3D and 0x39 address modifiers are allowed.

Switch 8 in SW1 selects the address modifiers that are permitted in the short I/O (16-bit) address space. If the switch is on, only supervisor accesses are permitted in short I/O space (address modifier 0x2D only). If it is off, then both 0x2D and 0x29 address modifiers are permitted.

STEP 4. POWER OFF SYSTEM

Once the board is configured, ensure that the system power is OFF.

CAUTION

System power and peripheral power must be turned OFF before attempting to install the Eagle. Failure to do so may result in severe damage to the board and/or system.

STEP 5. CABLING PROCEDURE

As part of the installation procedure, you will need to connect the Eagle to an Ethernet transceiver on the network. Your cabling options are as follows:

If you have the P2 - B Row Only version of the Eagle. The P2 - B Row Only version of the Eagle utilizes row B only on the P2 connector configuration, Ethernet I/O signals can only be directed out of the front panel. To do so, use a standard Ethernet 802.3 transceiver cable. The cable is one-to-one, with DB15 connectors on both ends. An example of such a cable is part number LCN2000 from Black Box Corporation. The cable pinout is shown in Chapter 6 of this User's Guide.

If you have the P2 - Full version of the Eagle. The P2 - Full version of the Eagle uses all three rows (A,B,C) on the P2 connector, you can direct Ethernet I/O signals out of either the front panel or the VMEbus P2 connector.

- For front panel I/O, use a standard Ethernet 802.3 transceiver cable, as described above.
- To route I/O signals off the P2 connector, connect an Interphase Eagle P2/DB15 Adapter (part no. SP-1040-000-XOA) to P2. A standard Ethernet 802.3 transceiver cable can then be used to interconnect the adapter and transceiver.

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The P2/DB15 Adapter is depicted in Figure 2-4.

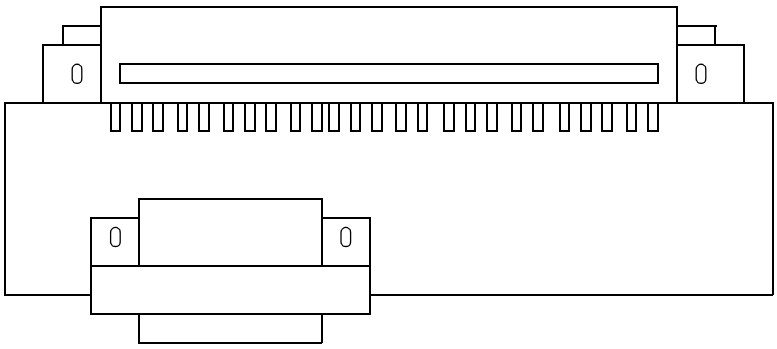


Figure 2-4. Eagle P2/DB15 Adapter

VMEbus P1 and P2 CONNECTOR CONFIGURATIONS

Both versions of the Eagle have the same VME P1 connector configuration.

The P2 - B Row Only version of the Eagle utilizes only row B on the P2 connector.

The P2 - Full version of the Eagle uses all three rows on the P2 connector.

CAUTION

If you have the P2 - Full version of the Eagle, do not plug it into a backplane that busses rows A and C of the P2 connector. Failure to observe this warning can result in extensive damage to the board and/or system.

INSTALLING THE CABLE AND BOARD

1. With the power off, connect the transceiver cable to the transceiver, making sure the pins are properly oriented.

2. If you are using the Interphase P2/DB15 Adapter to interface a standard Ethernet 802.3 transceiver cable to the P2 connector, plug this device onto the P2 connector.

3. Carefully slide the Eagle into the VMEbus card slot. It should slide all the way in without any difficulty. If it doesn't, pull it out and check to make sure that there are no cables in the way.

4. Plug the other end of the transceiver cable into the appropriate connector (front panel or P2/DB15 Adapter).

5. Once the board is properly seated in the slot, tighten the captive mounting screws on each end of the board.

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STEP 6. POWER ON SYSTEM

Once the 4207 is installed in the chassis and properly cabled, turn the host on. **If your other system documentation (host/peripherals) contains precautionary statements about powering up the system, please be sure to follow these precautions.**

After powering up the system, observe the 4207's LED located on the edge of the board opposite the VMEbus connectors. When the LED turns from red to green, the 4207 has successfully completed its power-up diagnostics and is ready to accept commands.

NOTE

> If the LED fails to turn green, turn off the system and ensure that the 4207 is fully inserted into the slot. If the LED still fails to turn green after reapplying power to the system, call Customer Service at Interphase for assistance.

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SECTION II
PB04207-000,REVD EAGLE MOTHERBOARD INSTALLATION

The figure below shows the location of the jumpers on the Eagle PB04207-000,REVD Motherboard.

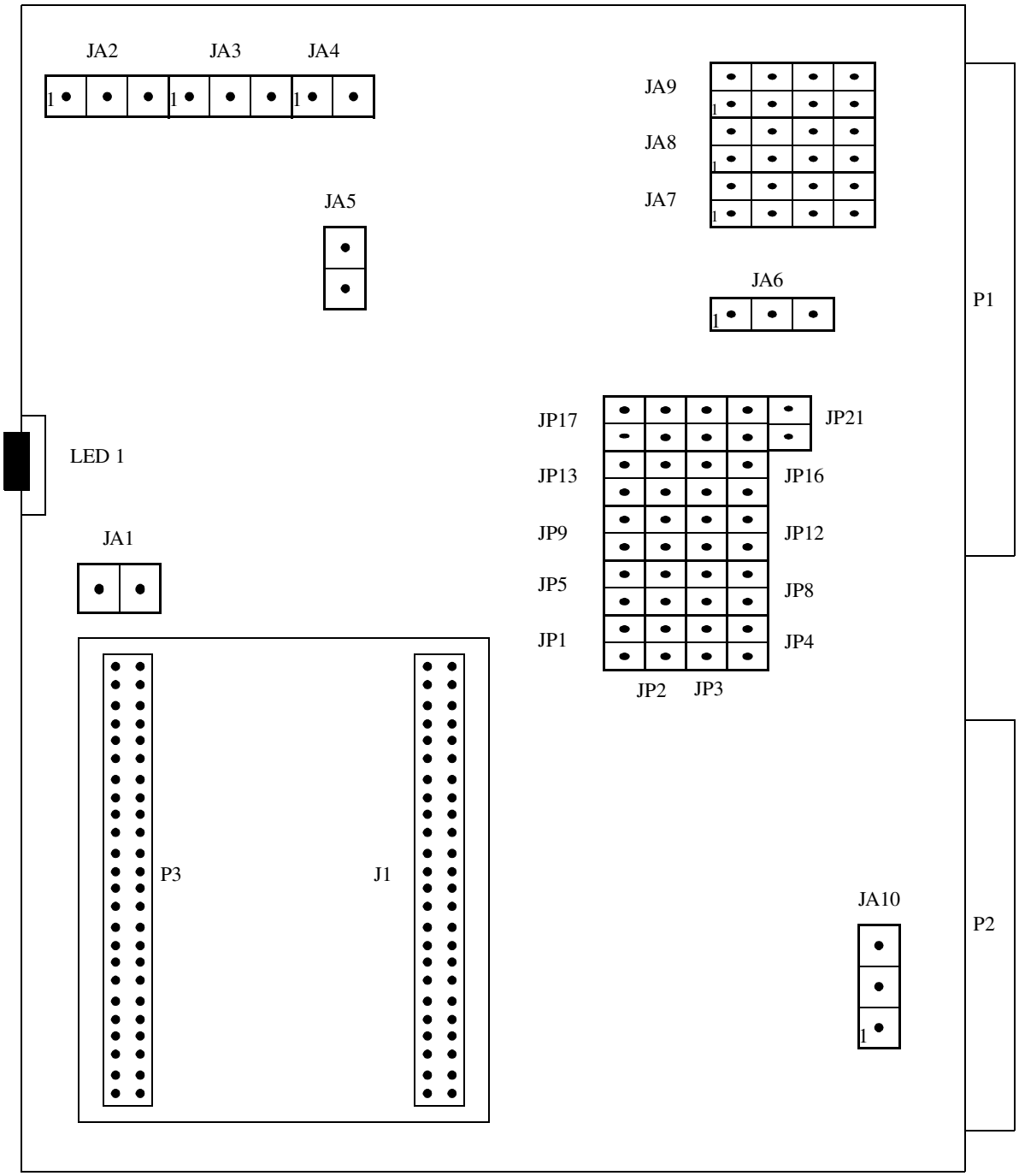


Figure 2-5. V/Ethernet 4207 Eagle Board Layout

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INSTALLATION PROCEDURE

For proper installation, it is imperative that you follow the steps below:

STEP 1. VISUAL INSPECTION

Before attempting the installation of this board, make sure you are wearing an anti-static or grounding device. Remove the Eagle board from the anti-static bag, and visually inspect it to ensure no damage has occurred during shipment. A visual inspection usually is sufficient, since each board is thoroughly checked at Interphase just prior to shipment.

If the board is undamaged and all parts are accounted for, proceed with the installation.

STEP 2. SET ONBOARD JUMPERS

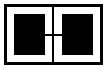
Set all onboard jumpers so that the Eagle is properly configured for operation within your system. The board layout on page 2-14 shows the location of each jumper on the Eagle.

Note that some of the jumper settings must not be altered.

MOTHERBOARD JUMPER SETTINGS

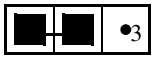
Set all onboard jumpers so that the Eagle is properly configured for operation within your system. The board layout as illustrated in Figure 2-5 shows the location of the jumpers.

JA1



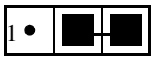
IN = **FACTORY DEFAULT** - **Do not alter.**
OUT = Deselects clock-to-memory circuitry.

JA2



1⇔2 A 27512 EPROM or larger is installed. **FACTORY DEFAULT** - **Do not alter**.
2⇔3 A 27256 EPROM is installed.

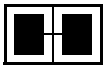
JA3



1⇔2 A EPROM larger than a 27512 is installed.
2⇔3 A 27512 EPROM is installed. **FACTORY DEFAULT** - **Do not alter**.

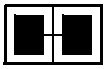
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JA4



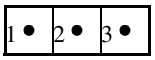
IN = **FACTORY DEFAULT** - **Do not alter.**
OUT = Deselects clock-to-CPU circuitry.

JA5



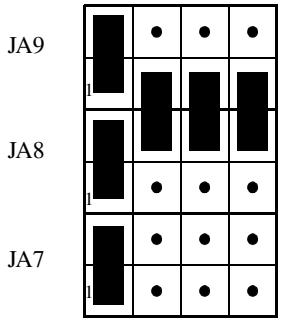
IN = **FACTORY DEFAULT** - **Do not alter.**
OUT = Disables tri-state drivers for testing.

JA6

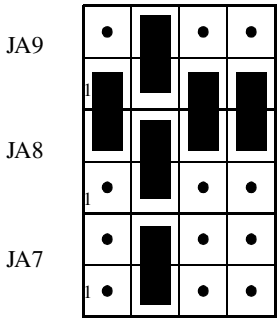


1⇔2 Enables Communications Buffer VME access.
2⇔3 Disables Communications Buffer VME access.

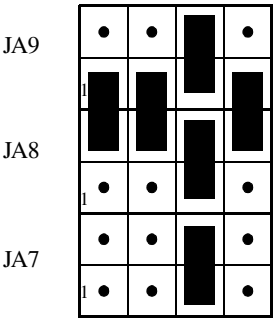
JA7, **JA8**, **JA9** Figure 2-6 shows all possible valid configurations for this jumper block. Set the Eagle's VMEbus request priority level from 0 (lowest) to 3 (highest).



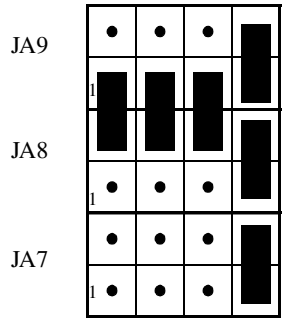
Bus Request Priority 0



Bus Request Priority 1



Bus Request Priority 2



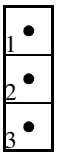
Bus Request Priority 3

Figure 2-6. VMEbus Request Priority Jumper Settings

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The JA10 jumper block determines whether the Eagle will use late or early release VMEbus arbitration when it is the bus master. If the jumper is set for late release, the VMEbus signal BBSY* is released **after** the last cycle is completely finished. If the jumper is set for early release, BBSY* is released **at the start** of the last cycle to allow for rearbitration during the last cycle. The possible settings are as follows:

JA10



1 ⇔ 2 Early release of BBSY*.
2 ⇔ 3 Late release of BBSY*

The Eagle has jumpers JP1-JP21. These jumpers are used to select:

— the VMEbus base address of the 512 Kbyte communications buffer RAM
— the base address of the 2 Kbyte short I/O RAM
— the allowable address modifiers

Refer to the board layout on page 2-14 to determine the location of the jumpers.

The following sections describe how to set the jumpers to meet the requirements of your specific installation.

SETTING BASE ADDRESS OF SHORT I/O SPACE RAM

Jumpers JP17-JP21 are used to set the base address of the 2 Kbytes of short I/O space RAM on the Eagle. As discussed in the Functional Overview, all interaction between the host and the 4207 takes place in this 2K space.

The short I/O space is mapped into the uppermost 2K of the Eagle's 512K communications buffer. The jumpers correspond to VMEbus address lines A15-A11, respectively, as shown in Table 2-4.

Table 2-5. Jumpers Used for Short I/O Base Address

Jumper #	VMEbus Address Bit
JP17	A15
JP18	A14
JP19	A13
JP20	A12
JP21	A11

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The following table shows all possible base address settings for the short I/O space

Table 2-6. Base Addresses for Short I/O RAM.

Address	Jumper ON/OFF				
	JP17	JP18	JP19	JP20	JP21
0000	O	O	O	O	O
0800	O	O	O	O	F
1000	O	O	O	F	O
1800	O	O	O	F	F
2000	O	O	F	O	O
2800	O	O	F	O	F
3000	O	O	F	F	O
3800	O	O	F	F	F
4000	O	F	O	O	O
4800	O	F	O	O	F
5000	O	F	O	F	O
5800	O	F	O	F	F
6000	O	F	F	O	O
6800	O	F	F	O	F
7000	O	F	F	F	O
7800	O	F	F	F	F
8000	F	O	O	O	O
8800	F	O	O	O	F
9000	F	O	O	F	O
9800	F	O	O	F	F
A000	F	O	F	O	O
A800	F	O	F	O	F
B000	F	O	F	F	O
B800	F	O	F	F	F
C000	F	F	O	O	O
C800	F	F	O	O	F
D000	F	F	O	F	O
D800	F	F	O	F	F
E000	F	F	F	O	O
E800	F	F	F	O	F
F000	F	F	F	F	O
F800	F	F	F	F	F

NOTE: O = ON, F = OFF

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SETTING BASE ADDRESS OF COMMUNICATIONS BUFFER

Jumpers JP1-JP13 are used to set the base address of the 512 Kbyte communications buffer, as shown in the following table: In addition, jumper JA6 is used to enable/disable the communications buffer access. If no access to the communications buffer is required, then it is suggested that this jumper be disabled. The address space in A24 or A32 does not have to be reserved, and jumpers JP1-JP13 are inactive. This capability does not exist on PB-0760-A08-010 motherboard.

Table 2-7. Jumpers Used For Communications Buffer Base Address

Jumper #	VMEbus Address Bit
JP1	A31
JP2	A30
JP3	A29
JP4	A28
JP5	A27
JP6	A26
JP7	A25
JP8	A24
JP9	A23
JP10	A22
JP11	A21
JP12	A20
JP13	A19

SETTING ADDRESS MODIFIERS ALLOWED IN EXTENDED, STANDARD, AND SHORT I/O ADDRESS SPACE

JP15 is used to select the address modifiers that are permitted in extended (32-bit) address space. If the jumper is on, only supervisor accesses are permitted in extended address space (address modifier 0x0D only). If the jumper is off, then both 0x0D and 0x09 address modifiers are selected.

JP16 selects the address modifiers permitted in the standard (24-bit) address space. If the jumper is on, only supervisor accesses are permitted in standard address space (address modifier 0x3D only). If it is off, then both 0x3D and 0x39 address modifiers are allowed.

JP14 selects the address modifiers that are permitted in the short I/O (16-bit) address space. If the jumper is on, only supervisor accesses are permitted in short I/O space (address modifier 0x2D only). If it is off, then both 0x2D and 0x29 address modifiers are permitted.

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Daughter Card Jumper Settings

COMPONENT SIDE

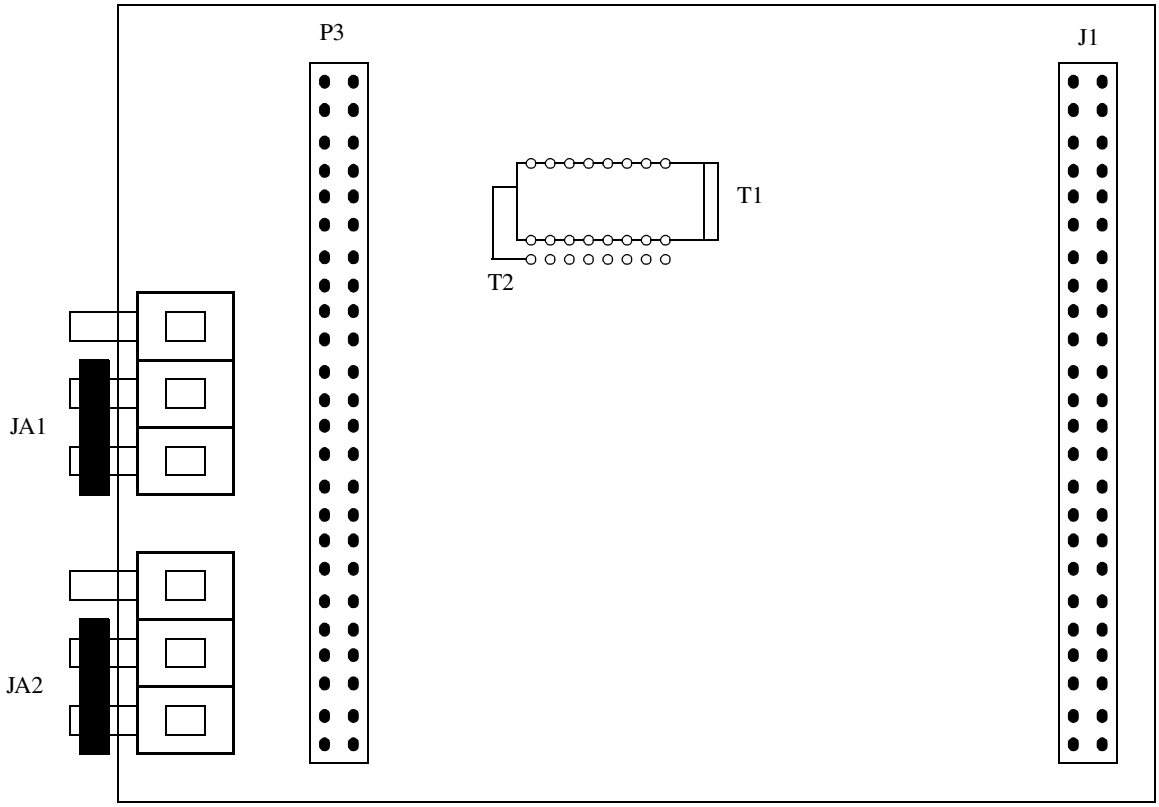


Figure 2-7. Eagle Daughter Card

Jumper Number	**Configuration**	**Use**
JA1	1 ⇔ 2	+12V (fused) transceiver power to AUI connection at VMEbus P2 connector. This setting is only valid for the P2 - Full version of the Eagle.
	2 ⇔ 3	+12V (fused) transceiver power to AUI connection at front panel connector. **These pins must always be jumpered together on the P2 - B Row Only version of the Eagle.**
JA2	1 ⇔ 2	Selects full-step signalling AUI interface (for Ethernet version 1.0 applications).
	2 ⇔ 3	Selects half-step signalling AUI interface (for Ethernet version 2.0 and IEEE 802.3 applications).

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STEP 4. POWER OFF SYSTEM

Once the board is configured, ensure that the system power is OFF.

CAUTION

System power and peripheral power must be turned OFF before attempting to install the Eagle. Failure to do so may result in severe damage to the board and/or system.

STEP 5. CABLING PROCEDURE

As part of the installation procedure, you will need to connect the Eagle to an Ethernet transceiver on the network. Your cabling options are as follows:

If you have the P2 - B Row Only version of the Eagle. The P2 - B Row Only version of the Eagle utilizes row B only on the P2 connector configuration, Ethernet I/O signals can only be directed out of the front panel. To do so, use a standard Ethernet 802.3 transceiver cable. The cable is one-to-one, with DB15 connectors on both ends. An example of such a cable is part number LCN2000 from Black Box Corporation. The cable pinout is shown in Chapter 6 of this user's guide.

If you have the P2 - Full version of the Eagle. The P2 - Full version of the Eagle uses all three rows (A,B,C) on the P2 connector you can direct, Ethernet I/O signals out of either the front panel or the VMEbus P2 connector.

- For front panel I/O, use a standard Ethernet 802.3 transceiver cable, as described above.
- To route I/O signals off the P2 connector, connect an Interphase Eagle P2/DB15 Adapter (part no. SP-1040-000-XOA) to P2. A standard Ethernet 802.3 transceiver cable can then be used to interconnect the adapter and transceiver.

The P2/DB15 Adapter is depicted in figure 2-8.

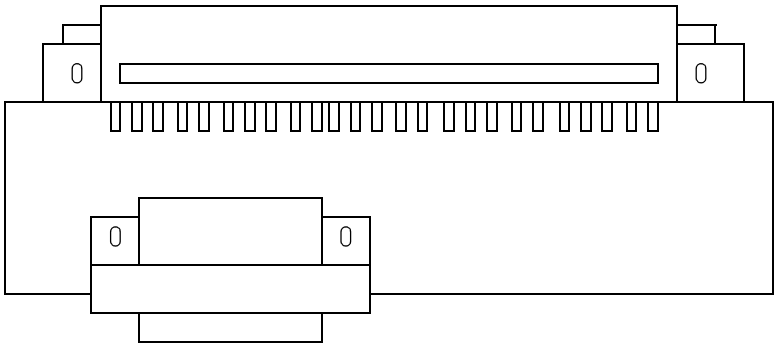


Figure 2-8. Eagle P2/DB15 Adapter

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VMEbus P1 AND P2 CONNECTOR CONFIGURATIONS

Both versions of the Eagle have the same VME P1 connector configuration. See the table in Chapter 6.

The P2 - B Row Only version of the Eagle utilizes only row B on the P2 connector. (See the table in Chapter 6 of this user's guide)

The P2 - Full version of the Eagle uses all three rows on the P2 connector. (See the table in Chapter 6 of this user's guide)

CAUTION

If you have the P2 - Full version of the Eagle, do not plug it into a backplane that busses rows A and C of the P2 connector. Failure to observe this warning can result in extensive damage to the board and/or system.

INSTALLING THE CABLE AND BOARD

1. With the power off, connect the transceiver cable to the transceiver, making sure the pins are properly oriented.

2. If you are using the Interphase P2/DB15 Adapter to interface a standard Ethernet 802.3 transceiver cable to the P2 connector, plug this device onto the P2 connector.

3. Carefully slide the Eagle into the VMEbus card slot. It should slide all the way in without any difficulty. If it doesn't, pull it out and check to make sure that there are no cables in the way.

4. Plug the other end of the transceiver cable into the appropriate connector (front panel or P2/DB15 Adapter).

5. Once the board is properly seated in the slot, tighten the captive mounting screws on each end of the board.

STEP 6. POWER ON SYSTEM

Once the 4207 is installed in the chassis and properly cabled, turn the host on. **If your other system documentation (host/peripherals) contains precautionary statements about powering up the system, please be sure to follow these precautions.**

After powering up the system, observe the 4207's LED located on the edge of the board opposite the VMEbus connectors. When the LED turns from red to green, the 4207 has successfully completed its power-up diagnostics and is ready to accept commands. For more information on the LED, see Chapter 6 of this user's guide.

NOTE

If the LED fails to turn green, turn off the system and ensure that the 4207 is fully inserted into the slot. If the LED still fails to turn green after reapplying power to the system, call Customer Service at Interphase for assistance.

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SECTION III
PB04207-001,REVA EAGLE MOTHERBOARD INSTALLATION

The figure below shows the location of the jumpers on the Eagle PB04207-001,REVB Motherboard.

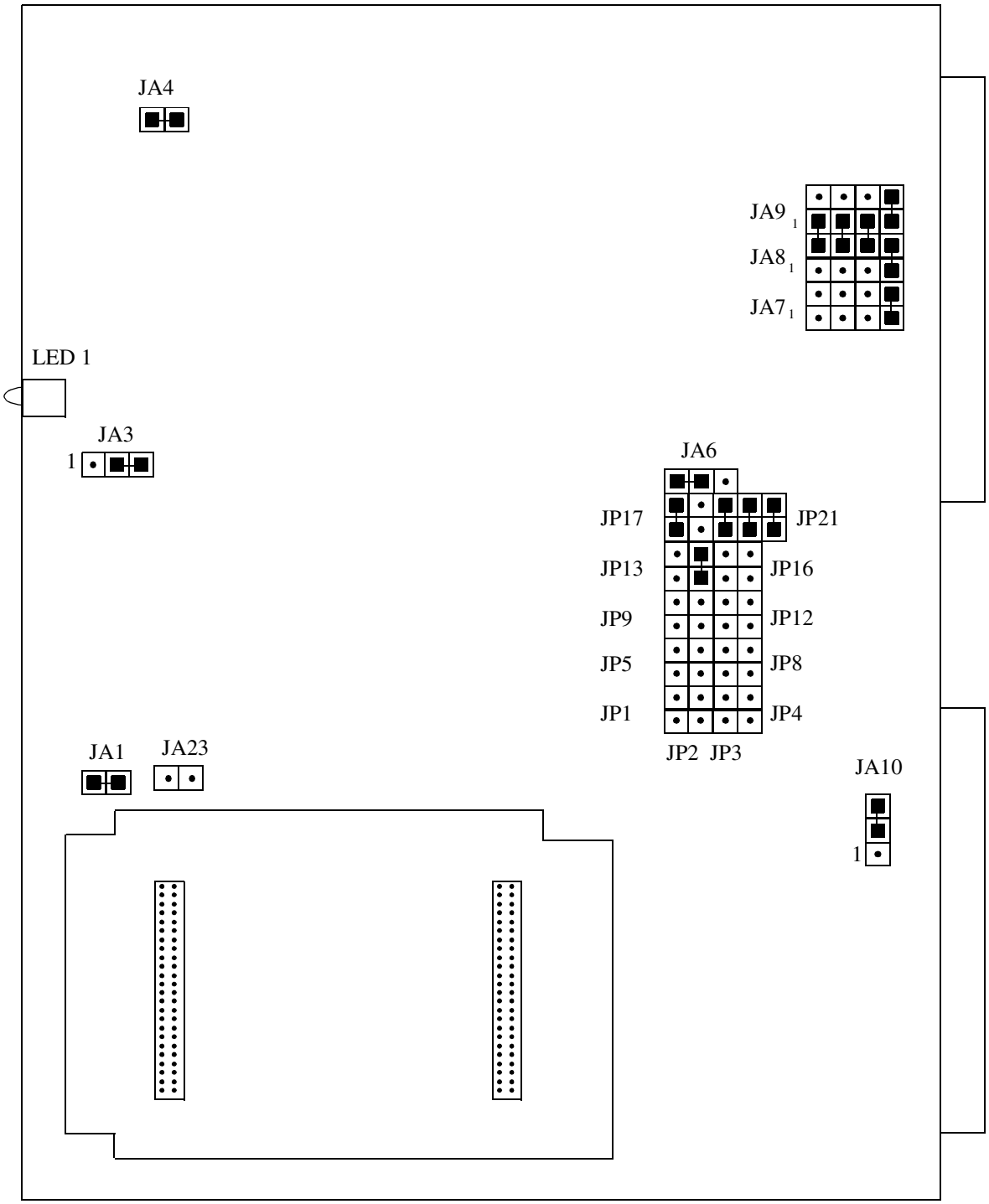


Figure 2-9. V/Ethernet 4207 Eagle Board Layout

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INSTALLATION PROCEDURE

For proper installation, it is imperative that you follow the steps below:

STEP 1. VISUAL INSPECTION

Before attempting the installation of this board, make sure you are wearing an anti-static or grounding device. Remove the Eagle board from the anti-static bag, and visually inspect it to ensure no damage has occurred during shipment. A visual inspection usually is sufficient, since each board is thoroughly checked at Interphase just prior to shipment.

If the board is undamaged and all parts are accounted for, proceed with the installation.

STEP 2. SET ONBOARD JUMPERS

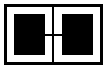
Set all onboard jumpers so that the Eagle is properly configured for operation within your system. The board layout on page 2-23 shows the location of each jumper on the Eagle.

Note that some of the jumper settings must not be altered.

MOTHERBOARD JUMPER SETTINGS

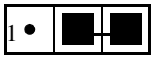
Set all onboard jumpers so that the Eagle is properly configured for operation within your system. The board layout as illustrated in Figure 2-9 shows the location of the jumpers.

JA1



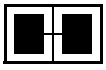
IN = **FACTORY DEFAULT** - **Do not alter.**
OUT = Deselects clock-to-memory circuitry.

JA3



1⇔2 A EPROM larger than a 27512 is installed.
2⇔3 A 27512 EPROM is installed. **FACTORY DEFAULT** - **Do not alter**.

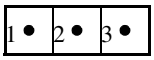
JA4



IN = **FACTORY DEFAULT** - **Do not alter.**
OUT = Deselects clock-to-CPU circuitry.

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JA6



1⇔2 Enables Communications Buffer VME access.
2⇔3 Disables Communications Buffer VME access.

JA7, **JA8**, **JA9** Figure 2-10 shows all possible valid configurations for this jumper block. Set the Eagle's VMEbus request priority level from 0 (lowest) to 3 (highest).

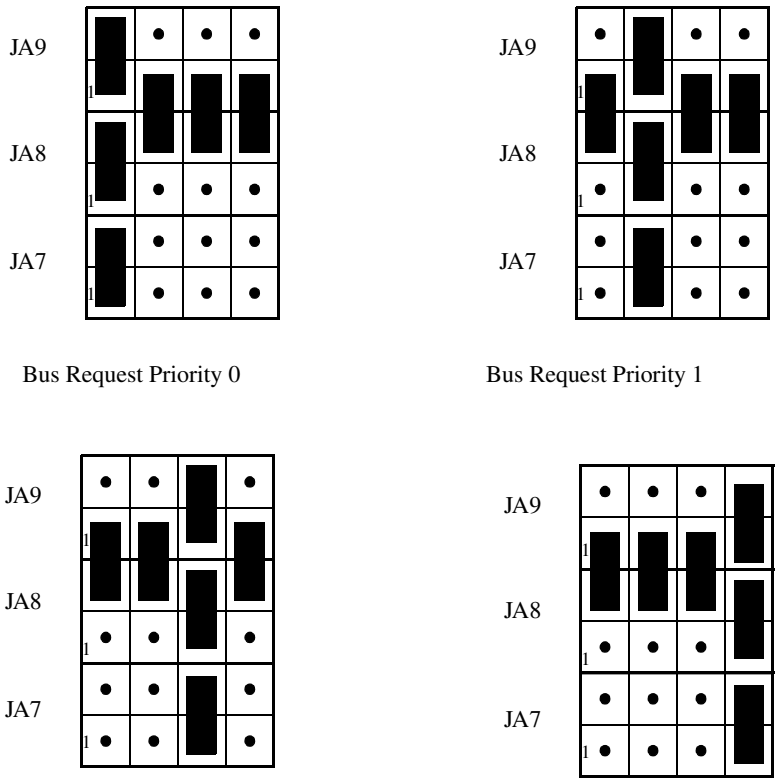
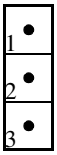


Figure 2-10. VMEbus Request Priority Jumper Settings

The JA10 jumper block determines whether the Eagle will use late or early release VMEbus arbitration when it is the bus master. If the jumper is set for late release, the VMEbus signal BBSY* is released **after** the last cycle is completely finished. If the jumper is set for early release, BBSY* is released **at the start** of the last cycle to allow for rearbitration during the last cycle. The possible settings are as follows:

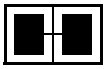
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JA10



1 ⇔ 2 Early release of BBSY*.
2 ⇔ 3 Late release of BBSY*

JA23



IN = **FACTORY DEFAULT** - **Do not alter.**
OUT = Disable Parallel Processor.

The Eagle has jumpers JP1-JP21. These jumpers are used to select:

— the VMEbus base address of the 512 Kbyte communications buffer RAM
— the base address of the 2 Kbyte short I/O RAM
— the allowable address modifiers

Refer to the board layout on page 2-23 to determine the location of the jumpers.

The following sections describe how to set the jumpers to meet the requirements of your specific installation.

SETTING BASE ADDRESS OF SHORT I/O SPACE RAM

Jumpers JP17-JP21 are used to set the base address of the 2 Kbytes of short I/O space RAM on the Eagle. As discussed in the Functional Overview, all interaction between the host and the 4207 takes place in this 2K space.

The short I/O space is mapped into the uppermost 2K of the Eagle's 512K communications buffer. The jumpers correspond to VMEbus address lines A15-A11, respectively, as shown in Table 2-7.

Table 2-8. Jumpers Used for Short I/O Base Address

Jumper #	VMEbus Address Bit
JP17	A15
JP18	A14
JP19	A13
JP20	A12
JP21	A11

The following table shows all possible base address settings for the short I/O space

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Table 2-9. Base Addresses for Short I/O RAM.

Address	Jumper ON/OFF				
	JP17	JP18	JP19	JP20	JP21
0000	O	O	O	O	O
0800	O	O	O	O	F
1000	O	O	O	F	O
1800	O	O	O	F	F
2000	O	O	F	O	O
2800	O	O	F	O	F
3000	O	O	F	F	O
3800	O	O	F	F	F
4000	O	F	O	O	O
4800	O	F	O	O	F
5000	O	F	O	F	O
5800	O	F	O	F	F
6000	O	F	F	O	O
6800	O	F	F	O	F
7000	O	F	F	F	O
7800	O	F	F	F	F
8000	F	O	O	O	O
8800	F	O	O	O	F
9000	F	O	O	F	O
9800	F	O	O	F	F
A000	F	O	F	O	O
A800	F	O	F	O	F
B000	F	O	F	F	O
B800	F	O	F	F	F
C000	F	F	O	O	O
C800	F	F	O	O	F
D000	F	F	O	F	O
D800	F	F	O	F	F
E000	F	F	F	O	O
E800	F	F	F	O	F
F000	F	F	F	F	O
F800	F	F	F	F	F

NOTE: O = ON, F = OFF

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SETTING BASE ADDRESS OF COMMUNICATIONS BUFFER

Jumpers JP1-JP13 are used to set the base address of the 512 Kbyte communications buffer, as shown in the following table: In addition, jumper JA6 is used to enable/disable the communications buffer access. If no access to the communications buffer is required, then it is suggested that this jumper be disabled. The address space in A24 or A32 does not have to be reserved, and jumpers JP1-JP13 are inactive. This capability does not exist on PB-0760-A08-010 motherboard.

Table 2-10. Jumpers Used For Communications Buffer Base Address

Jumper #	VMEbus Address Bit
JP1	A31
JP2	A30
JP3	A29
JP4	A28
JP5	A27
JP6	A26
JP7	A25
JP8	A24
JP9	A23
JP10	A22
JP11	A21
JP12	A20
JP13	A19

SETTING ADDRESS MODIFIERS ALLOWED IN EXTENDED, STANDARD, AND SHORT I/O ADDRESS SPACE

JP15 is used to select the address modifiers that are permitted in extended (32-bit) address space. If the jumper is on, only supervisor accesses are permitted in extended address space (address modifier 0x0D only). If the jumper is off, then both 0x0D and 0x09 address modifiers are selected.

JP16 selects the address modifiers permitted in the standard (24-bit) address space. If the jumper is on, only supervisor accesses are permitted in standard address space (address modifier 0x3D only). If it is off, then both 0x3D and 0x39 address modifiers are allowed.

JP14 selects the address modifiers that are permitted in the short I/O (16-bit) address space. If the jumper is on, only supervisor accesses are permitted in short I/O space (address modifier 0x2D only). If it is off, then both 0x2D and 0x29 address modifiers are permitted.

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Daughter Card Jumper Settings

COMPONENT SIDE

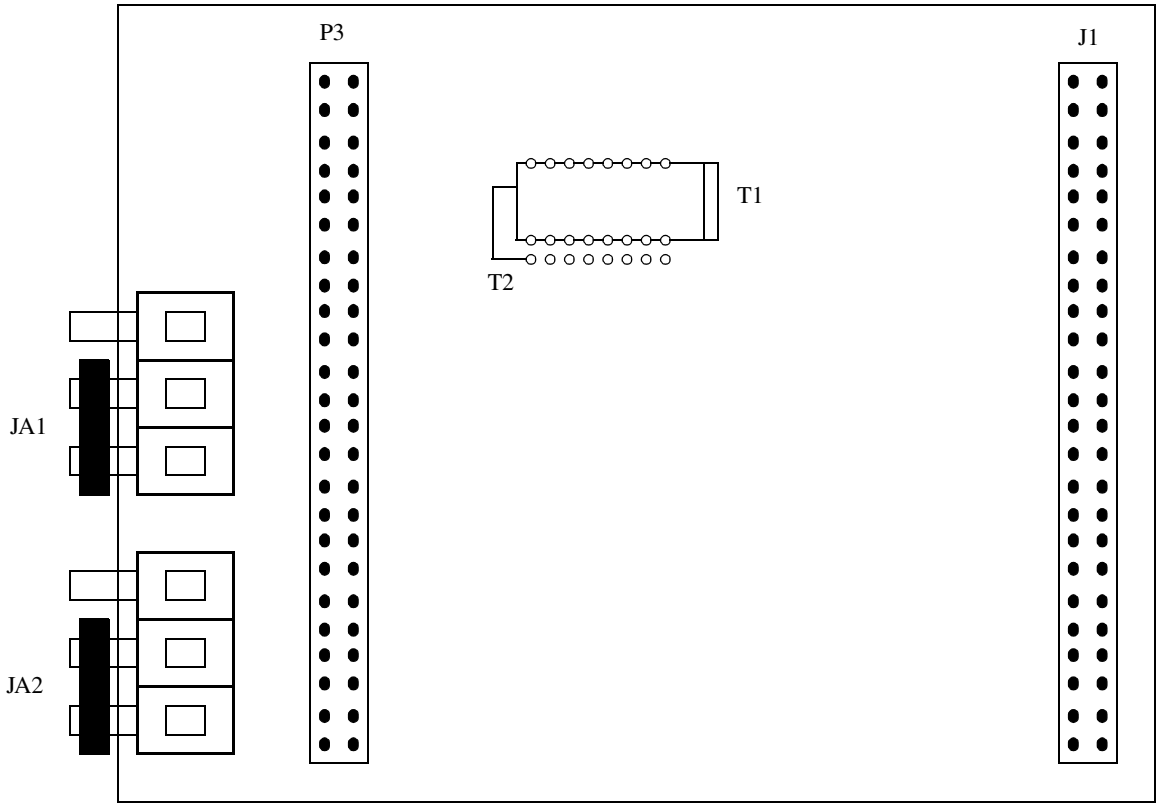


Figure 2-11. Eagle Daughter Card

Jumper Number	**Configuration**	**Use**
JA1	1 ⇔ 2	+12V (fused) transceiver power to AUI connection at VMEbus P2 connector. This setting is only valid for the P2 - Full version of the Eagle.
	2 ⇔ 3	+12V (fused) transceiver power to AUI connection at front panel connector. **These pins must always be jumpered together on the P2 - B Row Only version of the Eagle.**
JA2	1 ⇔ 2	Selects full-step signalling AUI interface (for Ethernet version 1.0 applications).
	2 ⇔ 3	Selects half-step signalling AUI interface (for Ethernet version 2.0 and IEEE 802.3 applications).

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STEP 4. POWER OFF SYSTEM

Once the board is configured, ensure that the system power is OFF.

CAUTION

System power and peripheral power must be turned OFF before attempting to install the Eagle. Failure to do so may result in severe damage to the board and/or system.

STEP 5. CABLING PROCEDURE

As part of the installation procedure, you will need to connect the Eagle to an Ethernet transceiver on the network. Your cabling options are as follows:

If you have the P2 - B Row Only version of the Eagle. The P2 - B Row Only version of the Eagle utilizes row B only on the P2 connector configuration, Ethernet I/O signals can only be directed out of the front panel. To do so, use a standard Ethernet 802.3 transceiver cable. The cable is one-to-one, with DB15 connectors on both ends. An example of such a cable is part number LCN2000 from Black Box Corporation. The cable pinout is shown in Chapter 6 of this user's guide.

If you have the P2 - Full version of the Eagle. The P2 - Full version of the Eagle uses all three rows (A,B,C) on the P2 connector you can direct, Ethernet I/O signals out of either the front panel or the VMEbus P2 connector.

- For front panel I/O, use a standard Ethernet 802.3 transceiver cable, as described above.
- To route I/O signals off the P2 connector, connect an Interphase Eagle P2/DB15 Adapter (part no. SP-1040-000-XOA) to P2. A standard Ethernet 802.3 transceiver cable can then be used to interconnect the adapter and transceiver.
-

The P2/DB15 Adapter is depicted in Figure 2-12.

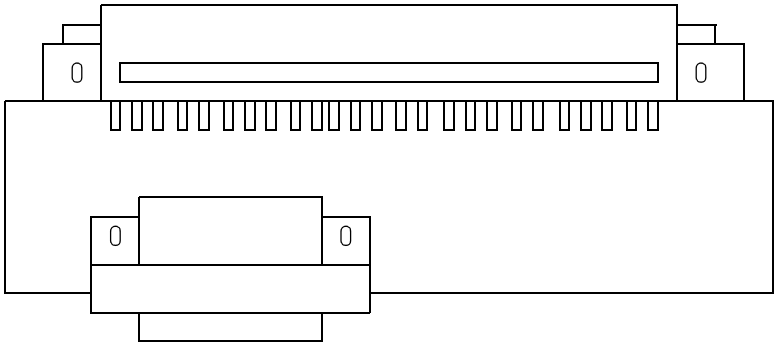


Figure 2-12. Eagle P2/DB15 Adapter

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VMEbus P1 AND P2 CONNECTOR CONFIGURATIONS

Both versions of the Eagle have the same VME P1 connector configuration. See the table in Chapter 6.

The P2 - B Row Only version of the Eagle utilizes only row B on the P2 connector. (See the table in Chapter 6 of this user's guide)

The P2 - Full version of the Eagle uses all three rows on the P2 connector. (See the table in Chapter 6 of this user's guide)

CAUTION

If you have the P2 - Full version of the Eagle, do not plug it into a backplane that busses rows A and C of the P2 connector. Failure to observe this warning can result in extensive damage to the board and/or system.

INSTALLING THE CABLE AND BOARD

1. With the power off, connect the transceiver cable to the transceiver, making sure the pins are properly oriented.

2. If you are using the Interphase P2/DB15 Adapter to interface a standard Ethernet 802.3 transceiver cable to the P2 connector, plug this device onto the P2 connector.

3. Carefully slide the Eagle into the VMEbus card slot. It should slide all the way in without any difficulty. If it doesn't, pull it out and check to make sure that there are no cables in the way.

4. Plug the other end of the transceiver cable into the appropriate connector (front panel or P2/DB15 Adapter).

5. Once the board is properly seated in the slot, tighten the captive mounting screws on each end of the board.

STEP 6. POWER ON SYSTEM

Once the 4207 is installed in the chassis and properly cabled, turn the host on. **If your other system documentation (host/peripherals) contains precautionary statements about powering up the system, please be sure to follow these precautions.**

After powering up the system, observe the 4207's LED located on the edge of the board opposite the VMEbus connectors. When the LED turns from red to green, the 4207 has successfully completed its power-up diagnostics and is ready to accept commands. For more information on the LED, see Chapter 6 of this user's guide.

NOTE

If the LED fails to turn green, turn off the system and ensure that the 4207 is fully inserted into the slot. If the LED still fails to turn green after reapplying power to the system, call Customer Service at Interphase for assistance.

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CHAPTER 3
FUNCTIONAL OVERVIEW

This chapter describes the general considerations involved in writing a driver for the Eagle. It is intended as an introduction to the detailed description of the Eagle's MACSI system interface and commands provided in Chapters 4 and 5.

OVERVIEW OF SYSTEM INTERFACE

The Eagle is programmed by building I/O parameter blocks (IOPBs) in short I/O RAM. The term "short I/O" refers to a VMEbus-addressed block of memory in which only the lower 16 VMEbus address lines are used for transactions. The upper 16 VMEbus address lines are not used.

On the Eagle, the short I/O space resides in the uppermost 2 Kbytes of the Eagle's communications buffer. This 2K space can be addressed via either a short I/O address or a buffer memory address.

The Eagle's Multiple Active Command Software Interface (MACSI) is implemented as a set of registers in this 2K space. All interaction between the host and the Eagle takes place there.

After building an IOPB, the host creates an entry pointing to the IOPB in one of the following MACSI data structures:

- The Command Queue
  - or -
- The Master Command Entry

The Command Queue is a circular queue used to queue up multiple commands to transmit or receive packets over the network. The Master Command Entry is used to issue individual, control-related commands to the Eagle (such as Initialize Controller or Perform Extended Diagnostics).

Commands are stored in one of four internal Work Queues. When the host issues a command, part of the information it gives the Eagle is which Work Queue that command should be placed in.

The Eagle operates with the following Work Queues:

- Transmit Work Queue
- Receive Work Queue
- Miscellaneous Work Queue
- DMA Work Queue

As their names imply, the Transmit and Receive Work Queues are used for transmitting and receiving data over the network. The Miscellaneous Work Queue is used for issuing various non-data movement commands. The DMA Work Queue is used for miscellaneous tasks requiring DMA data transfers in or out of the 512K communications buffer.

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MACSI ORGANIZATION

MACSI consists of six major sections:

- Master Control/Status Block
- Master Command Entry
- Command Queue
- Host Usable Space
- Command Response Block/Returned IOPB
- Controller Specific Space

The **Master Control/Status Block** is used to pass and receive information relative to the overall operation of the controller.

The **Master Command Entry** (MCE) provides a single-threaded command execution path for all Eagle commands, except those used to transmit and receive packets over the network. The MCE has the same format as a Command Queue entry. Unlike the Command Queue, however, a command submitted to the MCE is processed to completion before another command is processed.

The **Command Queue** consists of up to 37 Command Queue Entries. The number of entries is programmed via the Initialize Controller command. It is used to queue commands to transmit or receive packets over the network. The Command Queue is circular in nature (if more than one Command Queue entry is used). The Eagle infers ordering from the sequential nature of this queue, so no special ordering parameters are required.

Host Usable Space is free-form memory accessible to both the host and the controller. This area is typically used for IOPBs, the Controller Initialization Block, and any other data that must be passed to the controller. Some multiprocessing applications might find this a convenient place for semaphores between CPUs.

The **Command Response Block** and **Returned IOPB** areas are used to post command completion status and returned data. This section is mainly written by the Eagle and read by the host.

The **Controller Specific Space** contains the Controller Statistics Block and the Configuration Status Block. These blocks are used by the Eagle to post the current status of a variety of parameters.

EAGLE COMMAND SET

The Eagle command set is divided into two basic groups:

1. Commands involving the transmission and reception of data over the network (i.e. the Transmit and Receive commands). These commands are issued through the Command Queue.

2. All commands other than Transmit and Receive are issued through the Master Command Entry.

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MASTER COMMAND ENTRY AND COMMAND QUEUE

Both the Master Command Entry and Command Queue entry are 12-byte blocks with the same format, namely:

a. The code of the desired command.
b. A pointer to the command's IOPB.
c. A Go/Busy bit.

The Go/Busy bit is a semaphore reflecting ownership of the entry. Once the host sets the Go/Busy bit, the Eagle owns the entry. The host must not modify the entry until the Eagle clears the Go/Busy bit. The Eagle clears the bit to indicate that the space is available to accept another command from the host.

Commands issued through the Master Command Entry (MCE) have precedence over those issued through the Command Queue. In general, the MCE is used to get the board up and running. The Command Queue is then used to handle the transmission and reception of data. It should not be necessary to use the MCE again until there is a "break" in normal operations, such as a board reset. The only exception is if you are using the Multiple Completions per Interrupt feature (see Chapter 5).

To start Queued IOPB operation, the host must set the Start Queue Mode bit (bit 0 in the Master Control Register). This bit is set only once and is never reset during normal board operation.

The Eagle acknowledges entering Queue Mode by: 1) setting the Queue Mode Started bit (bit 5 in the Command Response Status Word), and 2) issuing a Command Complete Interrupt. The interrupt uses the Eagle's interrupt vectors, which should also be initialized prior to setting the Start Queue Mode bit. These vectors are set in the Controller Initialization Block (see Chapter 5)

The only way to exit from Queue Mode is to reset the controller.

Note that the Eagle uses an internal command queue pointer to determine which command to do next. To synchronize operations with the host, the Eagle bumps its counter and then the host bumps its counter once per interrupt. The Eagle can post multiple completions to the host per interrupt via the Initialize Multiple Completions per Interrupt command (see Chapter 5). If this feature is not enabled, the Eagle will post one command completion per interrupt.

ISSUING A COMMAND VIA THE MASTER COMMAND ENTRY

The Master Command Entry is used to issue to following commands:

- Initialize Controller
- Perform Extended Diagnostics
- Change Logical Address Filter
- Change Default Node Address
- Execute Downloaded Software
- DMA
- Initialize Multiple Completions per Interrupt

For a full description of each of the above commands, please refer to Chapter 5.

The following is a summary of the sequence of events that needs to take place in order to issue commands through the Master Command Entry:

1. the host builds an IOPB for the command in Host Usable Space.

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2. The host writes the following information to the Master Command Entry:

   - The command code
   - A pointer to the IOPB
   - The Go/Busy bit is set

3. The Eagle copies the command code and other command-related information from the IOPB into a work queue element.

4. The Eagle executes the command. It then writes the IOPB and control information to the Command Response Block. This Returned IOPB contains command completion status, as well as information to enable the host to identify which command has been completed.

5. The Eagle posts completion to the host by: 1) setting the ownership bit (i.e. Command Response Block Valid/ Clear Interrupt bit) in the Command Response Status Word in the Command Response Block, and 2) optionally generating an interrupt. This frees up the Master Command Entry for reuse by the host.

6. The host acknowledges the command's completion by clearing the Command Response Status Word. This frees up the Command Response Block to receive information concerning the next completed command.

ISSUING COMMANDS THROUGH THE COMMAND QUEUE

The Command Queue is used to issue the following commands:

- Transmit
- Receive

For a full description of the above commands, please refer to Chapter 5. The procedure for issuing commands through the Command Queue is as follows:

1. The host builds an IOPB for the desired command in Host Usable Space.

2. The host creates an entry in the next available slot in the Command Queue.

   A Command Queue entry is a 12-byte block with the same format as the Master Command Entry (i.e., the code of the desired command, a pointer to the command's IOPB, and a set Go/Busy bit).

3. The Eagle polls the Command Queue Entry pointed to by the current Command Queue pointer. If the Go/Busy bit is set, it copies the entry and its associated IOPB into an internal Work Queue. The Eagle clears the Go/Busy bit, freeing up that slot in the Command Queue for reuse by the host.

4. The Eagle executes the command in the Work Queue. A command issued through the Command Queue is executed in the same order that it was issued, regardless of the Work Queue in which it has been placed. The Eagle infers ordering from the sequential nature of the Command Queue.

5. The Eagle writes the IOPB and control information to the Command Response Block/Returned IOPB area. The Returned IOPB contains command completion status, as well as information to enable the host to identify which command has been completed.

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NOTE

> If you have enabled Multiple Completions per Interrupt, the Eagle will post multiple command completions to the Command Response Block before setting bit 0 of the Command Response Status Word and then optionally generating an interrupt. See Chapter 5 for details on this command.

6. The Eagle posts completion to the host by: 1) setting the Command Response Block Valid bit (bit 0) in the Command Response Status Word in the Command Response Block, and 2) optionally generating an interrupt.

7. The host acknowledges the command's completion by clearing all bits in the Command Response Status Word. This frees up the Command Response Block to receive information concerning the next completed command.

HANDLING DATA TRANSMISSIONS

512K COMMUNICATIONS BUFFER

The 512K communications buffer is used to send and receive data over the network. It is logically divided into two buffer pools — one for transmitted and one for received data.
It can also be used for downloading code to the Eagle, storing gather lists, and other application-specific tasks. A memory map of the buffer is depicted in figure 3-1.

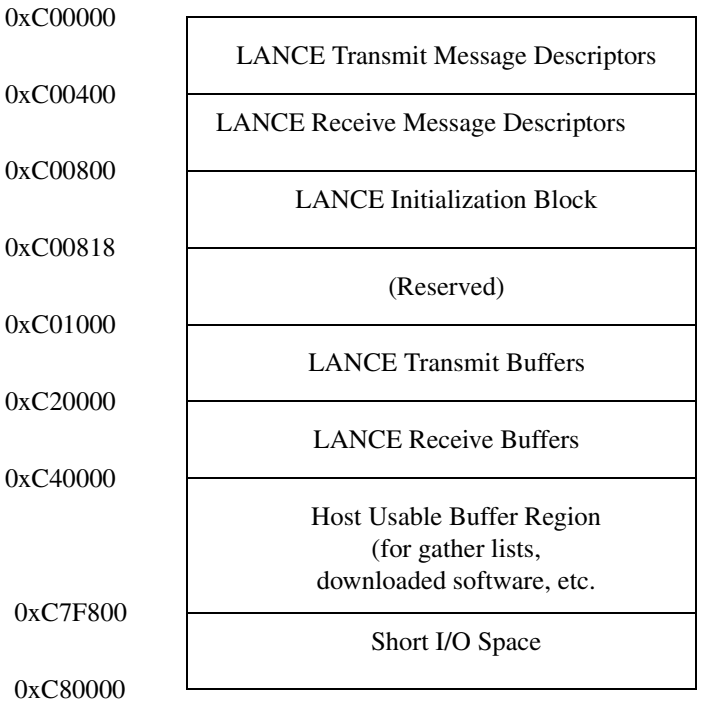


Figure 3-1. Communications Buffer Memory Map

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The buffer pools consist of 16 Transmit buffers and 32 Receive buffers.

The transmit buffers are those used by the Eagle for a transmit operation that automatically utilizes DMA (e.g. transmit IOPB with the DMA bit active).

The Eagle automatically allocates receive buffers for incoming packets. It assigns one receive buffer to each of the LANCE receive rings. Thus, the Eagle will not allow a configuration where the number of receive buffers is less than the number of LANCE receive rings.

TRANSMITTING AND RECEIVING DATA OVER THE NETWORK

The Eagle supports data transmissions in DMA mode. To transmit a message, the host builds a Transmit IOPB with DMA enabled, and then issues a Transmit command through the Command Queue. The Eagle then moves the data (via DMA) from system memory into internal transmit buffer memory. It queues the data up onto the LANCE's transmit buffer ring and transmits it at the first available opportunity. After all the data has been transferred over the network, the Eagle updates the Transmit IOPB with the status from the LANCE Descriptor Ring, copies it to the Returned IOPB and then, optionally, generates an interrupt to the host.

The maximum number of pending Transmit IOPBs is 16.

For the host to receive a network packet, it must place a Receive IOPB in the Receive Work Queue by issuing a Receive command through the Command Queue. Once a network packethas been received and the appropriate action taken with the data (depending on the Receive IOPB fields), the Receive IOPB will be updated with the status from the operation and copied to the Command Response Block. Optionally, the Eagle may generate an interrupt to the host.

The 4207 can receive packets up to 4 Kbytes in size, even though the maximum legal packet size in the Ethernet IEEE 802.3 specifications is 1518 bytes (1500 bytes plus 14 bytes of header and 4 bytes of checksum).

In order not to miss any incoming packets, it is advisable to prepost a number of Receive IOPBs, awaiting the next message. As a rule of thumb, you should have 16 "hanging" Receive IOPBs at all times, once the Eagle has gone into Queue Mode.

GATHER OPERATIONS

Gather operations enable you to retrieve contiguous data from noncontiguous areas of memory. These operations can be implemented using the Transmit command. To do so, specify the following information in the command's IOPB:

- A pointer to the gather list residing in the Eagle's 512K communications buffer (in the IOPB's 'VMEbus Transfer Address/Buffer Offset' field)
- The number of entries in the Gather list (in the 'Gather Entry Count' field)
- The offset of the gather list from the base of buffer memory (in the VMEbus Transfer Address/ Buffer Offset field)
- The gather bit and DMA bit must both be set

Gather lists reside in the Host Usable Buffer Region of the 512K communications buffer. Note that this region should not be confused with Host Usable Space in short I/O. (Refer to the memory map in Chapter 5) It is the responsibility of the host software to manage the use and reuse of the buffer memory containing the gather list(s).

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The gather list must not be changed for the duration of the IOPB request, since it is used by the firmware.

The format to use for each entry in the gather list is shown in table 3-1.

Table 3-13. Format Of A Gather List Entry

ELEMENT	SIZE (Bytes)
VMEbus Transfer Address	4
VMEbus Transfer Options	2
Transfer Byte Count	2

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- **VMEbus Transfer Address**
  This value is the VMEbus address for the start of the data.

- **VMEbus Transfer Options**
  This field specifies the VMEbus memory type, transfer type and address modifier codes used for all VMEbus transfers associated with the command. The bits are defined as follows:

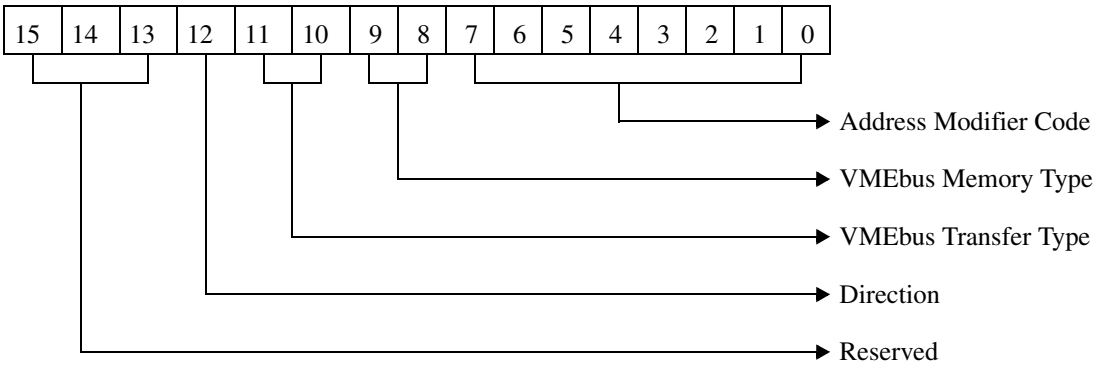


Figure 3-2. VMEbus Transfer Options for Gather

Bits 0-7 Address Modifier Code:

This byte is the address modifier used by the Eagle for all VMEbus data transfers associated with this command. It is not modified in any way by the Eagle.

Bits 8-9 VMEbus Memory Type:

This two-bit field specifies the width of the VMEbus data transfers. A value of 0x1 specifies 16-bit maximum data width. 0x2 specifies 32-bit maximum. All other values are invalid.

Bits 10-11 VMEbus Transfer Type:

These two bits specify the type of data transfer to be performed. A value of zero specifies a normal transfer. 0x1 specifies a block mode transfer. Values of 0x2 and 0x3 are invalid.

Bit 12 Direction:

The bit specifies the direction of the data transfer to be performed. A value of 1 specifies a VMEbus Write. 0 specifies a VMEbus Read.

Bits 13-15 Reserved:

These bits are reserved and must be set to zero by the host.

- **Transfer Size**
  This is the number of bytes that will be transferred across the VMEbus via DMA for this gather list entry.

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VMEbus INTERRUPTS

If desired, the Eagle can be enabled to generate a Command Complete interrupt on the VMEbus (either successful completion, or terminated due to an error). If you are using the Multiple Completions per Interrupt feature, the interrupt is enabled in the Initialize Multiple Completions per Interrupt IOPB (see Chapter 5). Otherwise, the interrupt is enabled in the IOPB of the given command.

The Eagle is capable of responding to the VMEbus interrupt acknowledge cycle with different interrupt vectors, based on the cause of the interrupt. For those systems where only one interrupt vector is allowed per device, the cause of the interrupt can be determined by checking the status bits in the Command Response Status Word and the Work Queue number (both found in the Command Response Block).

The actual VMEbus interrupt will be cleared at the completion of the interrupt acknowledge cycle. A Clear Interrupt operation must be executed to notify the Eagle that the interrupt has been handled. This is done by clearing the bit 0 in the Command Response Status Word in the Command Response Block.

NOTE

> Any information that the host processor needs from the Command Response Block/ Returned IOPB must be accessed BEFORE the Clear Interrupt is issued.

For a description of VMEbus timing considerations, please refer to Appendix D.

PERFORMING DIAGNOSTICS

TYPES OF AVAILABLE DIAGNOSTICS

The Eagle's diagnostics are partitioned into two distinct groups:

- Power Up diagnostics
- Extended Diagnostics

Power up diagnostics are executed when the 4207 undergoes a hard reset (i.e. power is cycled off then on). These initial diagnostics are described in the remainder of this chapter.

In contrast, extended diagnostics are initiated by the host using the Eagle's Perform Extended Diagnostics command. A detailed description of this command is included in the chapter on the Eagle's command set (see Chapter 5).

POWER UP DIAGNOSTICS

Power up diagnostics are executed as part of the first 68020 RESET after power up. After power up completion, a signature is placed in memory indicating that power up diagnostics have executed to normal completion. Because of the signature in memory, subsequent 68020 RESETs do not execute power up diagnostics. This is done to minimize RESET latency.

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Because execution time is especially critical with controller power up sequences, the power up code has been optimized for speed. In order to accomplish this, some compromises have been made, limiting the thoroughness of power up diagnostic functions.

Due to the fact that the Eagle has no intrinsic knowledge about the VMEbus in which it is installed, no VMEbus activity is initiated by power up diagnostics.

Below is a brief list of Eagle functions that are tested by either power up or extended diagnostics. In some cases, a combination of both power up and extended diagnostics is needed to test that Eagle function fully.

- 68020 processor
- Data/address/status registers
- Instructions and addressing modes
- EPROM data
- Onboard real-time clock (timer)
- Onboard RAM
- 68020 scratchpad/code RAM
- Data buffer RAM
- BUSpacket Event RAM
- Non-volatile RAM (NOVRAM)
- Host $\rightarrow$ Eagle mailbox interrupts
- Hardware control register
- LANCE & Ethernet interfaces.
- VMEbus transfers
- Eagle as master
- Eagle as slave

The remainder of this chapter describes the diagnostics that the Eagle executes as part of the first RESET sequence. The order in which the power up diagnostics appear below is the same order in which they are executed.

With the exception of the timer test, if a power up diagnostic fails, the test will attempt to preserve as much information about the problem as possible. In some cases, however, this will not be possible (e.g., 68020 diagnostics). An error code indicating which test failed will be stored in the Master Command Entry Control Register (located at an offset of +0x10 from the short I/O base address). As soon as the failure is detected, the 68020 branches to a tight loop.

Upon every RESET, the Controller Not Available bit (CNA) in the Master Status Register is set to '1'. If a power up diagnostic test detects an error, the CNA bit will be cleared to '0'.

When a power up reset occurs, the VMEbus signal SYSFAIL* is asserted and remains asserted until the diagnostics complete successfully. For additional information concerning SYSFAIL*, refer to the description of the SYSFAIL* Enable bit in the Master Control Register (see Chapter 4).

EPROM TEST

The EPROM Test validates the integrity of the data contained in EPROM. Because a double 32-bit checksum is used, the test checks not only the data, but the sequence in which the data appears as well.

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ONBOARD CONTROL REGISTER TEST

The Onboard Control Register Test performs a walking ones and walking zeros diagnostic on all bits of the Eagle's Onboard Control Register except for :

```
OCR_NVCE_BIT        /* NOVRAM chip enable*/
OCR_NVCK_BIT        /* NOVRAM clock*/
OCR_SFAIL_BIT       /* VMEbus SYSFAIL*/
```

QUICK STATIC RAM TEST

This test performs a quick pass test on 68020's scratchpad/code static RAM. It checks byte/word/longword data size capabilities. Every address line is toggled. Addressing is checked for aliasing problems. Every data value (0x0..0xFFFFFFFF) is used at least once.

QUICK BUFFER RAM TEST

This test uses the same algorithm as the Quick Static RAM Test, except that the target memory is data buffer DRAM.

TIMER TEST

The Timer Test verifies the period of onboard timer, as well as the 68020 interrupt level #5 request and the 68020 autovector #5 operation.

Because timer operation is optional (dependent upon the firmware/software running on the Eagle), timer failure alone is not enough to terminate power up diagnostics. In the event of a failure, the test will post global notification that timer is not functional, and power up diagnostics will continue with the next test.

Quick Buffer RAM FIFO Test

The Quick Buffer RAM FIFO Test verifies the data buffer DRAM's ability to transfer data from the random access port over to the sequential access port, and back again. Only one "row" is transferred.

Quick Event RAM Test

This test uses the same algorithm as the Quick Static RAM Test, except that the target memory is event RAM.

QUICK EVENT RAM FIFO TEST

This test uses the same algorithm as the Quick Buffer RAM FIFO Test, except that the target memory is event RAM.

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LANCE INTERNAL CONTROL/STATUS REGISTER TEST

All internal LANCE registers are tested to the extent that no network activity is induced. The test first performs an alternating bit test on CSR1 and CSR2. It then verifies the proper set/clear operation of the CSR0 and CSR3 bits. Finally, it checks the non-accessibility of CSR1..CSR3 when the LANCE is running (e.g.,~ CSR0_STOP).

LANCE INTERRUPT TEST

The Lance Interrupt Test verifies the LANCE INTR* signal operation, as well as the 68020 level #6 interrupt request and autovector #6 operations.

LANCE LOOPBACK TEST

The LANCE Loopback Test verifies the LANCE's internal loopback function. It also validates the LANCE pipeline and local bus master functions.

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CHAPTER 4
MACSI SYSTEM INTERFACE

OVERVIEW

Table 4-1 shows the location in 2K short I/O space of the functional blocks which comprise the MACSI interface.

IMPORTANT

> MACSI control registers should be accessed using single 16-bit operations (i.e. no byte accesses).

Table 4-3.Location Of MACSI Functional Blocks In Short I/O Space

OFFSET FROM SHORT I/O BASE ADDRESS	FUNCTIONAL BLOCK	SIZE (Bytes)
0x000 To 0x00F	Master Control/Status Block	16
0x010 To 0x01B	Master Command Entry	12
0x01C To (0x01C + (12* number of queues))	Command Queue	12* number of queues
(0x01C + (12* number of queues)) To 0x72F	Host Usable Space	1812 - (12* number of queues)
0x730 To 0x73B	Command Response Block	12
0x73C To 0x73F	(Alignment Fill)	4
0x740 To 0x763	Returned IOPB	36
0x764 To 0x7AB	Configuration Status Block	72
0x7AC To 0x7FB	Controller Statistics Block	80
0x7FC To 0x7FF	(Reserved)	4

This chapter describes each of the above functional blocks in detail.

MASTER CONTROL/STATUS BLOCK

The Master Control/Status Block consists of a Master Status Register, a Master Control Register, and a Queue Head Pointer. The location of these elements in short I/O space are shown in the following table.

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Table 4-4.Master Control/Status Block

Offset From Short I/O	Element	Size (Bytes)
+0x0000	Master Status Register	2
+0x0002	(Reserved)	2
+0x0004	Master Control Register	2
+0x0006	(Reserved)	2
+0x0008	Queue Head Pointer	4
+0x000C	(Reserved)	4

MASTER STATUS REGISTER

This register is used to report board level status. It is located at offset 0 from the Eagle's short I/O base address. The bits are defined as follows:

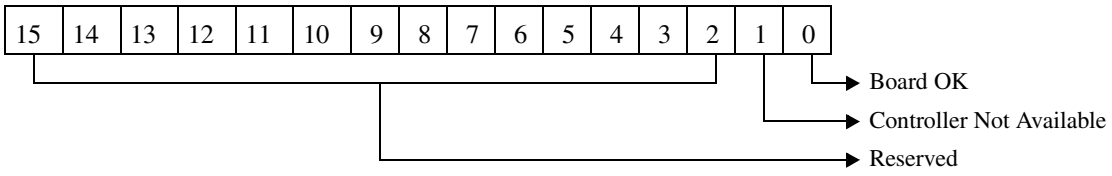


Figure 4-11. Master Status Register

Bit 0 Controller Not Available:

This bit is set to a 1 by the Eagle to indicate that it is not available for receipt of a command. This condition will be caused by a controller reset or execution of diagnostics. The Eagle will clear this bit to a 0 to indicate that it is capable of accepting a command.

Bit 1 Board OK:

If 1, this bit indicates that power up diagnostics were completed successfully. If 0, this bit indicates that the Eagle detected a failure during the power up diagnostics.

Bits 2-15 Reserved:

These bits are reserved and are cleared to 0 by the Eagle.

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MASTER CONTROL REGISTER

The Master Control Register, which is located at an offset of +0x0004 from the short I/O base address, contains bits that are set very infrequently. These bits are grouped here and segregated from the bits in the Master Status Register in order to avoid host/controller contention and timing windows.

All bits in this register are both set and cleared by the host. The Eagle clears these bits on power up or reset and does not alter them at any other time. The bits are defined as follows:

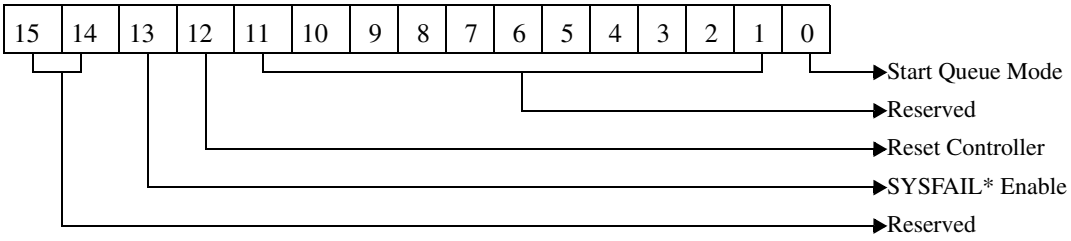


Figure 4-12. Master Control Register

Bit 0 Start Queue Mode:

Until all the Work Queues are initialized, all commands to the Eagle must be issued through the Master Command Entry. In order to start Queued IOPB operations, this bit must be set to a 1. The host sets this bit only after it has initialized the Work Queues. This bit is set only once and is never reset.

The Eagle will acknowledge entering Queue Mode by issuing a Command Complete Interrupt. The only way to exit from Queue Mode is to reset the controller.

Bits 1-11 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 12 Reset Controller:

This bit, which controls the microprocessor hardware reset line, is used to recover from a "board in the weeds" condition. To guarantee proper operation, the bit must: 1) remain set for at least 50 microseconds, and then 2) cleared. Use of this bit should not be necessary under normal operation.

Bit 13 SYSFAIL* Enable:

The host sets this bit to allow the Eagle to drive the SYSFAIL* signal on the VMEbus. If this bit is 0, the Eagle will only drive the SYSFAIL* signal for a very short time upon reset. If this bit is 1, the Eagle will drive the SYSFAIL* signal during the entire power on diagnostic, and will deassert upon successful completion of the diagnostic.

Bits 14-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

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QUEUE HEAD POINTER

The Queue Head Pointer is a **strictly optional** mechanism provided for use by the host. It is a four-byte value maintained by the host at an offset of +0x0008 from the Eagle's short I/O base address. **The Eagle does not use or modify this pointer in any way.**

MASTER COMMAND ENTRY

All IOPBs must be issued through the Master Command Entry (MCE) until the Eagle has been instructed to enter Queue Mode by setting the Start Queue Mode bit (bit 0) in the Master Control Register. As discussed in Chapter 3, all Eagle commands except Transmit and Receive are issued through the MCE.

Commands issued through the MCE are executed immediately, and take precedence over commands issued through the Command Queue. It is located at offset +0x0010 from the Eagle's short I/O base address.

Table 4-5.Master Command Entry

Offset From Short I/O Base	Element	Size (Bytes)
+0x0010	Master Command Entry Control Register	2
+0x0012	IOPB Address	2
+0x0014	Command Tag	4
+0x0018	(Reserved)	4

MASTER COMMAND ENTRY CONTROL REGISTER

This register is used to start the execution of the command.

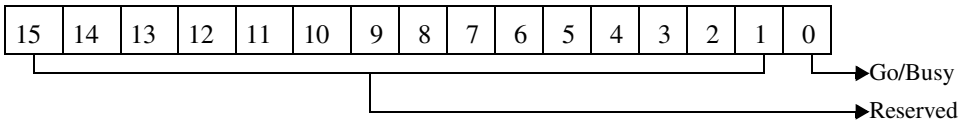


Figure 4-13. Master Command Entry Control Register

Bit 0 Go/Busy:

This bit is set by the host to initiate action on the Master Command Entry. Before this bit is set, an IOPB must be assembled for the entry. Upon completion of the command, the Eagle clears the Go/Busy bit to free up the MCE for a new command.

Bits 1-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

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IOPB ADDRESS

The IOPB Address is a pointer to the IOPB for the command being issued. This value is specified as an offset from the start of short I/O space. Note that IOPBs must be written to the Host Usable Space segment of short I/O. The space used by the IOPB will be released as soon as the Eagle clears the Go/Busy bit in the Master Command Entry Control Register.

COMMAND TAG

The Command Tag is a value that the host uses to distinguish one command from another. The Eagle does not use this value in any way. It simply returns the value during a command response.

COMMAND QUEUE

The Command Queue consists of from 1 to a maximum of 37 Command Queue entries. The number of Command Queue entries is set when the Eagle is initialized (specifically, in the Controller Initialization Block, refer to Chapter 5). As discussed in Chapter 3, only the Transmit and Receive commands are issued through the Command Queue.

Every entry in the Command Queue is identical. The first entry in the queue starts at an offset of +0x01C from the Eagle's short I/O base address.

COMMAND QUEUE ENTRY

A Command Queue entry is 12 bytes long and consists of a Queue Entry Control Register, an IOPB Address, a Command Tag, and a Work Queue Number.

Table 4-6. Command Queue Entry

ELEMENT	SIZE (Bytes)
Queue Entry Control Register	2
IOPB Address	2
Command Tag	4
(Reserved)	1
Work Queue Number	1
(Reserved)	2

QUEUE ENTRY CONTROL REGISTER

This register is used to start the execution of the command.

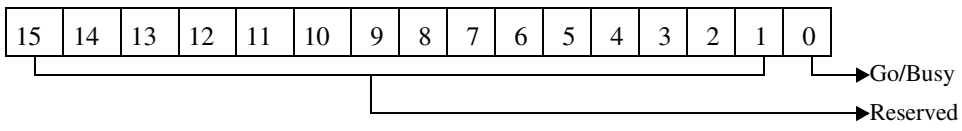


Figure 4-14. Queue Entry Control Register

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Bit 0 Go/Busy:

This bit is set by the host to initiate action on this Command Queue entry. Before this bit is set, an IOPB must be assembled for this entry, and the entire Command Queue must be valid.

Upon determining that the Go/Busy bit is set, the Eagle will move the Command Queue entry and IOPB into internal memory. The Eagle then clears the Go/Busy bit to free up this Command Queue entry for a new command.

Bits 1-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

IOPB ADDRESS

The IOPB Address is a pointer to the IOPB for the command being issued. This value is specified as an off-set from the start of short I/O space. Note that IOPBs are written to the Host Usable Space segment of short I/O. The space used by the IOPB will be freed up as soon as it and the Command Queue entry are moved from short I/O into internal memory. The Eagle indicates that this has occurred by clearing the Go/Busy bit in the Queue Entry Control Register for this Command Queue entry.

COMMAND TAG

The Command Tag is a value that the host uses to distinguish one command from another. It typically is a pointer to a control structure containing a pointer to the data buffer. The Eagle does not use this value in any way. It simply returns the value during a command response.

WORK QUEUE NUMBER

This value specifies which work queue the command is to be sent. If the work queue identified here has not been previously initialized, an error will be posted by the Eagle. Valid Work Queue types for the Command Queue entries are as follows:

Table 4-7. Work Queue Numbers for Use in Command Queue Entries

Work Queue #	Function
0x02	Receive Work Queue
0x03	Transmit Work Queue

HOST USABLE SPACE

The Host Usable Space is typically used to pass IOPBs to the Eagle. No partitioning of this space is implied or required by the interface. The manner in which the space is used is under control of the host. The Eagle has a total of 1812 bytes for dual ported memory to be used for the Command Queue and Host Usable Space.

The amount of Host Usable Space available is determined by the number of Command Queue entries defined when the Eagle is initialized. (The number of Command Queue entries is specified in the Controller Initialization Block, refer to Chapter 5.) For example, if the Command Queue is initialized with ten entries (each Command Queue entry is 12 bytes long), 1692 bytes of Host Usable Space are available.

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Size of Command Queue = number of Command Queue entries * 12
Size of Host Usable Space = 1812 - (size of Command Queue)

Host Usable Space starts at an offset of (0x01C + size of Command Queue) from the Eagle's short I/O base address.

NOTE

Be sure not to confuse "Host Usable Space" with "Host Usable Buffer Region". As discussed above, the former resides in short I/O and is used primarily for passing IOPBs to the Eagle. The latter is located outside short I/O (starting at offset +0xC40000) and is used for gather lists, downloaded software, and other application-specific tasks.

COMMAND RESPONSE BLOCK / RETURNED IOPB

The Command Response Block (CRB) is made up of a Command Response Status Word, Command Tag, and Work Queue Number. It is 12 bytes in length, with 4 bytes of fill between the Command Response Block and the Returned IOPB. The CRB is located at offset +0x0730 from the Eagle's short I/O base address, and the Returned IOPB is located at an offset of +0x0740.

Table 4-8.Command Response Block/Returned IOPB

Offset From Short I/O Base	Element	Size (Bytes)
+0x0730	Command Response Status Word	2
+0x0732	(Reserved)	2
+0x0734	Command Tag	4
+0x0738	IOPB Length*	1
+0x0739	Work Queue Number	1
+0x073A	(Reserved)	2
+0x073C	(Alignment Fill)	4
+0x0740	Returned IOPB	36

* This field is defined only when the Eagle has posted a multiple completion interrupt.

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The purpose of the above fields will vary somewhat depending on whether or not you are implementing multiple completions per interrupt. The field descriptions in the remainder of this section describe the differences between single and multiple command completions. For details on the Initialize Multiple Completions per Interrupt command, refer to Chapter 5.

COMMAND RESPONSE STATUS WORD

The Command Response Status Word describes the nature of the response and contains a handshake bit to synchronize the Eagle and the host.

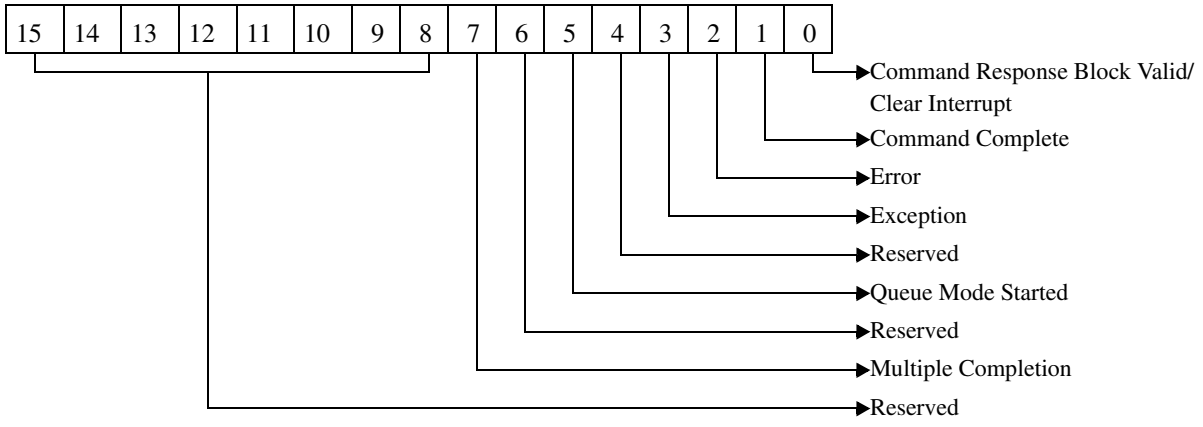


Figure 4-15. Command Response Status Word

The bits in the Command Response Status Word are defined as follows.

Bit 0 Command Response Block Valid/Clear Interrupt:

The Eagle sets this bit after filling in the Returned IOPB, Work Queue number, and Command Tag fields. This bit can also be considered the "Interrupt Pending" bit. The Eagle will keep the entire Command Response Block and Returned IOPB stable while this bit is set.

To clear the interrupt, the host clears this bit after it is finished using the information in the Command Response Block. This action allows the Eagle to present another command response. This bit is the only part of the Command Response Block that the host should modify.

Bit 1 Command Complete:

This bit is set when the Command Response Block is being presented in response to either of the following conditions:

1) Single Completion per Interrupt. A single command has completed, either successfully or with error or exception. Commands may be set for single completion per interrupt globally or on a per-IOPB basis (refer to Chapter 5).

2) Multiple Completions per Interrupt. The Eagle completed a command which was supposed to be grouped with others, but which completed with error or exception.

   If a command completes with error or exception, bit 2 (Error) or bit 3 (Exception) will also be set in the Command Response Status Word.

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Bit 2 Error:

This bit is set along with the Command Complete bit to indicate that the command completed due to an error. The nature of the error can be determined by examining the Returned IOPB. This bit is invalid until the Command Complete bit is set.

Bit 3 Exception:

This bit is set along with the Command Complete bit to indicate that the command completed with some type of exception. The nature of the exception can be determined by examining the Returned IOPB. This bit is invalid until the Command Complete bit is set.

Bit 4 Reserved:

This bit is reserved and will be cleared to 0 by the Eagle.

Bit 5 Queue Mode Started:

The Queue Mode Started bit is set by the Eagle in response to the host setting the Start Queue Mode bit in the Master Control Register. Once the Command and Work Queues have been initialized, the host starts Queued IOPB operation by setting the Start Queue Mode bit in the Master Control Register. The Eagle acknowledges entering Queue Mode by issuing a Command Complete Interrupt with the Queue Mode Started bit set in the Command Response Status Word. The interrupt uses the interrupt vectors specified in the Controller Initialization Block (refer to Chapter 5).

Bit 6 Reserved:

This bit is reserved and will be cleared to 0 by the Eagle.

Bit 7 Multiple Completion:

The Eagle sets this bit to 1 along with bit 0 (CRBV/CI) and bit 5 (QMS) to post a multiple IOPB completion interrupt. All other bits in the Command Response Status Word will be cleared to 0. The IOPB Length field will contain the number of completed commands. The Returned IOPB area will contain a list of completed IOPBs.

Bit 8-15 Reserved:

These bits are reserved and will be cleared to 0 by the Eagle.

COMMAND TAG

Single Completion per Interrupt. This 4-byte field contains the host-assigned Command Tag of the single command which has completed. It is not used or modified in any way by the Eagle.

Multiple Completions per Interrupt. When the Eagle is reporting multiple completions (i.e. bits 0, 5, and 7 of the Command Response Status Word are set), the Command Tag field will be cleared to 0. The tags of the completed commands are instead written to an IOPB list in the Returned IOPB area.

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WORK QUEUE NUMBER

Single Completion per Interrupt. This 2-byte field contains the number of the work queue to which the command was issued. It is provided for systems that allow only one interrupt vector per controller. Such systems can use the returned value for determining which interrupt handler (or portion of an interrupt handler) should process the interrupt.

Multiple Completions per Interrupt. When the Eagle is reporting multiple completions (i.e. bits 0, 5, and 7 of the Command Response Status Word are set), the Work Queue Number field is cleared to 0. The work queue numbers of the completed commands are instead written to an IOPB list in the Returned IOPB area.

IOPB LENGTH

Single Completion per Interrupt. This 1-byte field is UNDEFINED when the Eagle is reporting a single command completion.

Multiple Completions per Interrupt. If the Eagle is reporting a multiple command completion (i.e. bits 0, 5, and 7 of the Command Response Status Word are set), this 1-byte field contains the number of entries in the IOPB list written to the Returned IOPB area. For example, if the IOPB Length field contains 0x3, then there are three items in the IOPB list (where each item corresponds to a completed command). Since each item in the list is 8 bytes long, the total length of the list is 24 bytes.

RETURNED IOPB

Single Completion per Interrupt. This 36-byte field contains an updated copy of the IOPB of the executed command. Eagle-provided fields will contain updated information (as documented in the individual command descriptions in Chapter 5). Host-provided IOPB fields will be returned "as-is".

Multiple Completions per Interrupt. When a multiple completion interrupt is posted, the returned IOPB field contains a list of IOPB completions. Each element in the list is 8 bytes long, and returns the command tag, work queue number, and transfer count. Commands are listed in the same order that they were completed (item 1 completed first).

The format of the list is as follows:

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Table 4-9.List Format of Multiple IOPB Completions

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Bytes
0x740 0x744 0x746	Element 1													Command Tag Work Queue Number Transfer Count			4 2 2
0x748 • • • •														• • • • •			
	Element n													Command Tag Work Queue Number Transfer Count			4 2 2

The meaning of the fields in each IOPB list item is as follows:

- **Command Tag**

  This 4-byte field contains the host-assigned Command Tag of the completed command. It is not used or modified in any way by the Eagle. **Since the list of multiple IOPB completions only returns the command tag, work queue number, and transfer count of each completed IOPB, the command tag must be a pointer to a control structure containing a pointer to the returned data.**

- **Work Queue Number**

  This 2-byte field indicates the number of the work queue to which the command was issued. It is provided for systems that allow only one interrupt vector per controller. Such systems can use the returned value for determining which interrupt handler (or portion of an interrupt handler) should process the interrupt.

- **Transfer Count**

  The value in this 2-byte field equals the number of bytes transferred by the command. In general, the field is useless for all Eagle commands except Receive. (With the Transmit command, for instance, the host already knows how many bytes it has sent.) For a completed Receive command, the value in the Transfer Count field specifies the number of bytes in the received packet.

NOTE

> The list of IOPB completions can extend beyond the Returned IOPB area, overwriting data in the Configuration Status Block or even the Controller Statistics Block. This depends on the value entered in the "Maximum Group Count" field of the Initialize Multiple Completions per Interrupt IOPB. Refer to Chapter 5 for details.

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CONFIGURATION STATUS BLOCK

The Configuration Status Block is used to report the hardware and firmware configuration. It is 72 bytes in length and is located starting at an offset of +0x0764 from the Eagle's short I/O base address. Its format is as follows:

Table 4-10.Configuration Status Block

Offset From Short I/O Base	Element	Size (Bytes)
+0x0764	(Reserved)	1
+0x0765	Product Code	3
+0x0768	(Reserved)	1
+0x0769	Product Variation	1
+0x076A	(Reserved)	1
+0x076B	Firmware Revision Level	3
+0x076E	Firmware Revision Date	8
+0x0776	(Reserved)	12
+0x0782	Size Of Buffer	2
+0x0784	Host Usable Space Offset	4
+0x0788	Receive Buffer Pool Offset	4
+0x078C	Transmit Buffer Pool Offset	4
+0x0790	Available Buffer Memory	4
+0x0794	Current Physical Mode Address	6
+0x079A	Current Logical Address Filter	8
+0x07A2	(Reserved)	10

The following is an explanation of the elements in the Configuration Status Block:

- **Product Code** — The Interphase product code, represented as a three-digit ASCII number. This value is valid after the completion of a controller reset.
- **Product Variation** — The Interphase product variation code, represented as a one-digit ASCII value. This value is valid after the completion of a controller reset.
- **Firmware Revision Level** — The revision level of the installed firmware, represented as a three-digit ASCII value. This value is valid after the completion of a controller reset.
- **Firmware Revision Date** — The revision date of the installed firmware, represented as 8 ASCII

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digits. For example, a release date of January 15, 1989 would be 01_15_89. This value is valid after the completion of a controller reset.

- **Size of Buffer** — The amount of onboard buffer RAM, expressed in 2 Kbyte increments. This value is valid after the completion of a controller reset.
- **Host Usable Space Offset** — The offset from the start of the short I/O space to the start of the Host Usable Space. This value is valid after the completion of a controller reset and will be updated at the completion of an Initialize Controller command.
- **Receive Buffer Pool Offset** — The offset from the start of the onboard buffer RAM to the start of the receive buffer pool. This value is valid after the completion of a controller reset and will be updated at the completion of an Initialize Controller command.
- **Transmit Buffer Pool Offset** — The offset from the start of the onboard buffer RAM to the start of the transmit buffer pool. This value is valid after the completion of a controller reset and will be updated at the completion of an Initialize Controller command.
- **Available Buffer Memory Offset** — The offset from the start of the communications buffer to the start of the Host Usable Buffer Region. This value is valid after the completion of a controller reset and will be updated at the completion of an Initialize Controller command.
- **Current Physical Node Address** — The current physical node address for the Eagle. This value is the default node address at the completion of a controller reset. This value will be updated at the completion of an Initialize Controller command.
- **Current Logical Address Filter** — The current logical address filter for the Eagle. This value is the default logical address filter at the completion of a controller reset and will be updated at the completion of an Initialize Controller command.

CONTROLLER STATISTICS BLOCK

The Controller Statistics Block contains a variety of statistics concerning the transmission/reception of data. By default, these statistics are continuously updated by the Eagle during the idle loop of the firmware. However, these updates are ***disabled*** if the host is using the Multiple Completions Per Interrupt option and has specified a Maximum Group Count greater than 13. This prevents the returned list of IOPB completions from being overwritten by the Controller Statistics Block.

The Controller Statistics Block is 80 bytes in length and is located starting at an offset of +0x07AC from the Eagle's short I/O base address. Its format is shown in Table 4-9.

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Table 4-11.Controller Statistics Block

Element	Size (Bytes)
Number Of Transmit Commands Submitted	4
Number Of Transmit DMA Completions	4
Number Of LANCE Transmit Complete Interrupts	4
Number Of Successful Transmits	4
Number Of Failed Transmits	4
Number Of Transmit Completions Posted To Host	4
Number Of Receive Commands Submitted	4
Number Of Receives Dropped Because Of No Pending Receive Command	4
Number Of LANCE Receive Complete Interrupts	4
Number Of Successful Receives	4
Number Of Failed Receives	4
Number Of Receive DMA Completions	4
Number Of Receive Completions Posted To Host	4
(Reserved)	4
Number Of LANCE Interrupts	4
Number Of LANCE CSR0 Errors	4
Number Of LANCE CSR0 Babble Errors	4
Number Of LANCE CSR0 Collision Errors	4
Number Of LANCE CSR0 Miss Errors	4
Number Of LANCE CSR0 Memory Errors	4

The following is an explanation of the elements in the Controller Statistics Block:

.

- **Number of Transmit Commands submitted** — Total number of attempted frame transmissions (successful or not)
- **Number of Transmit DMA Completions** — Total number of DMA's Completed as a result of a transmit command.
- **Number of LANCE Transmit Complete Interrupts** — Total number of frames transmitted by the LANCE.

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• **Number of Successful Transmits**	Total number of frames successfully transmitted.
• **Number of Failed Transmits**	Total number of unsuccessful frame transmissions.
• **Number of Transmit Completions posted to Host**	Total number of frame transmission completions posted to the Command Response Block and Returned IOPB.
• **Number of Receive Commands submitted**	Total number of attempted message receptions (successful or not).
• **Number of Receives dropped because of no pending Receive command**	Number of frame receptions lost or ignored because the host had no outstanding Receive commands posted to the Eagle.
• **Number of LANCE Receive Complete Interrupts**	Total number of frames received by the LANCE.
•	
• **Number of Successful Receives**	Total number of messages successfully received.
• **Number of Failed Receives**	Total number of messages unsuccessfully received.
• **Number of Receive DMA Completions**	Total number of DMA's completed as a result of a Receive command.
• **Number of Receive Completions posted to Host**	Total number of Receive commands reported to the host via the Returned IOPB in the Command Response Block.
• **Number of LANCE Interrupts**	Total number of attempted LANCE interrupts serviced.
• **Number of LANCE CSR0 Errors**	Total number of Babble, Collision, and Miss errors.
• **Number of LANCE CSR0 Babble Errors**	Total number of times the LANCE reported that the transmitter was on longer than the time required to transmit the longest allowable frame (1519 bytes).
• **Number of LANCE CSR0 Collision Errors**	Total number of times the LANCE reported that its collision detected input did not activate within the allotted time after the completion of a transmission.

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- **Number of LANCE CSR0 Miss Errors** — Total number of times the LANCE reported that it missed a message because it did not have an available buffer in the receive buffer ring.

- **Number of LANCE CSR0 Memory Errors** — Total number of times the LANCE reported a time-out on an internal memory transfer during a transmit operation.

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CHAPTER 5
EAGLE COMMAND SET

OVERVIEW

This chapter provides a detailed description of the Eagle's command set, including the IOPB associated with each command. For additional information on using the commands, please refer to Chapter 3, "Functional Overview."

The commands that the Eagle can execute are listed below, along with the code associated with each command. The code is placed in the first two bytes of the IOPB to indicate which command is to be performed.

Table 5-16. Eagle Command Set

Command Code	Name
0x40	Perform Extended Diagnostics
0x41	Initialize Controller
0x45	Change Default Node Address
0x46	Change Logical Address Filter
0x47	Download Software
0x48	Execute Downloaded Software
0x50	Transmit
0x60	Receive
0x70	DMA
0x80	Initialize Multiple Completions per Interrupt

Please note the following convention regarding the tables which depict the IOPB for each command. **Boldface** type is used to indicate values returned by the Eagle at the completion of the command. IOPB elements which are set in standard type are host-supplied values.

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PERFORM EXTENDED DIAGNOSTICS (0x40)

The Perform Extended Diagnostics command is used to run extended diagnostics on the Eagle. The host issues one Perform Extended Diagnostics IOPB for each test to be performed. This enables the host to specify detailed test parameters such as relative buffer start address, number of execution iterations (size), VMEbus address, etc. It also provides a mechanism for detailed error reporting, as opposed to the pass/fail reporting provided by the power-up diagnostics. For a description of the tests performed during power up, please refer to the section "Power-up Diagnostics" in Chapter 3 of this user's guide.

Extended diagnostics are implemented as sub-commands of a IOPB. This approach gives the host total control over exactly how and which extended diagnostics are executed.

This command is issued through the Master Command Entry. The format of the IOPB for the Perform Extended Diagnostics command is as follows:

Table 5-17. Perform Extended Diagnostics IOPB

Element	Size (Bytes)
Command Code (0x40)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
VMEbus Transfer Options	2
VMEbus Transfer Address	4
Maximum Transfer Size/Memory Size	4
Internal Buffer Offset / **FAILING ADDRESS**	4
Field 1 / CORRECT DATA	4
Field 2 / **INCORRECT DATA**	4
Field 3	2
Starting Pattern	2

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Note that three elements in the above IOPB (the fifth, fourth, and third from the bottom of the table) are issued with host-provided values, but are returned with Eagle-provided values. It is recommended that you use a unique starting pattern each time you run the test.

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HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Perform Extended Diagnostics command:

- **Command Code**

  This field must be set to 0x0040 to execute the Perform Extended Diagnostics command.

- **Command Options**

  This field contains the options for this command. The bits are defined as follows:

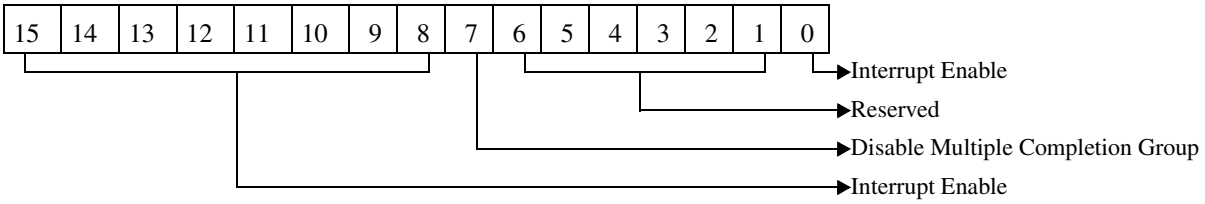


Figure 5-12. Perform Extended Diagnostics Command Options

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bits 1-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

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Bits 8-15 Test Number:

This byte contains the hex code of the test to be performed. The codes are listed below.

CODE	FUNCTION
0x01	Static RAM Address
0x02	Static RAM Extended
0x03	Buffer RAM Address
0x04	Buffer RAM Extended
0x05	Extended Buffer RAM FIFO
0x06	Buffer RAM Refresh
0x07	Event RAM Address
0x08	Event RAM Extended
0x09	Extended Event RAM FIFO
0x0A	Event RAM Refresh
0x0B	NOVRAM (Shadow RAM)
0x0C	Extended LANCE Loopback
0x0D	VMEbus Master
0x0E	VMEbus Slave
0x10	VMEbus ACFAIL*

- **Normal Completion Interrupt Level/Vector**

These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

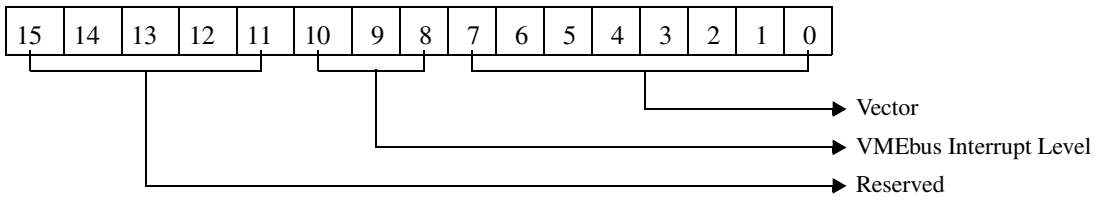


Figure 5-13. Perform Extended Diagnostics Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

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Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed. A level of 0 is allowed only when interrupts are disabled (i.e. bit 0 is cleared in the Command Options word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word and the discussion). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **VMEbus Transfer Options**

These fields allow you to specify the following information:

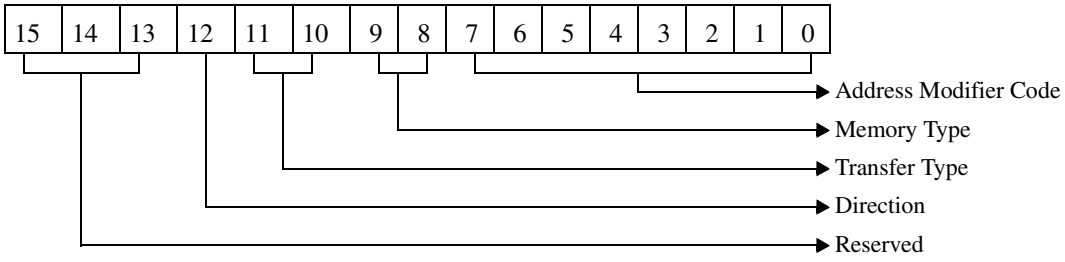


Figure 5-14. VMEbus Transfer Options for Perform Extended Diagnostics Command

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Bits 0-7 Address Modifier Code:

This byte contains the address modifier used by the Eagle for all VMEbus data transfers associated with this command. It is not modified in any way by the Eagle. In accordance with VMEbus specifications, the uppermost two bits of the byte (bits 6 & 7) are not significant. The most commonly used VME address modifiers are listed below for your reference:

CODE	FUNCTION
0x3F	Std. Supervisory Block Transfer
0x3E	" " Program Access
0x3D	" " Data Access
0x3B	" Non-Privileged Block Transfer
0x3A	" " Program Access
0x39	" " Data Access
0x2D	Short Supervisory Access
0x29	" Non-Privileged Access
0x0F	Ext. Supervisory Block Transfer
0x0E	" " Program Access
0x0D	" " Data Access
0x0B	Ext. Non-Privileged Block Transfer
0x0A	" " Program Access
0x09	" " Data Access

Bits 8-9 VMEbus Memory Type:

This two-bit field specifies the width of the VMEbus data transfers. A value of 0x1 specifies 16-bit maximum data width. 0x2 specifies 32-bit maximum. All other values are invalid.

Bits 10-11 VMEbus Transfer Type:

These two bits specify the type of data transfer to be performed. A value of 0 specifies a normal transfer. 0x1 specifies a block mode transfer. 0x2 also specifies block mode but with incrementing addresses disabled. A value of 0x3 is invalid.

Bit 12 Direction:

This bit specifies the direction of the data transfer to be performed. A value of 1 specifies a VMEbus write operation. 0 specifies a VMEbus read operation.

Bits 13-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **VMEbus Transfer Address**

  This field contains the VMEbus address to be used for the transfer.

- **Maximum Transfer Size/Memory Size**

  This field will have one of two meanings, depending upon the test being performed. Normally this field contains the number of bytes that will be transferred across the VMEbus via DMA. This field also indicates the size of the memory block to be tested.

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- **Internal Buffer Offset** (Returned Value: Failing Address)

  This value, provided by the host, specifies the relative starting address of the buffer to be used for the test. On completion, the Eagle writes one of the following to this field: 1) a zero (0) if successful completion, or 2) the memory location where the error was detected.

- **Field 1** (Returned Value: Correct Data)

  Values provided to this field by the host will have several meanings, depending on the test being performed. On error completion, the Eagle provides the correct data that was expected at the memory location returned by the Eagle in the "Failing Address" field.

- **Field 2** (Returned Value: Incorrect Data)

  Values provided to this field by the host will have several meanings, depending on the test being performed. Refer to the test descriptions starting on page. On error completion, the Eagle provides the incorrect data that was read from at the memory location returned by the Eagle in the "Failing Address" field.

- **Field 3**

  Values provided to this field by the host will have one of several meanings, depending on the test being performed.

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- **Starting Pattern**

  This host-provided field indicates the starting pattern in the test that performs increment/decrement algorithms. It is recommended that you use a unique starting pattern each time you run the test.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Perform Extended Diagnostics command.

- **Return Status**

  These two bytes contain the returned status for the Perform Extended Diagnostics command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

- **Failing Address** (Host-Provided Value: Internal Buffer Offset)

  When the host issues the IOPB, it writes the relative starting address of the buffer to be used for the test to this field. On command completion, the Eagle writes one of the following values to this field: 1) a zero (0) if successful completion, or 2) the memory location where the error was detected.

- **Field 1 — Correct Data** (Host-Provided Value: Test Dependent)

  When a test completes with error, the Eagle provides the correct data that was expected at the memory location specified in the "Failing Address" field. The value that was originally written to this field when the IOPB was issued depends on the test being performed. (Refer to the test descriptions below.)

- **Field 2 — Incorrect Data** (Host-Provided Value: Test Dependent)

  When a test completes with error, the Eagle provides the incorrect data that was read from at the memory location specified in the "Failing Address" field. The value that was originally written to this field when the IOPB was issued depends on the test being performed. (Refer to the test descriptions below.)

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EXTENDED DIAGNOSTICS TEST DESCRIPTIONS

The following subsections describe the specific tests which can be executed using the Perform Extended Diagnostics command.

Static RAM Address Test (Test Code # 0x01)

The Static RAM Address Test looks for addressing problems. It will find problems where a small area of memory appears in more than one spot inside of the total memory address space.

This test consists of five phases:

Setup Phase — Zeroes the memory starting at the location specified in the buffer address field of the IOPB for the length specified in the memory size field.

Phase 0 — Verifies memory is clear; then it writes an incrementing byte-wide data pattern using incrementing addresses.

Phase 1 — Verifies the incrementing pattern; then it clears memory using incrementing addresses.

Phase 2 — Verifies memory is cleared; then it writes an incrementing byte-wide data pattern using decrementing addresses.

Phase 3 — Verifies the incrementing pattern; then it clears the memory using decrementing addresses.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Static RAM Address Test are shown in table 5-3.

Table 5-18. Test-Specific IOPB Fields for Static RAM Address Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum Transfer Size; Memory Size	*	
Relative Buffer Address/**FAILING ADDRESS**	*	*
FIELD 1 - CORRECT DATA		*
FIELD 2 - INCORRECT DATA		*
Starting Pattern	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. It is recommended that you use a unique starting pattern each time you run the test.Test-Specific IOPB Fields for Static RAM Address Test

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The following list provides suggested values to use for the test-specific fields shown above for the Static RAM Address Test:

Maximum transfer size = 0x2000
Relative buffer address = 0x17000
Starting pattern = 0

Static RAM Extended Test (Test Code # 0x02)

The Static RAM Extended Test checks the data integrity of the static RAM. It consists of five phases:

Phase 0 — Writes a longword walking ones data pattern in memory starting at the location specified in the buffer address field of the IOPB for the length specified in the memory size field.

NOTE: The size field is converted from number of bytes to number of longwords.

Phase 1 — Writes a longword walking zeros data pattern in memory starting at the location specified in the buffer address field of the IOPB for the length specified in the memory size field.

Phase 2 — Writes a longword 0xAAAAAAAA data pattern in memory starting at the location specified in the buffer address field of the IOPB for the length specified in the memory size field.

Phase 3 — Writes a longword 0x55555555 data pattern in memory starting at the location specified in the buffer address field of the IOPB for the length specified in the memory size field.

Phase 4 — Writes a byte-wide incrementing data pattern in memory starting at the location specified in the buffer address field of the IOPB for the length specified in the memory size field. It uses the starting pattern field of the IOPB as the initial value to verify that the pattern was written correctly.

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The fields in the Perform Extended Diagnostics IOPB which are specific to the Static RAM Extended Test are shown in table 5-4.

Table 5-19. Test-Specific IOPB Fields for Static RAM Extended Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum transfer size; memory size	*	
Relative buffer address/**FAILING ADDRESS**	*	*
CORRECT DATA		*
INCORRECT DATA		*

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

The following list provides suggested values to use for the test-specific fields shown above for the Static RAM Extended Test:

Maximum transfer size = 0x2000
Relative buffer address = 0x17000
Starting pattern = 0

Buffer RAM Address Test (Test Code # 0x03)

The Buffer RAM Address Test looks for addressing problems. It flags situations in which a small area of memory appears in more than one location inside of the total memory address space. The test is identical to the static RAM address test, except that the target hardware is the data buffer RAM.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Buffer RAM Address Test are shown in table 5-5.

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Table 5-20. Test-Specific IOPB Fields for Buffer RAM Address Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum transfer size; memory size	*	
Relative buffer address/**FAILING ADDRESS**	*	*
CORRECT DATA		*
INCORRECT DATA		*
Starting Pattern	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. It is recommended that you use a unique starting pattern each time you run the test.

The following list provides suggested values to use for the test-specific fields shown above for the Buffer RAM Address Test:

Maximum transfer size = 0x2000
Relative buffer address = 0
Starting pattern = 0

Buffer RAM Extended Test (Test Code # 0x04)

The Buffer RAM Extended Test performs a standard memory test suite: walking 0's, walking 1's, alternating bits (0xAA/0x55), and an incrementing data pattern at each byte address. It uses the same algorithm as the static RAM extended test, except that the target hardware is data buffer RAM.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Buffer RAM Extended Test are shown in table 5-6.

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Table 5-21. Test-Specific IOPB Fields for Buffer RAM Extended Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum transfer size; memory size	*	
Relative buffer address/**FAILING ADDRESS**	*	*
CORRECT DATA		*
INCORRECT DATA		*
Starting Pattern	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. It is recommended that you use a unique starting pattern each time you run the test.t

The following list provides suggested values to use for the test-specific fields shown above for the Buffer RAM Extended Test:

Maximum transfer size = 0x2000
Relative buffer address = 0
Starting pattern = 0

Extended Buffer RAM FIFO Test (Test Code # 0x05)

The Extended Buffer RAM FIFO Test is an extended test which verifies the data buffer DRAM's ability to transfer data from the random access port over to the sequential access port and back. Both the read and write capability for all 256 DRAM rows are tested. This test uses the same algorithm as the Quick Buffer RAM FIFO test performed during power up, except that all rows are tested (both as source and destination).

Table 5-22. Test-Specific IOPB Fields for Extended Buffer RAM FIFO Test

ELEMENT	Provided By:	
	Host	**Eagle**
Relative buffer address/**FAILING ADDRESS**	*	
FIELD 1 - CORRECT DATA	*	*
FIELD 2 - INCORRECT DATA	*	
Field 3 - Number Of Rows To Check	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

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The following list provides suggested values to use for the test-specific fields shown above for the Extended Buffer RAM FIFO Test:

Relative buffer address = 0
Field 3 - Number of rows to check = 0x100

Buffer RAM Refresh Test (Test Code # 0x06)

The Buffer RAM Refresh Test first writes an incrementing byte data pattern into the data buffer memory. It then waits a host-specified period of time and verifies the contents of the data buffer against the expected pattern.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Buffer RAM Refresh Test are shown in table 5-8.

Table 5-23. Test-Specific IOPB Fields for Buffer RAM Refresh Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum transfer size; memory size	*	
Relative buffer address/**FAILING ADDRESS**	*	*
Field 3 - Timeout value	*	
Starting pattern	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. It is recommended that you use a unique starting pattern each time you run the test.

The following list provides suggested values to use for the test-specific fields shown above for the Buffer RAM Refresh Test:

Maximum transfer size = 0x2000
Relative buffer address = 0
Field 3 - Timeout value = 0x100
Starting pattern = 0

Event RAM Address Test (Test Code # 0x07)

The Event RAM Address Test looks for addressing problems. It flags situations in which a small area of memory appears in more than one location inside of the total memory address space. The test uses the same algorithm as the Static RAM Address and Buffer RAM Address tests, except that the target hardware is event RAM.

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The fields in the Perform Extended Diagnostics IOPB which are specific to the Event RAM Address Test are as follows:

Table 5-24. Test-Specific IOPB Fields for Event RAM Address Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum transfer size; memory size	*	
Relative buffer address/**FAILING ADDRESS**	*	*
FIELD 1 - CORRECT DATA		*
FIELD 2 - INCORRECT DATA		*

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

The following list provides suggested values to use for the test-specific fields shown above for the Event RAM Address Test:

Maximum transfer size = 0x2000
Relative buffer address = 0

Event RAM Extended Test (Test Code # 0x08)

The Event RAM Extended Test performs the standard memory test suite: walking 0's, walking 1's, alternating bits (0xAA/0x55), and an incrementing data pattern at each byte address. It uses the same algorithm as the Static RAM Extended and Buffer RAM Extended tests, except that the target hardware is event RAM.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Event RAM Extended Test are as follows:

Table 5-25. Test-Specific IOPB Fields for Event RAM Extended Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum transfer size; memory size	*	
Relative buffer address/**FAILING ADDRESS**	*	*
FIELD 1 - CORRECT DATA		*
FIELD 2 - INCORRECT DATA		*

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

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The following list provides suggested values to use for the test-specific fields shown above for the Event RAM Extended Test:

Maximum transfer size = 0x2000
Relative buffer address = 0

Event RAM Extended FIFO Test (Test Code # 0x09)

The Event RAM Extended FIFO Test verifies the data buffer DRAM's ability to transfer data from the random access port over to the sequential access port and back. Both the read and write capability for all 256 DRAM rows are tested. This test uses the same algorithm as the Buffer RAM Extended FIFO test, except that the target hardware is event RAM.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Event RAM Extended FIFO Test are as follows:

Table 5-26. Test-Specific IOPB Fields for Event RAM Extended FIFO Test

ELEMENT	Provided By:	
	Host	**Eagle**
Relative buffer address/failing address	*	
Field 3 - Number of rows to check	*	

The following list provides suggested values to use for the test-specific fields shown above for the Event RAM Extended FIFO Test:

Relative buffer address = 0
Field 3 - Number of rows to check = 0xFE

Event RAM Refresh Test (Test Code # 0x0A)

The Event RAM Refresh Test first writes an incrementing byte data pattern into the data buffer memory. It then waits a host-specified period of time and verifies contents of the data buffer against the expected pattern. This test uses the same algorithm as the Buffer RAM Refresh Test, except that the target hardware is event RAM.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Event RAM Refresh Test are as follows:

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Table 5-27. Test-Specific IOPB Fields for Event RAM Refresh Test

ELEMENT	Provided By:	
	Host	**Eagle**
Maximum Transfer Size; Memory Size	*	
Relative Buffer Address; Failing Address	*	
Field 3 - Timer Value	*	
Starting Pattern	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. It is recommended that you use a unique starting pattern each time you run the test.

The following list provides suggested values to use for the test-specific fields shown above for the Event RAM Refresh Test:

Maximum transfer size = 0x2000
Relative buffer address = 0
Field 3 - Timeout value = 0x100
Starting pattern = 0

NOVRAM Test (Test Code # 0x0B)

The NOVRAM Test verifies the shadow RAM portion of the non-volatile RAM. Every data pattern is verified at every location. The EPROM portion of the NOVRAM is not tested.

The fields in the Perform Extended Diagnostics IOPB which are specific to the NOVRAM Test are shown in table 5-13.

Table 5-28. Test-Specific IOPB Fields for NOVRAM Test

ELEMENT	Provided By:	
	Host	**Eagle**
RELATIVE BUFFER ADDRESS; FAILING ADDRESS		*
FIELD 1 - CORRECT DATA		*
FIELD 2 - INCORRECT DATA		*
Field 3 - Pattern	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. It is recommended that you use a unique starting pattern each time you run the test.

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The following list provides a suggested value to use for the test-specific field shown above for the NOVRAM Test:

Field 3 - Pattern = 0

Extended LANCE Loopback Test (Test Code # 0x0C)

The Extended LANCE Loopback Test verifies the LANCE loopback function, including both internal and external loopback (at the host's option). It provides statistics on: 1) internal result codes, 2) LANCE interrupt flags, 3) LANCE receive message descriptor #1 (RMD1), 4) LANCE receive message descriptor #3 (RMD3), 5) LANCE transmit message descriptor #1 (TMD1), 6) LANCE transmit message descriptor #3 (TMD3), and 7) LANCE control status register #0 (CSR0). In addition, the test validates SIA, external cabling, and transceiver operation.

For information on the external loopback algorithm used for retries on hard LANCE errors, refer to page C-1 in the *Local Network Controller Am7990 Technical Manual* from Advanced Micro Devices.

The fields in the Perform Extended Diagnostics IOPB which are specific to the Extended LANCE Loopback Test are shown in table 5-14.

Table 5-29. Test-Specific IOPB Fields for Extended LANCE Loopback Test

ELEMENT	Provided By:	
	Host	**Eagle**
Loopback Delay; Maximum Transfer Size/Memory Size	*	
Relative Buffer Address; **FAILING ADDRESS**	*	*
Field 1 - **CORRECT ADDRESS**		*
Field 2 - **INCORRECT ADDRESS**		*
Field 3 - Mode; **MOST RECENT VALUE IN CSR0**	*	*
Starting Pattern; **PASSCOUNT**	*	*

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

The following list provides suggested values to use for the test-specific fields shown of the Extended LANCE Loopback Test:

Loopback Delay; Maximum transfer size/memory size – 0x000004
Relative buffer address – 0x04
Mode – either 0x0044 or 0x8004
Starting pattern – 0x0000

The host-provided IOPB fields in are explained below:

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- **Loopback Delay; Maximum Transfer Size/Memory Size**

  The bits in this field are defined as follows
  :

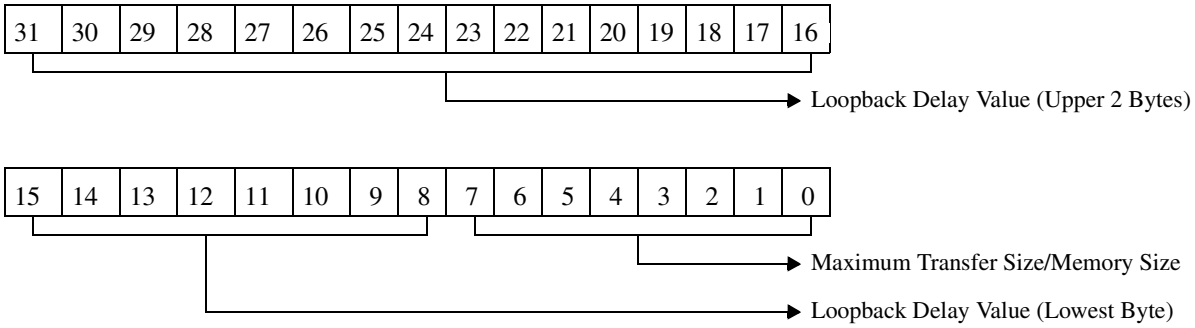


Figure 5-15. Maximum Transfer Size/Memory Size Fields For Extended LANCE Loopback Test

Loopback Delay Value

These three bytes set a delay value in the internal/external loopback tests. The value determines how long the test will wait for a receive interrupt to follow a transmit interrupt prior to returning an external loopback timeout error. (The Eagle flags such an error by setting bit 28 in the Failing Address field of the Returned IOPB. If this field is cleared to 0, a default value of 0x1000 will be used.

Maximum Transfer Size/Memory Size

This byte specifies the LANCE initialization block transmit ring length (TLEN) value. It should contain the number of entries in the transmit ring expressed as a power of two. The current Eagle memory management scheme requires that TLEN be greater than or equal to 4 (i.e., maximum # of entries in transmit ring = 16).

Each time the host issues an Extended Diagnostics IOPB specifying the Extended LANCE Loopback Test, the Eagle transmits/receives *x* number of packets, where *x* is the lesser of TLEN or RLEN (see next field, Relative Buffer Address).

- **Relative Buffer Address**

This 4-byte field specifies the LANCE initialization block receive ring length (RLEN) value. It is the number of entries in the receive ring expressed as a power of two. The current Eagle memory management scheme requires that RLEN be _4 (i.e., maximum # of entries in receive ring = 16).

Each time the host issues an Extended Diagnostics IOPB specifying the Extended LANCE Loopback Test, the Eagle transmits/receives *x* number of packets, where *x* is the lesser of TLEN or RLEN (see previous field).

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- **Field 3: Mode**

  This 2-byte field specifies the LANCE initialization block mode register value. The value placed here is copied verbatim into the LANCE initialization block. Typical values would be:

  — xxxx xxxx x1xx x100 (for internal loopback - suggested test value: 0x0044)

  or

  — 1xxx xxxx x0xx x100 (for external loopback - suggested test value: 0x8004)

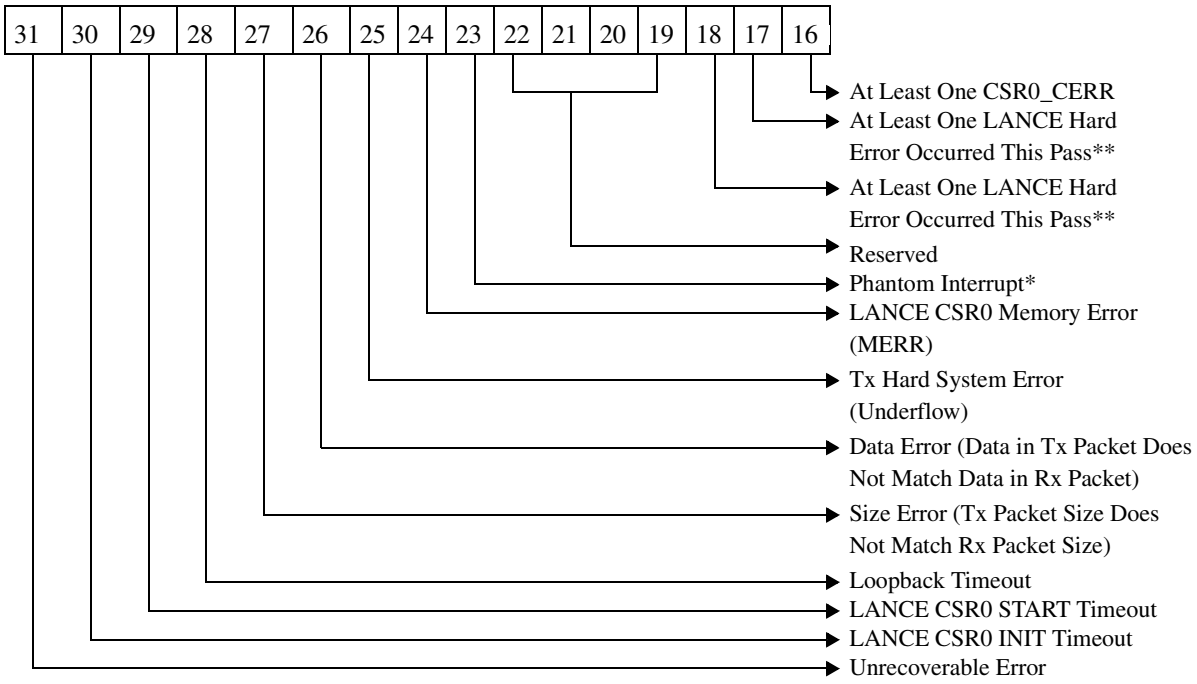
- **Starting Pattern**

  This field specifies an 8-bit seed value used to construct the transmit packets. *It is recommended that you use a unique value in this field each time you run the Extended LANCE Loopback Test.*

The Eagle-provided IOPB fields in table 5-14 are explained below.

- **Failing Address**

  This field contains two sets of values: 1) the Extended LANCE Loopback Test's internal result code in the upper word, and 2) the LANCE interrupt handler flags in the lower word. The upper and lower word of the Failing Address field are depicted in figure 5-5 and figure 5-6.



* Fatal hard system error: no retries attempted

** Fatal hard system error occurs only after 32 attempts have failed

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Figure 5-16. Upper Word Of Failing Address Field For Extended LANCE Loopback Test

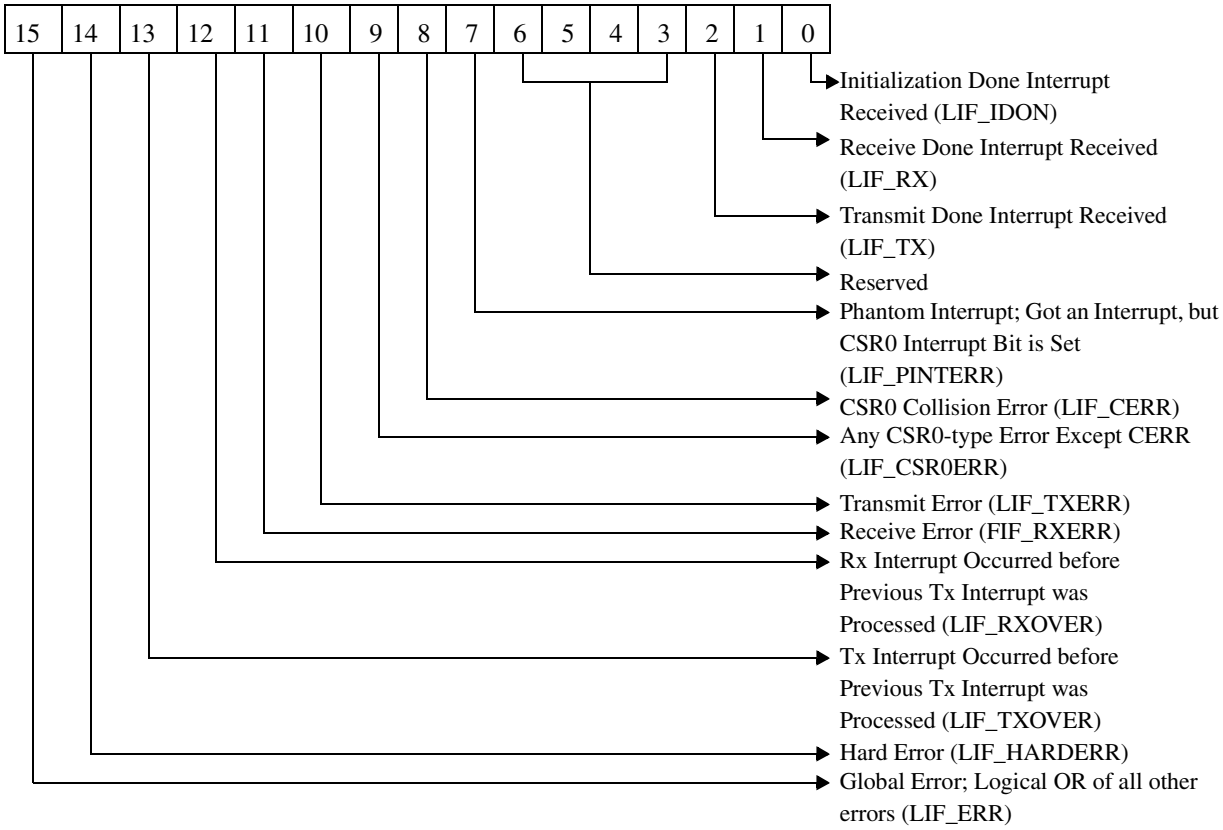


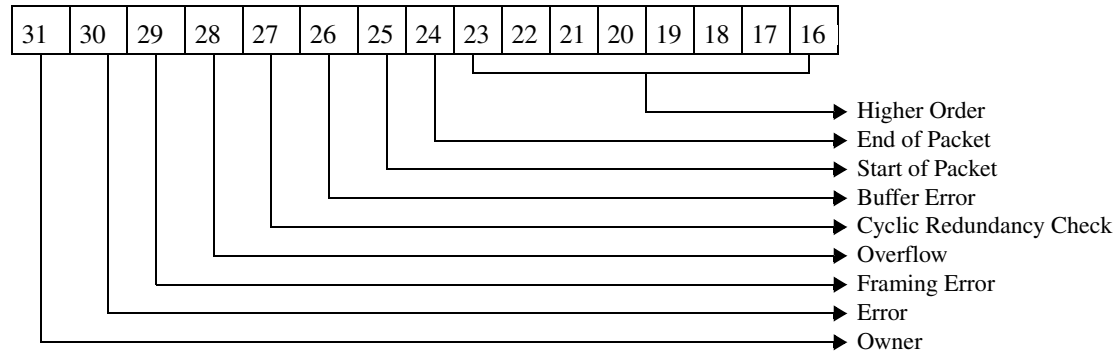
Figure 5-17. Lower Word of Failing Address Field for Extended Diagnostics
LANCE Interrupt Handler Flags

- **Field 1 — Correct Address**

  This field contains two sets of values — the LANCE RMD1 (upper word) and RMD3 (lower word) values captured from the most recent LANCE receive interrupt. These registers are depicted in figure 5-7.

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RMD1:



RMD3

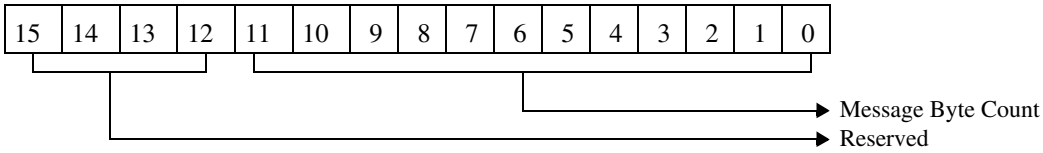
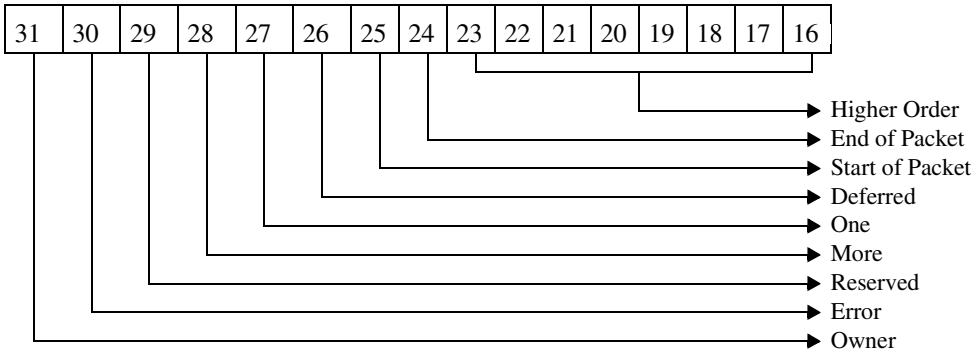


Figure 5-18. Field 1 (LANCE RMD1 & RMD3 Registers)

- **Field 2 — Incorrect Address**

  This field contains two sets of values — the LANCE TMD1 (upper word) and TMD3 (lower word) values captured from the most recent LANCE transmit interrupt. These registers are depicted in figure 5-8.

TMD1:



TMD3:

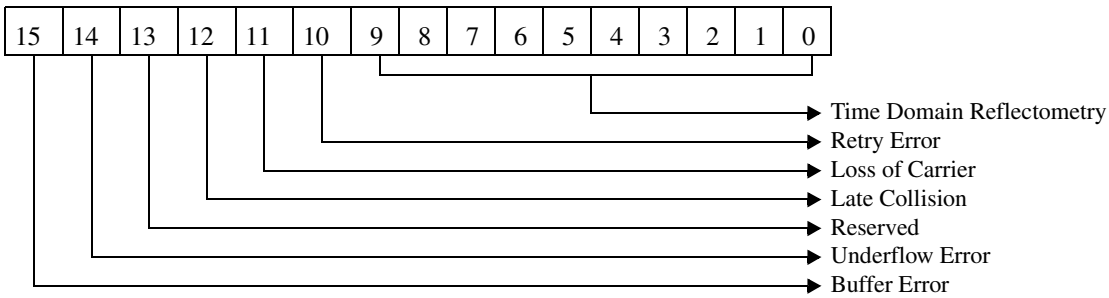


Figure 5-19. Field 2 (LANCE TMD1 & TMD3 Registers)

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- **Field 3 — Most Recent Value in CSR0**

  This field contains the LANCE CSR0 value captured from the most recent LANCE interrupt. This register is depicted in figure 5-9.

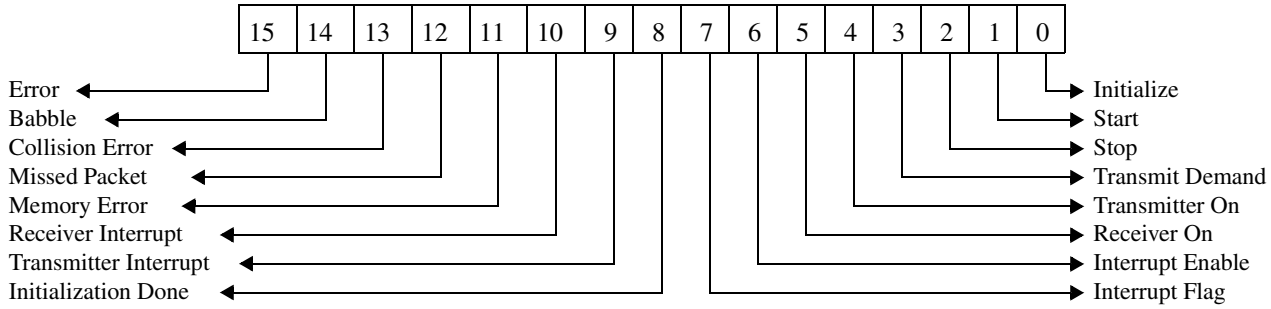


Figure 5-20. LANCE Control Status Register 0 (CSR0)

- **Passcount**

  This field contains the internal passcount value.

  If you successfully run the Extended LANCE Loopback Test using the sample values given on p., your test results will be similar to the following. (**NOTE:** The results may not be identical; check which bits are set in the LANCE registers.)

  Failing Address - 0x00000006
  Field 1 - 0x0380 (RMD1) 0x0020 (RMD3)
  Field 2 - 0x0300 (TMD1) 0xFFFF (TMD3)
  Field 3 - 0x4F3 (CSR0)

VMEbus Master Test (Test Code # 0x0D)

This test checks the Eagle's ability to perform VMEbus DMA transfers by:

— filling buffer RAM with an incrementing byte data pattern
— writing the filled area of buffer RAM to the VMEbus
— clearing the filled area of buffer RAM
— reading the data back from the VMEbus
— comparing the buffer RAM against the incrementing byte data pattern.

A "dead man check" is implemented using the onboard timer, exiting after N timer ticks (where N is specified in "Field 3" of the IOPB). The host uses the "VMEbus Transfer Options" field to specify VMEbus transfer parameters.

The test repeats the actions described above until three nested loops are exhausted. The innermost loop increments the buffer RAM address N times, where N is specified in "Field 1" of the IOPB (the internal address loop counter field). The base buffer RAM address is specified by the IOPB's "Internal Buffer Offset" field.

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The middle loop increments the VMEbus target address N times, with N specified by "Field 2" in the IOPB (the external address loop counter field). The base VMEbus address is specified by the "VMEbus Transfer Address" field.

The outer loop increments the VMEbus transfer size from 1 to M, where M is specified by the "Maximum transfer size; memory size" field.

Invalid internal/external address combinations are automatically skipped over.

Normal Eagle DMA operation includes presenting and handling processor level 4 autovectored interrupts. It also detects VMEbus transfers terminated with error (BERR*). Although this test does not explicitly check BERR* response, it can be checked by passing the test a VMEbus address that will force a BERR* response.

The fields in the Perform Extended Diagnostics IOPB which are specific to the VMEbus Master Test are as follows:

Table 5-30. Test-Specific IOPB Fields for VMEbus Master Test

ELEMENT	Provided By:	
	Host	**Eagle**
VMEbus Transfer Options	*	
VMEbus Transfer Address	*	
Maximum Transfer Size; Memory Size	*	
Internal Buffer Offset/**FAILING ADDRESS**	*	*
FIELD 1 - CORRECT DATA		*
FIELD 2 - INCORRECT DATA		*
Field 3 - Timer	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

The following list provides suggested values to use for the test-specific fields shown above for the VMEbus Master Test:

VMEbus transfer options = 0x22D
VMEbus transfer address =the short I/O base address of your board
Maximum transfer size = 0x750
Internal buffer offset = 0
Field 3 - Timer = 0x100

VMEbus Slave Test (Test Code # 0x0E)

This test checks the word or longword wide availability of the upper 2K of buffer RAM via VMEbus short I/O address space. This is done by using the Eagle's DMA facilities as the VMEbus master.
The IOPB's Relative Buffer Address field points to the free buffer RAM to be used as work space. The first 2K of this space is used to save an image of short I/O accessible memory contents for restoration after the

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DMA write/read operations are completed. The second 2K of this space is used to build the data packet prior to DMA write operations. It is also used to hold data fetched via DMA read operations.
The fields in the Perform Extended Diagnostics IOPB which are specific to the VMEbus Slave Test are shown in table 5-16.

Table 5-31. Test-Specific IOPB Fields for VMEbus Slave Test

ELEMENT	Provided By:	
	Host	Eagle
VMEbus Transfer Options	*	
VMEbus Transfer Address	*	
Maximum Transfer Size; Memory Size	*	
Internal Buffer Offset/**FAILING ADDRESS**	*	*
FIELD 1 - CORRECT DATA		*
FIELD 2 - INCORRECT DATA		*
Field 3 - Timer	*	

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided.

The following list provides suggested values to use for the test-specific fields shown above for the VMEbus Slave Test:

VMEbus transfer options = 0x22D
VMEbus transfer address = the short I/O base address of your board
Maximum transfer size = 0x750
Internal buffer offset = 0
Field 3 - Timer = 0x100

VMEbus ACFAIL* Test (Test Code # 0x10)

This test verifies the Eagle's monitoring of the VMEbus ACFAIL* signal, 68020 interrupt level #7 request, and 68020 autovector #7 operation. This test assumes that the Eagle's host has control over the VMEbus ACFAIL* signal. The fields in the Perform Extended Diagnostics IOPB which are specific to the VMEbus ACFAIL* Test are shown in table 5-17.

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Table 5-32. Test-Specific IOPB Fields for VMEbus ACFAIL Test

ELEMENT	Provided By:	
	Host	**Eagle**
Field 3	*	

The following list provides a suggested value to use for the test-specific field shown above for the VMEbus ACFAIL* Test:

Field 3 = 0x100

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INITIALIZE CONTROLLER (0x41)

The Initialize Controller command is used to configure the Eagle for use in a particular system. It must be issued before the Eagle will engage in any activity on the network.

This command is issued through the Master Command Entry. The format of the IOPB for the Initialize Controller command is as follows:

Table 5-33. Initialize Controller IOPB

Element	Size (Bytes)
Command Code (0x41)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
(Reserved)	2
Controller Initialization Block Offset	4
(Reserved)	20

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Initialize Controller command.

- **Command Code**

  This field must be set to 0x0041 to execute the Initialize Controller command.

- **Command Options**

  This field contains the options for this command. The bits are defined in figure 5-10.

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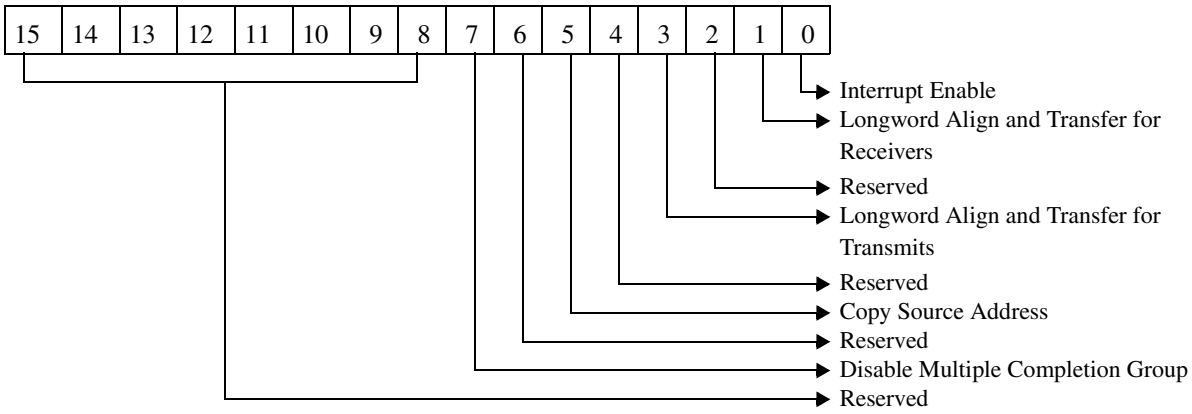


Figure 5-21. Initialize Controller Command Options

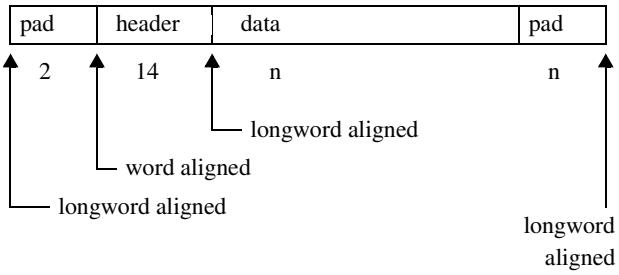
Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bit 1 Longword Align And Transfer For Receives:

Setting this bit to '1' enables the 4207 to longword-align received packets so that they can be uploaded to the host using 32-bit transfers. To align the data, the 4207 forces a 2-byte prefix pad, followed by the 14-byte MAC header, the packet data, and a suffix pad which rounds up the packet size to the next longword boundary. This format is shown below:



All host VME receive buffers must be longword aligned in order for this feature to work. Longword aligning the host receive buffers allows all the data to be uploaded using 32-bit transfers. Any other setup will cause all or a portion of the packet to be transferred as 16-bit transfers, which will double the VMEbus bandwidth required to DMA the packet and therefore decrease performance.

Bit 2 Reserved:

This bit is reserved and must be cleared to 0 by the host.

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Bit 3 Longword Align And Transfer For Transmits:

Setting this bit to '1' enables the Eagle to perform system reads in longword mode (32 bit), regardless of the buffer word alignment. When the system buffer starts on an odd-word boundary, the actual transfer address is backed up to the previous longword address, the transfer count is internally bumped by a word, and the internal LANCE buffer address is adjusted to ignore the extra data which was transferred.
Clearing Bit 3 disables this feature, causing the VME transfer mode to be dictated by the starting address of the system buffer. In this case, odd-word-aligned buffers cause the Eagle to perform D16 type VME transfers, even if the command specifies 32-bit transfers.

IMPORTANT NOTE

If GATHER Transmits are utilized, the Longword align will work for Element 1. Elements 2 through N must be Longword-Aligned and be of Longword-Multiple length if strictly 32-bit. VME transfers are desired.

Bit 4 Reserved:

This bit is reserved and must be cleared to 0 by the host.

Bit 5 Copy Source Address:

This bit is used to help troubleshoot error 0x40 (Receive Error). Setting this bit enables the Eagle to report the address of the node which sent the bad packet. This information is written to the Source Node Address field of the returned Receive IOPB.

Bit 6 Reserved:

This bit is reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.

(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bits 8-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The

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fields are defined as follows:

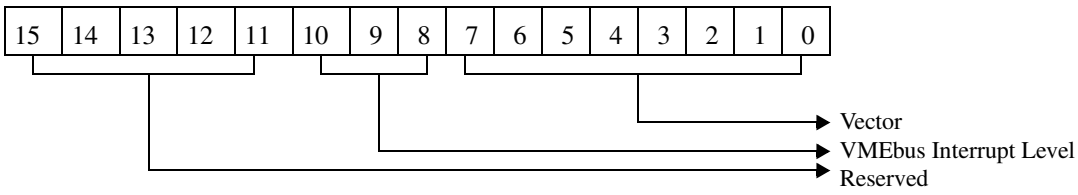


Figure 5-22. Initialize Controller Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Options word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word and the discussion). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **Controller Initialization Block Offset**

  This value is the offset from the start of the short I/O space to the start of the Controller Initialization Block.

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CONTROLLER INITIALIZATION BLOCK FORMAT

The following table shows the format of the Controller Initialization Block. It is followed by a description of each Controller Initialization Block element. Off board (in VME system Memory) Responses are enabled by specifying non-zero values in the CIB's ECRB and transfer option fields.

Table 5-34. Controller Initialization Block

Element	Size (Bytes)
(Reserved)	1
Number of Command Queue Entries	1
Network Options	2
(Reserved)	2
Physical Node Address	6
Address Filter	8
(Reserved)	10
Completion Interrupt Level	1
Completion Interrupt Vector	1
Error Interrupt Level	1
Error Interrupt Vector	1
DMA Burst Count	2
DMA Burst Timeout	2
ECRB Transfer Options	2
ECRB VME Address	4

NOTE
Reserved elements must be cleared to 0 by the host.

- **Number of Command Queue Entries**

  The number of entries in the Command Queue. The maximum number of Command Queue entries is 37.

- **Network Options**

  The bits are defined as shown in figure 5-12.

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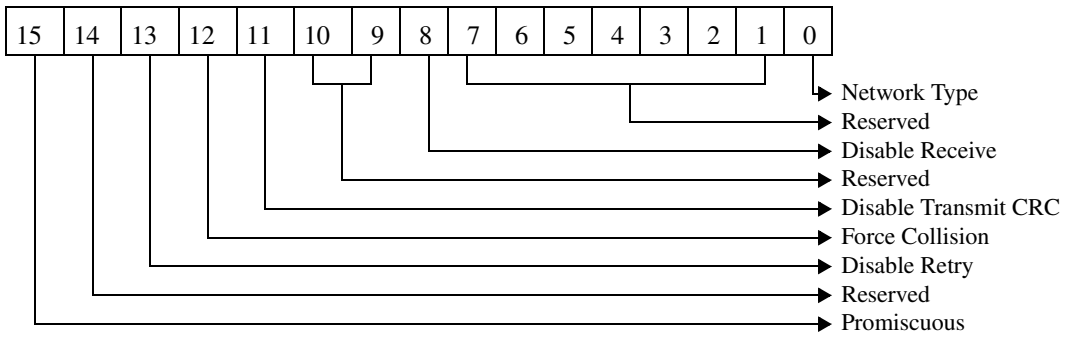


Figure 5-23. Initialize Controller Network Options

Bit 0 Network Type:

(1) Ethernet
NOTE: This bit must be set to 1.

Bits 1-7 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 8 Disable Receive:

This bit is presented as bit 0 of the LANCE initialization block mode register. It should be cleared to 0 for normal board operation. This bit is used for testing purposes only. If it is set, the Eagle will be unable to receive incoming packets.

Bits 9-10 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 11 Disable Transmit CRC:

This bit is presented as bit 3 of the LANCE initialization block mode register.

Bit 12 Force Collision:

This bit is presented as bit 4 of the LANCE initialization block mode register.

Bit 13 Disable Retry:

This bit is presented as bit 5 of the LANCE initialization block mode register.

Bit 14 Reserved:

This bit is reserved and must be cleared to 0 by the host.

Bit 15 Promiscuous Mode:

This bit is presented as bit 15 of the LANCE initialization block mode register.

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To enable promiscuous mode, reset the board using bit 12 of the Master Control Register. Then, issue the Initialize Controller command with the Promiscuous Mode bit set to '1'.

To disable promiscuous mode, reset the board again and re-initialize the controller with the Promiscuous Mode bit cleared to '0'.

- **Physical Node Address**

  Unique 48-bit network node address assigned to the Eagle. The least significant bit must be cleared to 0.

- **Address Filter**

  A 64-bit mask used by the LANCE for decoding multicast addressing. See the AMD 7990 LANCE data sheet for details. Also refer to Appendix B for a sample multicast address program written in C.

- **Completion Interrupt Level/Vector**

  These bytes specify the VMEbus interrupt level and interrupt vector used to notify the host of normal command completion not associated with a specific IOPB. The fields are defined as follows:

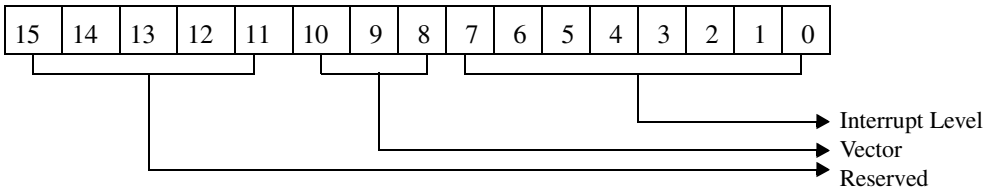


Figure 5-24. Initialize Controller Command Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Options word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Interrupt Level/Vector**

  These two bytes specify the VMEbus interrupt level and interrupt vector used to notify the host of a controller error condition not associated with a specific IOPB (such as an bus error or LANCE TX shutdown). The fields are defined the same as the Controller Command Completion Interrupt Level and Vector bytes (above).

- **DMA Burst Count**

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This value determines the number of VMEbus DMA transfers the Eagle will perform in a single burst before releasing the bus and requesting it. (Size is specified by the mode word.) If this value is 0, then the entire message will be transferred in a single burst.

- **DMA Bus Timeout**

  This value is the number of milliseconds the controller will wait for bus grant after requesting the bus during a DMA transfer.

- E**CRB Transfer Options**

  The DMA mode word used by the firmware to post completions to the ECRB in host memory.

- **ECRB VME Address**

  A non-zero value will cause the firmware to DMA the CRB (except for the CRSW) and the RIOPB to host memory at this address upon normal completion. The true CRSW will still be posted in the Short I/O offset +0x730.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Initialize Controller command:

- **Return Status**

  These two bytes contain the returned status for the Initialize Controller command. Any non-zero value indicates an error code. A list of valid error codes appears in Chapter 7.

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CHANGE DEFAULT NODE ADDRESS (0x45)

The Change Default Node Address command is used to instruct the Eagle to change the physical node address currently specified in the Controller Initialization Block. It optionally allows you to update the physical node address stored in the Eagle's non-volatile RAM. For your protection, the NOVRAM also contains a write-protected copy of the factory-assigned node address. The Change Default Node Address command can be used to restore the factory-assigned address from this backup copy, if necessary.

The Change Default Node Address command can also be used to reinitialize the LANCE using the updated Controller Initialization Block.

This command is issued through the Master Command Entry. The format of the IOPB for the Change Default Node Address command is as follows:

Table 5-35. Change Default Node Address IOPB

Element	Size (Bytes)
Command Code (0x45)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
(Reserved)	10
Physical Node Address	6
(Reserved)	10

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Change Default Node Address command.

- **Command Code**

  This field must be set to 0x0045 to execute the Change Default Node Address command.

- **Command Options**

  This field contains the options for this command. The bits are defined as follows:

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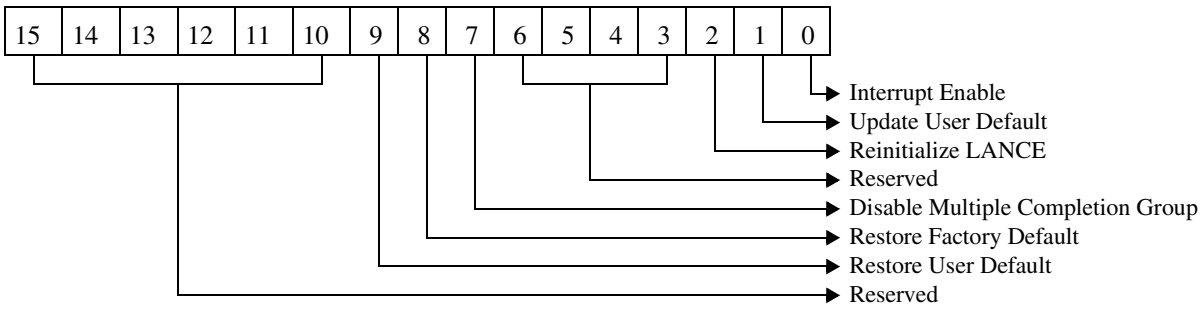


Figure 5-25. Change Default Node Address Command Options

Bit 0 Interrupt Enable

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bit 1 Update User Default:

(1) Change the user default physical node address stored in NOVRAM to the value specified in the IOPB's "Physical Node Address" field.
(0) Do not change the user default node address stored in NOVRAM.

NOTE: The value originally stored in NOVRAM as the user default is the factory-assigned physical node address.

Bit 2 Reinitialize LANCE:

(1) Reinitialize the LANCE using the updated Controller Initialization Block, which will contain the updated physical node address.
(0) Do not the reinitialize the LANCE.

Bits 3-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

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Bit 8 Restore Factory Default:

(1) Change node address to the factory-assigned default stored in NOVRAM.
(0) Do not change node address to the factory-assigned address.

Bit 9 Restore User Default:

(1) Change node address to the user-assigned default stored in NOVRAM.
(0) Do not change node address to the user-assigned address.

Bits 10-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

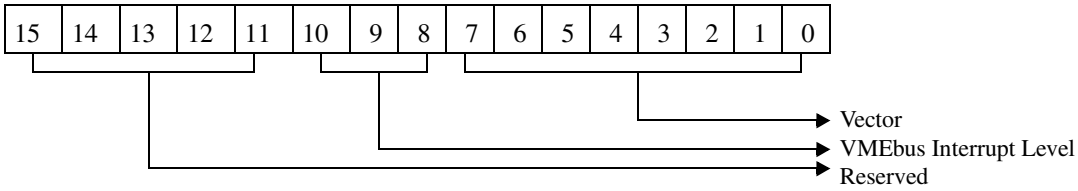


Figure 5-26. Change Default Node Address Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Options word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled. The bit definition is the same as for the Normal Completion Interrupt Level/Vector fields (described above).

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- **Physical Node Address**

  A unique 48-bit network node address written to non-volatile RAM. The least significant bit of this address must be cleared to 0.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Change Default Node Address command.

- **Return Status**

  These two bytes contain the returned status for the Change Default Node Address command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

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CHANGE LOGICAL ADDRESS FILTER (0x46)

The Change Default Logical Address Filter command is used to instruct the Eagle to change the logical address filter currently specified in the Controller Initialization Block. It optionally allows you to update the default logical address filter stored in the Eagle's non-volatile RAM.

The command can also be used to reinitialize the LANCE using the updated Controller Initialization Block.

This command is issued through the Master Command Entry. The format of the IOPB for the Change Default Logical Address Filter command is as follows:

Table 5-36. Change Default Logical Address Filter IOPB

Element	Size (Bytes)
Command Code (0x46)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
(Reserved)	10
Logical Address Filter	8
(Reserved)	8

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Change Default Logical Address Filter command.

- **Command Code**

  This field must be set to 0x0046 to execute the Change Default Logical Address Filter command.

- **Command Options**

  This field contains the options for this command. The bits are defined in figure 5-16.

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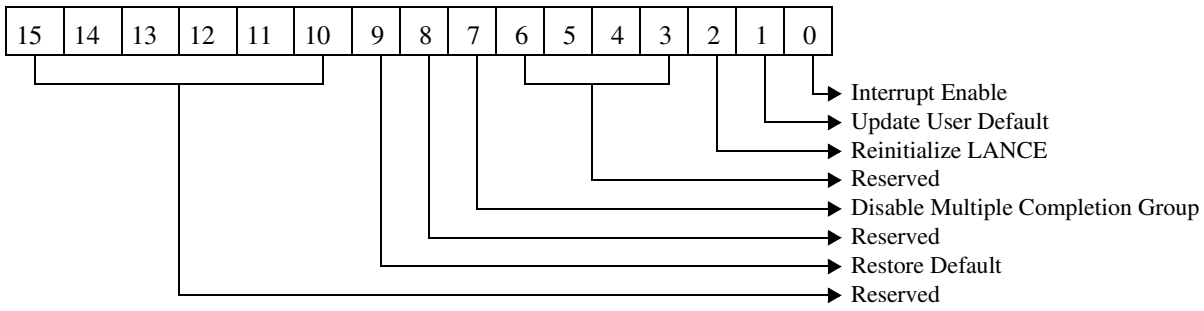


Figure 5-27. Change Logical Address Filter Command Options

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bit 1 Update User Default:

(1) Change the logical address filter stored in NOVRAM to the value specified in the IOPB's "Logical Address Filter" field.
(0) Do not change the logical address filter stored in NOVRAM.

NOTE: The value originally stored in NOVRAM as the logical address filter is zero (0).

Bit 2 Reinitialize LANCE:

(1) Reinitialize the LANCE using the updated Controller Initialization Block, which will contain the updated logical address filter.
(0) Do not the reinitialize the LANCE.

Bits 3-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bit 9 Restore User Default:

(1) Change logical address filter to the user-assigned default stored in NOVRAM (see discussion for bit 1).
(0) Do not change logical address filter to the value stored in NOVRAM.

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Bits 10-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

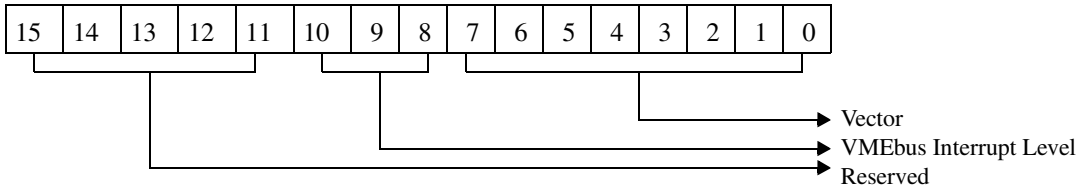


Figure 5-28. Change Logical Address Filter Normal Completion Fields

Bit 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Options word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **Logical Address Filter**

  A 64-bit address written to non-volatile RAM. This address is used by the LANCE for decoding multicast addressing. See the LANCE data sheet from the AMD 7990 Data Book. Also refer to Appendix B for a sample multicast program written in C.

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EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Change Default Logical Address Filter command.

- **Return Status**

  These two bytes contain the returned status for the Change Default Logical Address Filter command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

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DOWNLOAD SOFTWARE (0x47)

The Download Software command is used to download executable code into the Eagle's communications buffer using DMA transfers. A download may also be done by mapping in Eagle buffer memory and copying the executable code to the buffer memory at the target execution address.

The downloaded code must be a binary image, PC relative, or absolute linked at the target execution address. The target execution address must be specified in terms of the Eagle physical address. The downloaded code is responsible for: 1) relocation of code or data to static memory, 2) masking unwanted interrupts, and 3) patching the vector table.

It is important to remember that the Eagle firmware also makes use of buffer memory. To avoid damaging the Eagle data structures when downloading code, you will need to determine a safe target code buffer. A typical target address would be in the Host Usable Buffer Region (which is also used for host-managed gather lists, if any). Refer to the memory map of the communications buffer in Chapter 3 to determine a safe target code buffer.

This command is issued through the Master Command Entry. The format of the IOPB for the Download Software command is shown in table 5-22.

Table 5-37. Download Software IOPB

Element	Size (Bytes)
Command Code (0x47)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
VMEbus Address	4
VMEbus Buffer Address	4
Transfer Size	4
VMEbus Transfer Options	2
(Reserved)	12

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

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HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Download Software command.

- **Command Code**

  This field must be set to 0x0047 to execute the Download Software command.

- **Command Options**

  This field contains the options for this command. The bits are defined as follows:

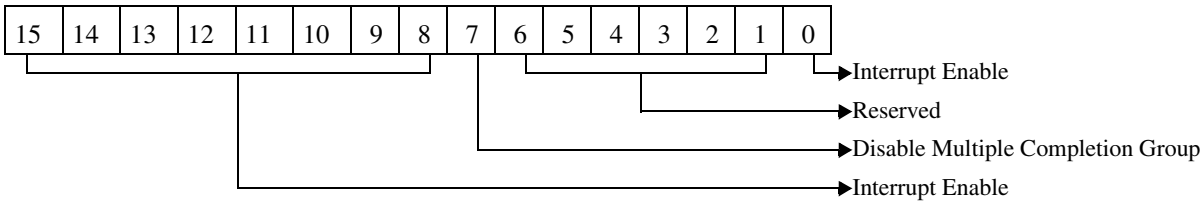


Figure 5-29. Download Software Command Options

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bits 1-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bits 8-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VME interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined in figure 5-19.

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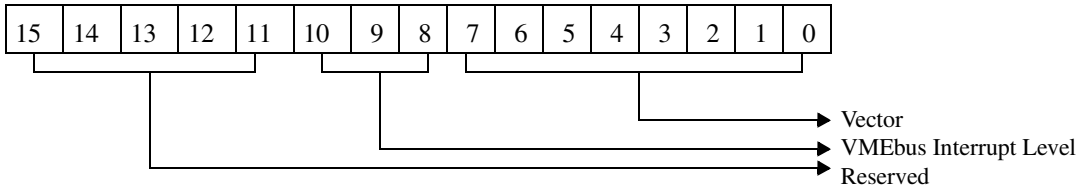


Figure 5-30. Download Software Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Option word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word.). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **VMEbus Address**

  This value is the VMEbus address for the start of the data.

- **VMEbus Buffer Address**

  This field contains the communications buffer origin (0xC40000).

- **Transfer Size**

  This value is the size of the data transfer in bytes.

- **VMEbus Transfer Options**

  These fields allow you to specify the following information:

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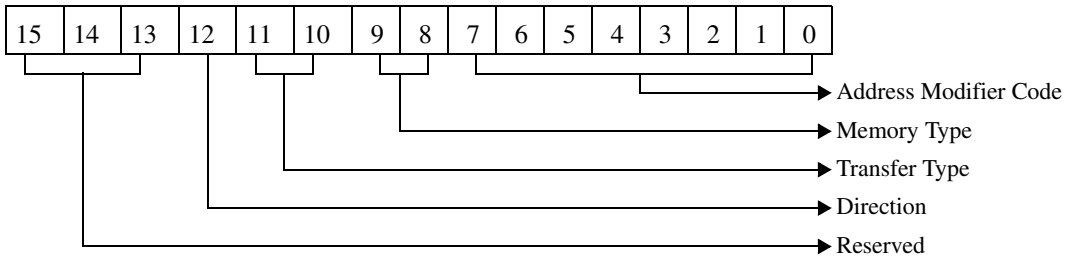


Figure 5-31. VMEbus Transfer Options for Download Software Command

Bits 0-7 Address Modifier Code:

This byte contains the address modifier used by the Eagle for all VMEbus data transfers associated with this command. It is not modified in any way by the Eagle. In accordance with VMEbus specifications, the uppermost two bits of the byte (bits 6 & 7) are not significant. The most commonly used VME address modifiers are listed below for your reference:

CODE	FUNCTION
0x3F	Std. Supervisory Block Transfer
0x3E	" " Program Access
0x3D	" " Data Access
0x3B	" Non-Privileged Block Transfer
0x3A	" " Program Access
0x39	" " Data Access
0x2D	Short Supervisory Access
0x29	" Non-Privileged Access
0x0F	Ext. Supervisory Block Transfer
0x0E	" " Program Access
0x0D	" " Data Access
0x0B	Ext. Non-Privileged Block Transfer
0x0A	" " Program Access
0x09	" " Data Access

Bits 8-9 VMEbus Memory Type:

This two-bit field specifies the width of the VMEbus data transfers. A value of 0x1 specifies 16-bit maximum data width. 0x2 specifies 32-bit maximum. All other values are invalid.

Bits 10-11 VMEbus Transfer Type:

These two bits specify the type of data transfer to be performed. A value of 0 specifies a normal transfer. 0x1 specifies a block mode transfer. 0x2 also specifies block mode, but with incrementing addresses disabled. A value of 0x3 is invalid.

Bit 12 Direction:

This bit specifies the direction of the data transfer to be performed. A value of 1 specifies a VMEbus write operation. 0 specifies a VMEbus read operation.

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Bits 13-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Download Software command.

- **Return Status**

  These two bytes contain the returned status for the Download Software command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

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EXECUTE DOWNLOADED SOFTWARE (0x48)

The Execute Downloaded Software command is used to start execution of previously downloaded code. It does so by modifying the program counter of the Eagle's processor (68020), causing it to branch to the specified target execution address. The firmware will acknowledge this command before turning control over to the downloaded code. It is the responsibility of the downloaded code to assume all control of the Eagle. Interrupts must be masked to level seven. Processed interrupt vectors must be loaded with the execution address of downloaded interrupt handlers to prevent conflict.

This command is issued through the Master Command Entry. The format of the IOPB is as follows:

Table 5-38. Execute Downloaded Software IOPB

Element	Size (Bytes)
Command Code (0x48)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
(Reserved)	2
Target Execution Address	4
(Reserved)	20

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Execute Downloaded Software command.

- **Command Code**

  This field must be set to 0x0048 to execute the command.

- **Command Options**

  This field contains the options for this command. The bits are defined in figure 5-21.

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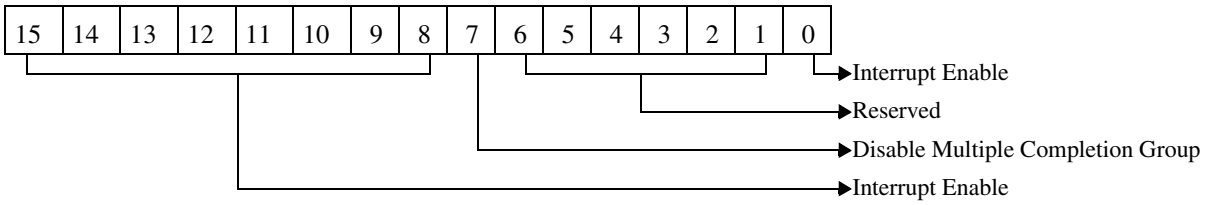


Figure 5-32. Execute Downloaded Software Command Option

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bits 1-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bits 1-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

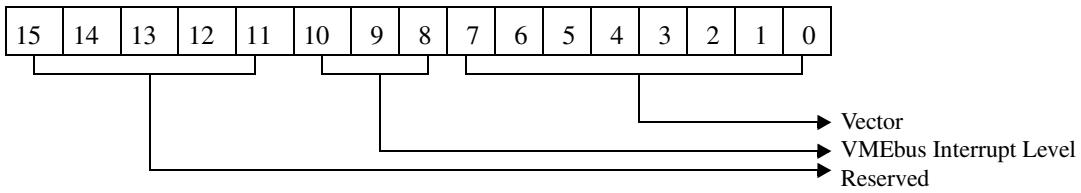


Figure 5-33. Download Software Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

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Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Option word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **Target Execution Address**

  This field contains the offset between the start of the communications buffer and the start of the data.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Execute Downloaded Software command.

- **Return Status**

  These two bytes contain the returned status for the Execute Downloaded Software command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

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TRANSMIT (0x50)

The Transmit command provides the Eagle with the necessary information to transmit a message over the network. The maximum number of pending Transmit IOPBs is 16.

This command is issued through the Command Queue to the Transmit Work Queue. The format of the IOPB for the Transmit command is as follows:

Table 5-39. Transmit IOPB

Element	Size (Bytes)
Command Code (0x50)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
VMEbus Transfer Options	2
VMEbus Transfer Address or Gather List Offset	4
DMA Transfer Size	4
(Reserved)	10
Gather Entry Count	2
LANCE TRANSMIT DESCRIPTOR WORD 1	2
LANCE TRANSMIT DESCRIPTOR WORD 3	2

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Transmit command.

- **Command Code**

  This field must be set to 0x0050 to execute the Transmit command.

- **Command Options**

  This field contains the options for this command. The bits are defined as follows:

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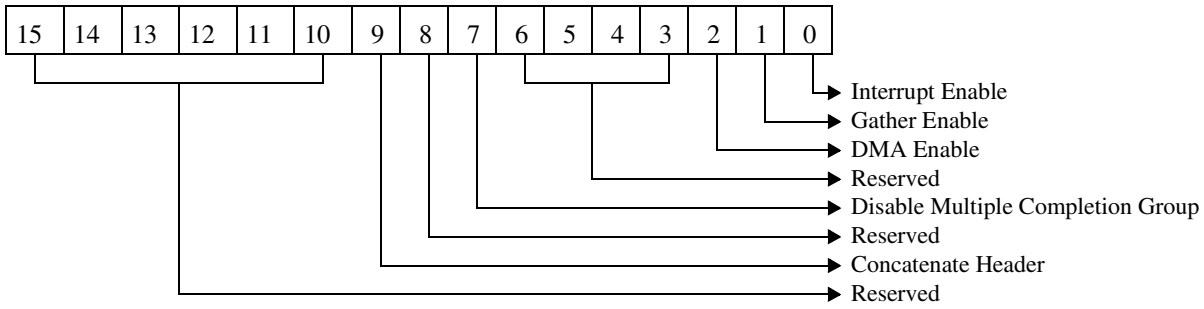


Figure 5-34. Transmit Command Options

Bit 0 Interrupt Enable:

(1) Interrupt host on the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bit 1 Gather Enable:

(1) Gather enabled.
(0) Gather disabled.

The Gather bit is significant only when the DMA bit is set.

Bit 2 DMA Enable:

(1) Read data from VMEbus prior to transmission.
NOTE: This bit must be set to 1.

Bits 3-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bit 8 Reserved:

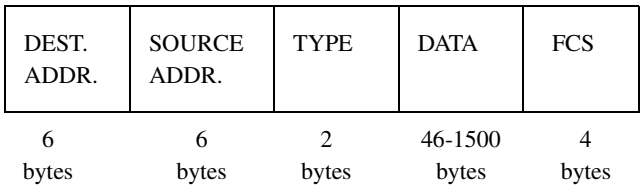
This bit is reserved and must be cleared to 0 by the host.

Bit 9 Concatenate Header:

(1) This bit setting is not defined and should not be used.
(0) DMA/buffer data already contains MAC layer header.

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The MAC (medium access control) header layout is shown below. For additional information, please refer to the LANCE specifications.



Bits 10-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined in figure 5-24.

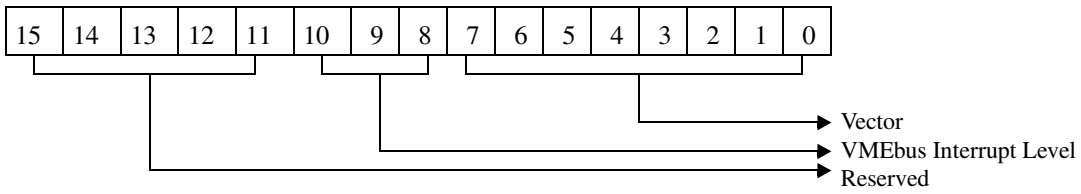


Figure 5-35. Transmit Command Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Option word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of Command Completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word). The bit definition is the same as for the Normal Completion Interrupt

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Level/Vector word (described above).

- **VMEbus Transfer Options**

  This field specifies the VMEbus memory type, transfer type and address modifier codes used for all VMEbus transfers associated with the command. The bits are defined as follows:

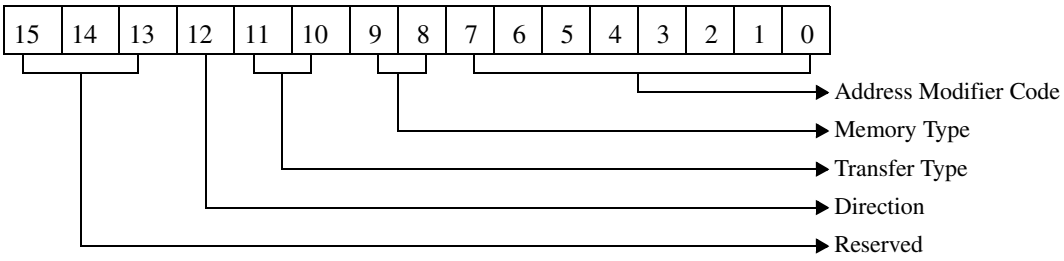


Figure 5-36. Transmit Command VMEbus Transfer Options

Bits 0-7 Address Modifier Code:

This byte contains the address modifier used by the Eagle for all VMEbus data transfers associated with this command. It is not modified in any way by the Eagle. In accordance with VMEbus specifications, the uppermost two bits of the byte (bits 6 & 7) are not significant. The most commonly used VMEbus address modifiers are listed below for your reference:

CODE	FUNCTION
0x3F	Std. Supervisory Block Transfer
0x3E	" " Program Access
0x3D	" " Data Access
0x3B	" Non-Privileged Block Transfer
0x3A	" " Program Access
0x39	" " Data Access
0x2D	Short Supervisory Access
0x29	" Non-Privileged Access
0x0F	Ext. Supervisory Block Transfer
0x0E	" " Program Access
0x0D	" " Data Access
0x0B	Ext. Non-Privileged Block Transfer
0x0A	" " Program Access
0x09	" " Data Access

Bits 8-9 VMEbus Memory Type:

This two-bit field specifies the width of the VMEbus data transfers. A value of 0x1 specifies 16-bit maximum data width. 0x2 specifies 32-bit maximum. All other values are invalid.

Bits 10-11 VMEbus Transfer Type:

These two bits specify the type of data transfer to be performed. A value of 0 specifies a normal transfer. 0x1 specifies a block mode transfer. 0x2 also specifies block mode, but with incrementing addresses disabled. A value of 0x3 is invalid.

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Bit 12 Direction:

This bit should always be cleared to 0. It specifies the direction of the data transfer to be performed. Thus it should be cleared to indicate a VMEbus read operation.

Bits 13-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **VMEbus Transfer Address or Gather List Offset**

  The meaning of this field depends on the type of data transfer to be performed. If a DMA transfer is required (where Command Options DMA bit = 1), this field contains the VMEbus address for the start of the data. If a gather operation is required (where Command Options Gather bit = 1), this field contains the offset between the start of the buffer memory and the Gather list.

- **DMA Transfer Size**

  This is the number of bytes to be moved via DMA. It also specifies frame length.

- **Gather Entry Count**

  The number of entries in the Gather specification list.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Transmit command.

- **Return Status**

  These two bytes contain the returned status for the Transmit command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

- **LANCE Transmit Descriptor Word 1**

  The values from word 1 of the LANCE transmit message descriptor are returned here (see the LANCE data sheet from the AMD 7990 Data Book for details). In the case of an error, the values returned here will be from the buffer which encountered the error.

- **LANCE Transmit Descriptor Word 3**

  The values from word 3 of the LANCE transmit message descriptor are returned here (see the LANCE data sheet from the AMD 7990 Data Book for details). In the case of an error, the values returned here will be from the buffer which encountered the error.

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RECEIVE (0x60)

The Receive command is used to receive a network packet. Once the network packet has been received and the appropriate action taken with the data (depending on the Receive IOPB fields), the Receive IOPB will be updated with the status from the operation and copied to the Command Response Block/Returned IOPB. Optionally, the Eagle may generate an interrupt to the host.

As noted in Chapter 3, it is advisable to prepost a number of Receive IOPBs in order not to miss any incoming packets. As a rule of thumb, you should have 16 "hanging" Receive IOPBs at all times, once the Eagle has gone into Queue Mode.

The 4207 can receive packets up to 4 Kbytes in size, even though the maximum legal packet size in the Ethernet IEEE 802.3 specifications is 1518 bytes (1500 bytes plus 14 bytes of header and 4 bytes of checksum).

The Receive command is issued through the Command Queue to the Receive Work Queue. The format of the IOPB for the Receive command is as follows:

Table 5-40. Receive IOPB

Element	Size (Bytes)
Command Code (0x60)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
VMEbus Transfer Options	2
VMEbus Transfer Address	4
Maximum Transfer Size / **ACTUAL TRANSFER SIZE**	4
(Reserved)	2
PACKET TYPE/LENGTH FIELD	2
SOURCE NODE ADDRESS	6
(Reserved)	2
LANCE RECEIVE MESSAGE DESCRIPTOR 1	2
LANCE RECEIVE MESSAGE DESCRIPTOR 3	2

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

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HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Receive command.

- **Command Code**

  This field must be set to 0x0060 to execute the Receive command.

- **Command Options**

  This field contains the options for this command. The bits are defined as follows:

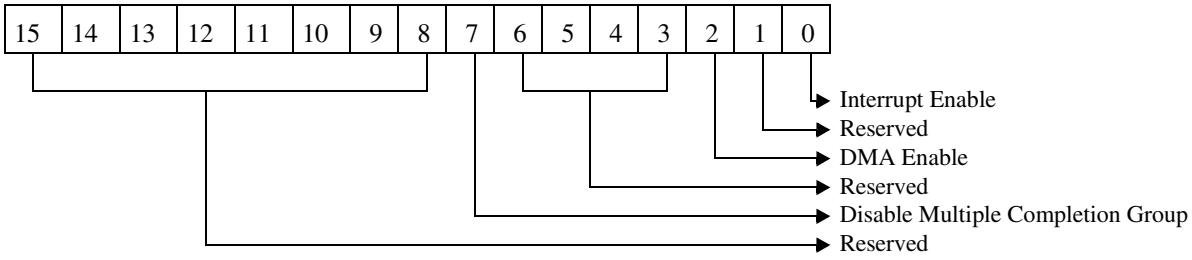


Figure 5-37. Receive Command Options

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bit 1 Reserved:

This bit is reserved and must be cleared to 0 by the host.

Bit 2 DMA Enable:

(1) Write data to VMEbus after reception.
NOTE: This bit must be enabled.

Bits 3-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

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Bits 8-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

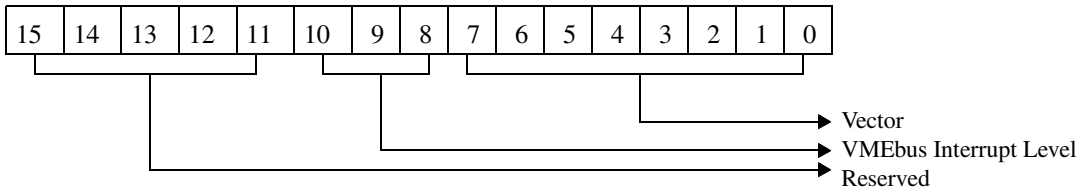


Figure 5-38. Receive Command Normal Completion Fields

Bits 0-7 Vector:

This byte is the vector used for a command complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a command complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Option word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **VMEbus Transfer Options**

  This field specifies the VMEbus memory type, transfer type and address modifier codes used for all VMEbus transfers associated with the command. The bits are defined as follows:

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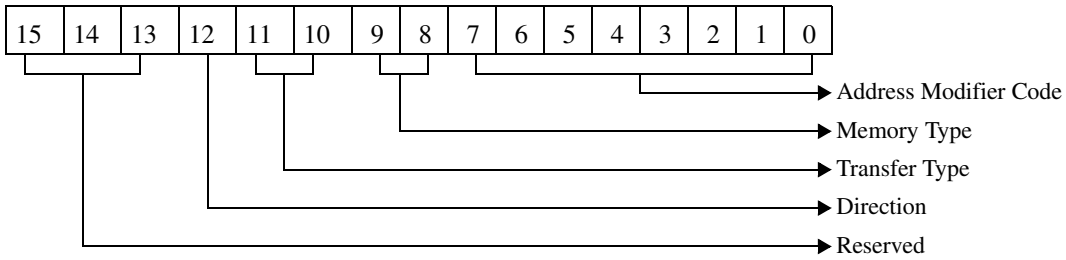


Figure 5-39. Receive Command VMEbus Transfer Options

Bits 0-7 Address Modifier Code:

This byte contains the address modifier used by the Eagle for all VMEbus data transfers associated with this command. It is not modified in any way by the Eagle. The most commonly used VMEbus address modifiers are listed below for your reference:

CODE	FUNCTION
0x3F	Std. Supervisory Block Transfer
0x3E	" " Program Access
0x3D	" " Data Access
0x3B	" Non-Privileged Block Transfer
0x3A	" " Program Access
0x39	" " Data Access
0x2D	Short Supervisory Access
0x29	" Non-Privileged Access
0x0F	Ext. Supervisory Block Transfer
0x0E	" " Program Access
0x0D	" " Data Access
0x0B	Ext. Non-Privileged Block Transfer
0x0A	" " Program Access
0x09	" " Data Access

Bits 8-9 VMEbus Memory Type:

This two-bit field specifies the width of the VMEbus data transfers. A value of 0x1 specifies 16-bit maximum data width. 0x2 specifies 32-bit maximum. All other values are invalid.

Bits 10-11 VMEbus Transfer Type:

These two bits specify the type of data transfer to be performed. A value of zero specifies a normal transfer. 0x1 specifies a block mode transfer. 0x2 also specifies block mode but with incrementing addresses disabled. A value of 0x3 is invalid.

Bit 12 Direction:

This bit should always be set to 1. It specifies the direction of the data transfer to be performed. It should be set to indicate a VMEbus write operation.

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Bits 13-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **VMEbus Transfer Address**

  This value is the VMEbus address for the start of the data.

- **Maximum Transfer Size**

  This field specifies the maximum number of bytes that can be transferred across the VMEbus via DMA. It should be filled in with the maximum packet size you expect to receive. In most applications, this value is 1518 (i.e. the maximum legal packet size as defined in the IEEE 802.3 Ethernet specifications). However, as noted previously, the 4207 is capable of receiving packets up to 4 Kbytes in length.

  In the returned IOPB, the 4207 overwrites this field with the actual size of the received packet.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Receive command.

- **Return Status**

  These two bytes contain the returned status for the Receive command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

- **Actual Transfer Size**

  This is the number of bytes DMA'ed over to the host receive buffer. It overwrites the value placed by the host in the Maximum Transfer Size field. To extract the size of the received packet, examine the returned LANCE Receive Message Descriptor 1 (RMD1) field.

- **Type/Length**

  This value is the Message Type field in the frame header when operating on an Ethernet compatible network. On an IEEE 802.3 network, this value is the Length field in the frame header.

- **Source Node Address**

  The Eagle fills in this field only if both of the following conditions are met: 1) Error code 0x40 (Receive Error) occurs, and 2) the Copy Source Address feature is enabled. If these conditions are met, the field contains the address of the node which sent the bad packet. (Ordinarily, the host can extract the source node address from the received packet itself.)

  To enable the Copy Source Address feature, the host must initialize the Eagle with bit 5 in the Command Options Word of the Initialize Controller command set to '1'.

- **LANCE Receive Message Descriptor 1**

  This contains a copy of the LANCE Receive Message Descriptor 1 (RMD1). In the case of a multiple buffer receive, these values will be from the last buffer. In the case of a receive error, these values will be from the buffer on which the error was encountered.

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- **LANCE Receive Message Descriptor 3**

  This contains a copy of the LANCE Receive Message Descriptor 3 (RMD3). In the case of a multiple buffer receive, these values will be from the last buffer. In the case of a receive error, these values will be from the buffer on which the error was encountered.

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DMA (0x70)

The DMA command is used for miscellaneous tasks requiring DMA data transfers in or out of the 512K communications buffer. Note that this command is *not* required in order to send or receive packets via DMA. In general, the Transmit and Receive IOPBs (with DMA enabled) serve this purpose. (If desired, however, the DMA command can be used to repetitively DMA large numbers of packets off the Eagle to load up the bus.)

This command is issued through the Master Command Entry. The format of the IOPB for the DMA command is as follows:

Table 5-41. DMA IOPB

Element	Size (Bytes)
Command Code (0x70)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
VMEbus Address	4
VMEbus Buffer Address	4
DMA Transfer Size	4
VMEbus Transfer Options	2
(Reserved)	12

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the DMA command.

- **Command Code**

  This field must be set to 0x0070 to execute the DMA command.

- **Command Options**

  This field contains the options for this command. The bits are defined as follows:

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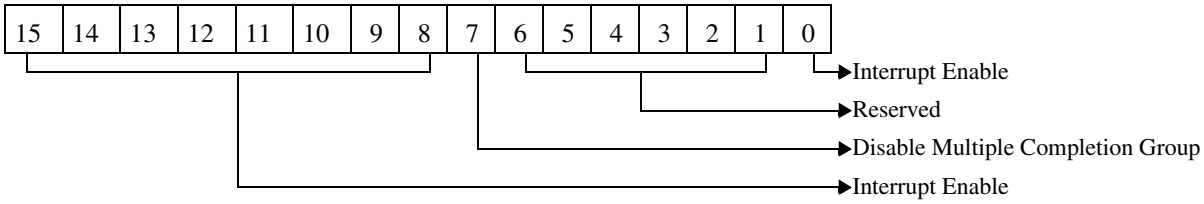


Figure 5-40. DMA Command Options

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion.

Bits 1-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bits 8-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

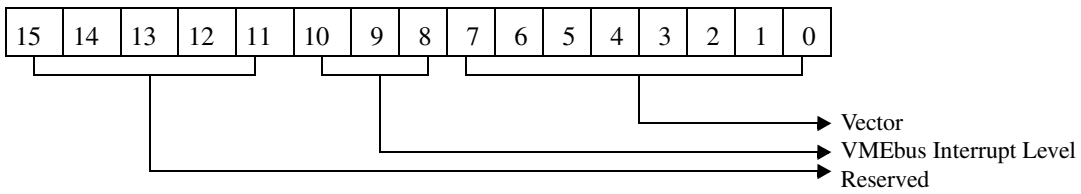


Figure 5-41. DMA Command Normal Completion Fields

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Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Option word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word and the discussion on p.). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **VMEbus Address**

  This value is the VMEbus address for the start of the data.

- **VMEbus Buffer Address**

  This field contains the communications buffer origin (0xC00000).

- **DMA Transfer Size**

  This value is the size of the data transfer in bytes.

- **VMEbus Transfer Options**

  These fields allow you to specify the following information:

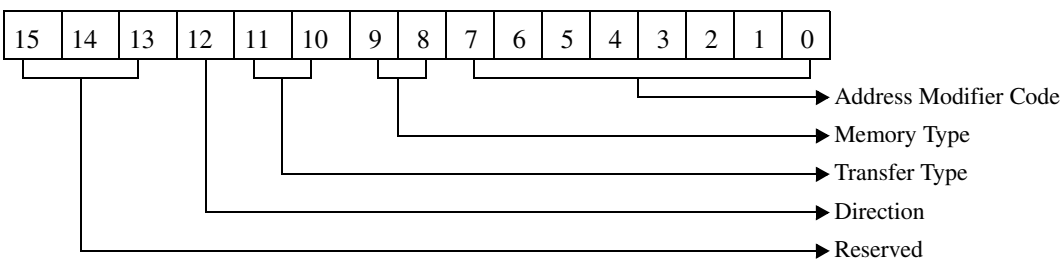


Figure 5-42. VMEbus Transfer Options for DMA Command

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Bits 0-7 Address Modifier Code

This byte contains the address modifier used by the Eagle for all VMEbus data transfers associated with this command. It is not modified in any way by the Eagle. In accordance with VMEbus specifications, the uppermost two bits of the byte (bits 6 & 7) are not significant. The most commonly used VME address modifiers are listed below for your reference:

CODE	FUNCTION
0x3F	Std. Supervisory Block Transfer
0x3E	" " Program Access
0x3D	" " Data Access
0x3B	" Non-Privileged Block Transfer
0x3A	" " Program Access
0x39	" " Data Access
0x2D	Short Supervisory Access
0x29	" Non-Privileged Access
0x0F	Ext. Supervisory Block Transfer
0x0E	" " Program Access
0x0D	" " Data Access
0x0B	Ext. Non-Privileged Block Transfer
0x0A	" " Program Access
0x09	" " Data Access

Bits 8-9 VMEbus Memory Type:

This two-bit field specifies the width of the VMEbus data transfers. A value of 0x1 specifies 16-bit maximum data width. 0x2 specifies 32-bit maximum. All other values are invalid.

Bits 10-11 VMEbus Transfer Type:

These two bits specify the type of data transfer to be performed. A value of 0 specifies a normal transfer. 0x1 specifies a block mode transfer. 0x2 also specifies block mode, but with incrementing addresses disabled. A value of 0x3 is invalid.

Bit 12 Direction:

This bit specifies the direction of the data transfer to be performed. A value of 1 specifies a VMEbus write operation. 0 specifies a VMEbus read operation.

Bits 13-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the DMA command.

- **Return Status**

  These two bytes contain the returned status for the DMA command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

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INITIALIZE MULTIPLE COMPLETIONS PER INTERRUPT (0x80)

Initialize Multiple Completions Per Interrupt allows the Eagle to post multiple command completions to the Command Response Block before interrupting the host. Interrupts can optionally be disabled, in which case the host must poll bit 0 of the Command Response Status Word to determine if the Eagle has posted additional command completions.

IMPORTANT NOTE

> Your V/Ethernet 4207 Eagle controller must have firmware revision level ***0760-A19-00D or later*** in order to support the Multiple Completions per Interrupt feature. This enhanced firmware is completely backwards compatible with previous versions of the board. The 4207 reports its firmware revision level in the Configuration Status Block.

You can specify the maximum number of commands to be grouped together. However, the Eagle will post whatever it has completed as soon as the host acknowledges that it has processed the last batch. (The host acknowledges this by clearing bit 0 of the Command Response Status Word.)

When the Eagle posts a multiple completion, the Command Response Block and Returned IOPB areas have a significantly different format than they do when posting a single command completion. For example, the Returned IOPB area will contain a list of IOPB completions instead of a returned IOPB. IOPBs appear in the list in the same order that they were completed. Refer to Chapter 4 for details on the Command Response Block/Returned IOPB areas.

Once the host has enabled multiple completions, it is still possible to post individual completed commands. In fact, any of the following conditions will cause a command to be posted in the "single completion per interrupt" format:

— Bit 7 (Disable Multiple Completion Group) is set to 1 in the Command Options word of the individual IOPB.

— The IOPB completed with error or exception. Such commands will posted in the "single completion per interrupt" format, regardless of how bit 7 in the Command Options word is set.

— The Eagle only has one completed command ready to post when the host clears bit 0 of the Command Response Status Word.

When a group of commands is posted as a multiple completion, the Eagle ignores the interrupt enable bit and interrupt level/vector specified in the individual IOPBs. Instead, it uses the following interrupt-related settings in the Initialize Multiple Completions per Interrupt IOPB:

- The multiple completion interrupt is enabled/disabled using bit 1 of the Control Flags field.
- The interrupt level and vector used by the multiple completion interrupt is set in the Group Interrupt Level and Vector fields.

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On the other hand, if the IOPB is posted as a single completion, the Eagle will use whatever interrupt parameters are set in the individual IOPB.

This command must be issued AFTER Queue Mode is started. It is issued through the Master Command Entry.

In most applications, the host will typically: 1) initialize the controller, 2) start Queue Mode, 3) issue the Initialize Multiple Completions per Interrupt IOPB, and then 4) proceed to issue Transmit and Receive commands as needed.

The format of the Initialize Multiple Completions per Interrupt IOPB is as follows:

Table 5-42. Multiple Completions per Interrupt IOPB

Element	Size (Bytes)
Command Code (0x80)	2
Command Options	2
RETURN STATUS	2
Normal Completion Level	1
Normal Completion Vector	1
Error Completion Level	1
Error Completion Vector	1
(Reserved)	2
Control Flags	2
Group Interrupt Level	1
Group Interrupt Vector	1
Minimum Group Count	2
Maximum Group Count	2
(Reserved)	16

NOTE

Elements set in bold capital letters are returned values. All other values are host-provided. Reserved elements must be cleared to 0 by the host.Initialize

HOST-PROVIDED IOPB FIELDS

The following information must be provided in the IOPB for the Initialize Multiple Completions per Interrupt command.

- **Command Code**

  This field must be set to 0x0080 to execute the Initialize Multiple Completions per Interrupt command.
- **Command Options**

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This field contains the options for this command. The bits are defined in.

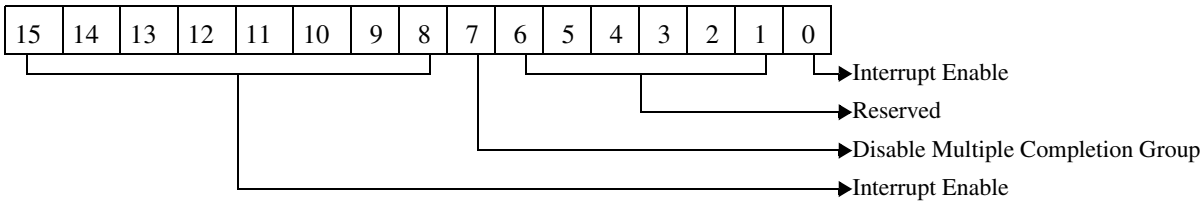


Figure 5-43. Command Options For Initialize Multiple Completions Per Interrupt

Bit 0 Interrupt Enable:

(1) Interrupt host upon the command's completion.
(0) Do not interrupt host upon the command's completion.

This bit is ignored if the command is posted as a multiple completion. Like all other Eagle commands, the Initialize Multiple Completions per Interrupt command cannot be posted as a multiple completion unless the host has already issued an Initialize MCI command to enable multiple completions.

Bits 1-6 Reserved:

These bits are reserved and must be cleared to 0 by the host.

Bit 7 Disable Multiple Completion Group:

(1) Disable this command from being grouped with others upon completion. Post in the "single completion per interrupt" format.
(0) Post this command in the "multiple completions per interrupt" format if multiple completions are enabled. If multiple completions are not enabled, the command will be posted in the "single completion per interrupt format". It will also be posted as a single completion if multiple completions are enabled but certain conditions occur.

Bits 8-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Normal Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of a normal command completion. **These values are ignored if the command is posted as a multiple completion.** The fields are defined as follows:

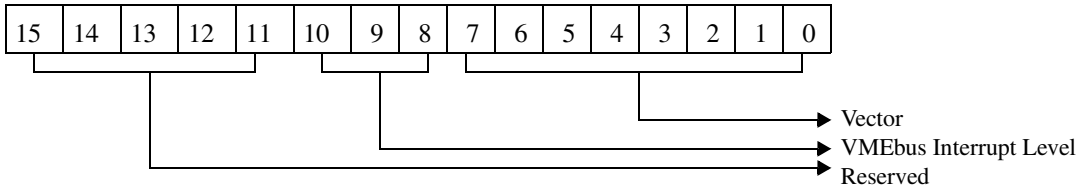


Figure 5-44. Normal Completion Fields For Initialize Multiple Completions Per Interrupt

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Bits 0-7 Vector:

This byte is the vector used for a Command Complete interrupt when no errors occur in the command processing.

Bits 8-10 VMEbus Interrupt Level:

These three bits set the interrupt level used by the Eagle to present a Command Complete interrupt to the VMEbus. Values of 0 through 7 are allowed, but a level of 0 is allowed only when interrupts are disabled (Interrupt Enable bit = 0 in the Command Option word).

Bits 11-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

- **Error Completion Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host of command completion with error. These values will be used even if the multiple completion feature is enabled (see bit 7 of the Command Options word). The bit definition is the same as for the Normal Completion Interrupt Level/Vector word (described above).

- **Control Flags**

  This 2-byte field sets *global* options for the multiple completions feature (as opposed to the Command Options word, which sets options for this command only).

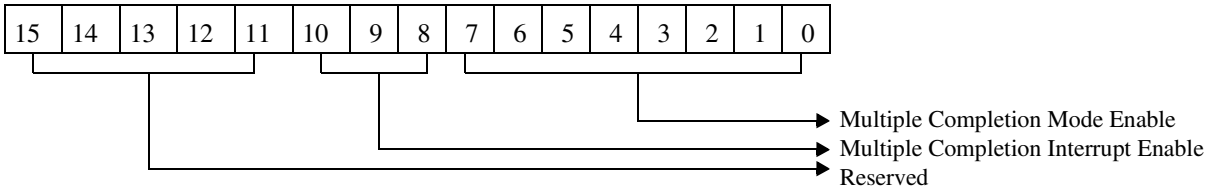


Figure 5-45. Control Flags For Multiple Completions Per Interrupt IOPB

Bit 0 Multiple Completion Mode Enable:

(1) Enable the posting of multiple command completions.
(0) Disable the posting of multiple completions.

This bit is used to turn the multiple completion feature on and off.

Bit 1 Multiple Completion Interrupt Enable:

(1) Interrupt the host after posting a multiple command completion. The Eagle will use the interrupt level and vector supplied in the Group Interrupt Level/Vector fields, described below.
(0) Do not interrupt the host after posting a multiple completion.

Bits 2-15 Reserved:

These bits are reserved and must be cleared to 0 by the host.

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- **Group Interrupt Level/Vector**

  These fields specify the VMEbus interrupt level and interrupt vector used to notify the host that the Eagle has posted a multiple completion to the Command Response Block. These values override the interrupt level/vector specified in the individual IOPBs being posted. The bit definition is the same as for the Normal Completion Interrupt Level/Vector word described previously in this section.

- **Minimum Group Count**

  This field specifies the minimum number of IOPB completions to post per host interrupt. For example, if this field contains '5', the controller will not interrupt the host until it has at least five completions to post.

  The Minimum Group Count must be greater than 1 and less than 24. In addition, it must be less than or equal to the value in the Maximum Group Count field.

- **Maximum Group Count**

  This count specifies the maximum number of IOPB completions to post per host interrupt. For a description of the completed IOPB list, refer to Chapter 4.

  A maximum of 4 list entries will fill the Returned IOPB area, while 13 overwrites the Configuration Status Block, and 24 fills all of the remaining short I/O space (Controller Statistics Block).

NOTE

> Entering a value greater than 13 disables the firmware from periodically updating the Controller Statistics Block. For information on the Controller Statistics Block, refer to Chapter 4.

EAGLE-PROVIDED IOPB FIELDS

Upon completion, the following information is provided by the Eagle in the Returned IOPB for the Initialize Multiple Completions per Interrupt command.

- **Return Status**

  These two bytes contain the returned status for the Initialize Multiple Completions per Interrupt command. Any non-zero value indicates an error code. A list of valid error codes is available in Chapter 7.

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CHAPTER 6
SPECIFICATIONS

ENVIRONMENTAL SPECIFICATIONS

Operating Temperature:	32-131 degrees F. (0-55 degrees C)
Maximum Relative Humidity:	10-90% noncondensing

ELECTRICAL SPECIFICATIONS

Power:	6A (typical), 8A (max.) @ +5V DC (+/-5%)
	0.5A max. @ +12V DC (+/-5%) (This value is dependent on the transceiver, which is the sole user of the +12V supply. However, it must not exceed 0.5A.)
	-0 A max @ -12V DC (+/-5%)

PHYSICAL SPECIFICATIONS

The Eagle occupies one double-high VMEbus slot.

Width:	9.20" (233 mm)
Height:	6.30" (160 mm)
Thickness (from back of board to highest component):	0.6" (15.24 mm)
Weight:	19.7 ounces (558 grams) (+/-3%)

LED

The 4207 has one red/green LED which signals the following conditions:

- Turns red during power-up diagnostics, then green when diagnostics have completed successfully.
- Turns red during SYSFAIL*.
- During normal operation, turns red during DMA cycles and green when no DMA is taking place.

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Table 6-43. Transceiver Connector Signal Description (Front Panel I/O Only)

PIN	SIGNAL
1	Ground (Control IN Shield)
2	Control IN Circuit A (Collision+)
3	Data OUT Circuit A (Transmit+)
4	Ground (Data IN Shield)
5	Data IN Circuit A (Receive+)
6	Ground (Power-)
7	
8	Ground (Control OUT Shield)
9	Control IN Circuit B (Collision-)
10	Data OUT Circuit B (Transmit-)
11	Ground (Data OUT Shield)
12	Data IN Circuit B (Receive-)
13	Fused +12V DC
14	Ground (Power Shield)
15	

NOTE: If no signal is referenced, then the Eagle does not use that pin.

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Table 6-44. P1 Connector Signal Descriptions (P2 - B Row Only and P2 - Full versions)

Pin	Row A Signal Mnemonic	Row B Signal Mnemonic	Row C Signal Mnemonic
1	DOO	BBSY*	DO8
2	DO1	BCLR*	DO9
3	DO2	ACFAIL*	D1O
4	DO3	BGOIN*	D11
5	DO4	BG0OUT*	D12
6	DO5	BG1IN*	D13
7	DO6	BG1OUT*	D14
8	DO7	BG2IN*	D15
9	GND	BG2OUT*	GND
10	SYSCLK	BG3IN*	SYSFAIL*
11	GND	BG3OUT*	BERR*
12	DS1*	BR0*	SYSRESET*
13	DSO*	BR1*	LWORD*
14	WRITE	BR2*	AM5
15	GND	BR3*	A23
16	DTACK	AM0	A22
17	GND	AM1	A21
18	AS*	AM2	A20
19	GND	AM3	A19
20	IACK	GND	A18
21	IACKIN*		A17
22	IACKOUT*		A16
23	AM4	GND	A15
24	AO7	IRQ7*	A14
25	AO6	IRQ6*	A13
26	AO5	IRQ5*	A12
27	AO4	IRQ4*	A11
28	AO3	IRQ3*	A10
29	A02	IRQ2*	A09
30	AO1	IRQ1*	A08
31			+12V DC
32	+5V DC	=5V DC	+5V DC

NOTES: If no signal is referenced, then the Eagle does not use that pin. A "*" denotes an active low signal. If ACFAIL* occurs, the 4207 must be reset.

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Table 6-45. VMEbus P2 Connector Signal Descriptions (P2 - B Row Only version only)

Pin	Row A Signal Mnemonic	Row B Signal Mnemonic	Row C Signal Mnemonic
1		+5V DC	
2		GND	
3			
4		A24	
5		A25	
6		A26	
7		A27	
8		A28	
9		A29	
10		A30	
11		A31	
12		GND	
13		+5V DC	
14		D16	
15		D17	
16		D18	
17		D19	
18		D20	
19		D21	
20		D22	
21		D23	
22		GND	
23		D24	
24		D25	
25		D26	
26		D27	
27		D28	
28		D29	
29		D30	
30		D31	
31		GND	
32		+5V DC	

NOTE: If no signal is referenced, then the Eagle does not use that pin.

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Table 6-46. VMEbus P2 Connector Signal Descriptions (P2 - Full version only)

Pin	Row A Signal Mnemonic	Row B Signal Mnemonic	Row C Signal Mnemonic
1		+5V DC	
2		GND	
3			
4		A24	
5		A25	
6		A26	
7		A27	
8		A28	
9		A29	
10		A30	
11		A31	
12		GND	
13		+5V DC	
14	GND	D16	GND
15	RESERVED	D17	RESERVED
16	GND	D18	GND
17	RESERVED	D19	RESERVED
18	GND	D20	GND
19	RESERVED	D21	RESERVED
20	GND	D22	GND
21	RESERVED	D23	RESERVED
22	GND	GND	GND
23	COLLISION+	D24	COLLISION-
24	GND	D25	GND
25	TRANSMIT+	D26	TRANSMIT-
26	GND	D27	GND
27	RECEIVE+	D28	RECEIVE-
28	GND	D29	GND
29	FUSED +12V DC	D30	FUSED +12V DC
30	GND	D31	GND
31	RESERVED	GND	RESERVED
32	+5V DC	+5V DC	+5V DC

NOTE: If no signal is referenced, then the Eagle does not use that pin.

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CHAPTER 7
ERROR CODES

CONTROLLER ERRORS:

E_CTRL_OK	0x00	No errors
E_CTRL_BAD	0x80	Miscellaneous error

QUEUE AND COMMAND ERRORS:

E_QCMD_INCMD	0x04	Invalid command

ERRORS ASSOCIATED WITH MULTIPLE COMPLETIONS PER INTERRUPT IOPB:

	0x09	Queue Mode not started prior to initializing multiple interrupts (i.e. Start Queue Mode bit is not set in the Master Control Register).
	0x10	One or more reserved fields have not been cleared to 0, as specified in the MCI IOPB description.
	0x12	Controller must be initialized first.
	0x13	The Minimum Group Count or Maximum Group Count field is out of bounds (contains a value less than 2 or greater than 24). Also occurs if the Minimum Group Count field is greater than the Maximum Group Count field.
	0x14	The Group Interrupt field is out of bounds (less than 1 or greater than 7).

GENERAL ERRORS:

NOERROR	0x00	No errors, general
E_GEN_NIMP	0x11	Command not implemented
E_GEN_CINIT	0x12	Controller must be initialized first.

VMEbus ERRORS:

E_VME_BUSS	0x20	Bus error during DMA
E_VME_TOUT	0x21	VMEbus timeout

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ETHERNET ERRORS:

E_ENET_TX	0x30	Ethernet transmit
E_ENET_TX_DIS	0x31	LANCE transmitter disabled
E_ENET_RX	0x40	Ethernet receive

To identify the type of receive error, examine the upper byte of the LANCE Receive Message Descriptor 1 (RMD1) in the returned IOPB. The lower byte of RMD1 consists of address bits and should be ignored.

The values which are most likely to occur in the upper byte of RMD1 are:

Value	Meaning
0x00	middle of babble
0x01	end of babble
0x02	start of babble
0x03	the received packet size is greater than the host buffer size

If a value other than 0x00 - 0x03 occurs in the upper byte of RMD1, consult the description of RMD1 in your LANCE manual to determine the cause of the receive error.

MISCELLANEOUS ERRORS:

E_NRAM_BAD	0x50	Bad NOVRAM access
E_CPU_EXCEPTION	0xA0	Onboard 68020 exception

POWER UP DIAGNOSTICS — PROCESSOR ERRORS:

P_INST_ERROR	0x01	Bad 68020, instruction problems
P_REG_A0_ERROR	0x02	Address register 0 failure
P_REG_A1_ERROR	0x03	Address register 1 failure
P_REG_A2_ERROR	0x04	Address register 2 failure
P_REG_A3_ERROR	0x05	Address register 3 failure
P_REG_A4_ERROR	0x06	Address register 4 failure
P_REG_A5_ERROR	0x07	Address register 5 failure
P_REG_A6_ERROR	0x08	Address register 6 failure
P_REG_A7_ERROR	0x09	Address register 7 failure
P_REG_D0_ERROR	0x0A	Data register 0 failure
P_REG_D1_ERROR	0x0B	Data register 1 failure
P_REG_D2_ERROR	0x0C	Data register 2 failure
P_REG_D3_ERROR	0x0D	Data register 3 failure
P_REG_D4_ERROR	0x0E	Data register 4 failure
P_REG_D5_ERROR	0x0F	Data register 5 failure
P_REG_D6_ERROR	0x10	Data register 6 failure
P_REG_D7_ERROR	0x11	Data register 7 failure

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POWER UP DIAGNOSTICS — EPROM ERRORS:

P_EPROM_ERROR	0x12	Checksum did not match power up Onboard Control Register diagnostic error codes
P_OCR_W1_ERROR	0x13	Walking 1 failure
P_OCR_W0_ERROR	0x14	Walking 0 failure

POWER UP DIAGNOSTICS — RAM SIZE ERRORS:

P_SIZE_BASE_ERROR 0x0100

Full error code is defined as:
bbbb bbbb bbbb bbbb bbbb bbbb xxxx yyyy
where: *bbbb* is P_SIZE_BASE_ERROR, *xxxx* is pup_size internal phase count, *yyyy* is pup_size internal pass count

Valid values for *xxxx* are:

- 0 - test finished
- 1 - walking zeros/ones
- 2 - alternating bits
- 3 - incrementing data

Valid values for *yyyy* are:

- 1 - fill, byte wide
- 2 - verify, byte wide
- 3 - fill, word wide
- 4 - verify, word wide
- 5 - fill, longword wide
- 6 - verify, longword wide
- > 6 - finished

POWER UP DIAGNOSTICS — RAM ADDRESS ERRORS:

P_ADDR_P1_0_V_ERROR	0x15	Pass 1 zero verify error
P_ADDR_P1_D_V_ERROR	0x16	Pass 1 data verify error
P_ADDR_P2_0_V_ERROR	0x17	Pass 2 zero verify error
P_ADDR_P2_D_V_ERROR	0x18	Pass 2 data verify error
P_ADDR_P3_0_V_ERROR	0x19	Pass 3 zero verify error

POWER UP DIAGNOSTICS — RAM VALUE ERRORS:

P_VALUE_ERROR 0x1A Data value does not verify

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POWER UP DIAGNOSTICS — LANCE CSR ERRORS:

P_DB_NOT_PRESENT	0x1B	Daughter card not present
P_LCSR_NOSET0_A	0x1C	Was set by a 0
P_LCSR_NOSET0_B	0x1D	Was not expected value
P_LCSR_NOSET1_A	0x1E	Was set by a 1
P_LCSR_NOSET1_B	0x1F	Was not expected value
P_LCSR_SET_W_1_A	0x20	
P_LCSR_SET_W_1_B	0x21	
P_LCSR_NOCLR_0_A	0x22	
P_LCSR_NOCLR_0_B	0x23	
P_LCSR_NOCLR_1_A	0x24	
P_LCSR_NOCLR_1_B	0x25	
P_LCSR_CLR0_A	0x26	
P_LCSR_CLR0_B	0x27	
P_LCSR_CLR1_A	0x28	
P_LCSR_CLR1_B	0x29	
P_LCSR_PARTII_ERROR	0x2A	
P_LCSR_CSR1_NOVERIFY	0x2B	Pattern to CSR1 did not verify
P_LCSR_CSR2_NOVERIFY	0x2C	Pattern to CSR2 did not verify
P_LCSR_CSR0_NOSTOP	0x2D	Stop bit in CSR0 did not set properly

EXTENDED DIAGNOSTICS — MISCELLANEOUS ERRORS:

E_DIAG_NIMP	0x200	Test not implemented
E_DIAG_IVTN	0x201	Invalid test number
E_DIAG_QMODE	0x202	Extended diagnostics not allowed when Eagle is in Queue Mode
E_DB_NOT_PRESENT	0x203	Daughter card not present

EXTENDED DIAGNOSTICS — MEMORY ERRORS:

E_REFRESH_ERROR	0x204	Refresh error
E_NOVRAM_ERROR	0x205	NOVRAM error
E_WALKING_ERROR	0x206	Walking 1's and 0's failed
E_ALTDATA_ERROR	0x207	Alternating data verification error
E_INCDATA_ERROR	0x208	Incrementing data verification error
E_FIFO_DNLD_ERROR	0x209	FIFO download error
E_FIFO_UPLD_ERROR	0x20A	FIFO upload error
E_ADDR_BASE_ERROR	0x20B	Address base error with status 0 "0x20CAddress base error with status 1 "0x20DAddress base error with status 2 "0x20EAddress base error with status 3

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EXTENDED DIAGNOSTICS — RAM ERRORS:

E_RAM_P0_ERROR	0x210
E_RAM_P1_ERROR	0x211
E_RAM_P2_ERROR	0x212
E_RAM_P3_ERROR	0x213
E_RAM_P4_ERROR	0x214

EXTENDED DIAGNOSTICS — EVENT RAM ERRORS:

E_EVENT_RAM_P0_ERROR	0x210
E_EVENT_RAM_P1_ERROR	0x211
E_EVENT_RAM_P2_ERROR	0x212
E_EVENT_RAM_P3_ERROR	0x213
E_EVENT_RAM_P4_ERROR	0x214

EXTENDED DIAGNOSTICS — DMA-RELATED ERRORS:

E_MEMTYPE_ERROR	0x21B	
E_RD_DMA_BASE_ERROR	0x220	DMA read error with status 0
"	0x221	DMA read error with status 1
"	0x222	DMA read error with status 2
"	0x223	DMA read error with status 3
E_WT_DMA_BASE_ERROR	0x230	DMA write error with status 0
"	0x231	DMA write error with status 1
"	0x232	DMA write error with status 2
"	0x233	DMA write error with status 3

EXTENDED DIAGNOSTICS — LANCE LOOPBACK ERRORS:

E_LLOOP_ERROR	0x21A	Loopback test failed

The Extended LANCE Loopback test returns bit-type errors to the Failing Address field of the Returned IOPB. For details on these errors, refer to Chapter 5.

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APPENDIX A
MACSI ACRONYMS

If you have an device driver for the Eagle that was written by Interphase, you may wish to examine the driver source code. The following list of acronyms is provided to help you interpret variable names, comments, etc. embedded in the code.

CIB	Controller Initialization Block
CQEs	Command Queue entries
CQE	Command Queue entry
CQ	Command Queue
CRB	Command Response Block
CIOPB	Control IOPB
CRSW	Command Response Status Word
CSR	Command Status Register
CSS	Controller Specific Space
HUS	Host Usable Space
MCE	Master Command Entry
MCR	Master Control Register
MCSB	Master Control/Status Block
MSR	Master Status Register
OCR	Onboard Control Register
QECR	Queue Entry Control Register
QHP	Queue Head Pointer
QMS	Queue Mode Started
RIOPB	Returned IOPB
RXIOPB	Receive IOPB
SQM	Start Queue Mode
TXIOPB	Transmit IOPB

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APPENDIX B
MULTICAST PROGRAM

The following C program is included here to help you implement multicast addressing in your installation.

```
#include  <stdio.h>

#define  PHY_ADDR_SIZE      6
#define  LOGICAL_ADDR_FILTER 4

unsigned  int lafbit;
unsigned  int  p[6];
unsigned  long  crc         = 0xffffffff;
unsigned long  orgcrc      = 0;
unsigned  char  pa[PHY_ADDR_SIZE+1];
unsigned  short  laf[LOGICAL_ADDR_FILTER];

/*
 * p2lf  -  physical to logical filter
 *
 *     An Ethernet Packet with D==Destination, S==Source, TY==Type
 *          ---------------------------------------------------
 *          |D0|D1|D2|D3|D4|D5|D6|S0|S1|S2|S3|S4|S5|S6|TY|
 *          ---------------------------------------------------
 *     For a destination address to be a potential logical address, bit 0
 *     of octet 0 (the first octet that appears above) must be set.
 *     This program will give a logical filter on output with the
 *     proper bit set to accept the input logical address. Due to certain
 *     8086-specific features built into the LANCE, it is necessary to stuff
 *     this filter into memory byte-swapped within the word.  In the Lance
 *     Initialization Block, the Logical Filter that is represented in the 68000
 *     world as L0 L1 L2 L3 L4 L5 L6 L7, should be seen as L1 L0 L3 L2 L5 L4 L7 L6.
 *
 *     Examples:
 *               logical address        hash   logical filter (Lance order)
 *               -------------------    ----   ----------------------------
 *               85.00.00.00.00.00      0      00.01.00.00.00.00.00.00
 *               4D.00.00.00.00.00      63     00.00.00.00.00.00.80.00
 *               03.04.06.08.0A.0C      57     00.00.00.00.00.00.02.00
 *               01.02.03.04.05.06      31     00.00.80.00.00.00.00.00
 *               FF.00.00.00.00.00      42     00.00.00.00.04.00.00.00
 *               33.00.44.00.55.00      7      00.80.00.00.00.00.00.00
 *               05.00.00.00.00.00      7      00.80.00.00.00.00.00.00
 *
 */
main( argc, argv )


int argc;
```


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```
char *argv[];
{
   register int i;

   if ( argc <= 1 ) {
      printf("Usage:   %s  physical_address\n",argv[0]);
      printf("         where physical_address is entered in dot notation,\n");
      printf("         lowest octet to highest [in packet order]\n\n");
      printf("Example: If the packet header has a destination address of\n");
      printf("         800102030405, then we would enter the following: \n");
      printf("         p2lf 80.01.02.03.04.05\n\n");
      printf("Output:  Output consists of:\n");
      printf("         a) The input physical address in packet order\n");
      printf("         b) The calculated CRC of the input address\n");
      printf("         c) The hash (bit) value constructed from the CRC\n");
      printf("         d) The logical filter address in 68000 order\n");
      printf("         e) The logical filter address in required LANCE order\n");
   } else {
       sscanf(argv[1],"%x.%x.%x.%x.%x.%x",&p[0],&p[1],&p[2],&p[3],&p[4],&p[5]);
       pa[0] = p[0];
       pa[1] = p[1];
       pa[2] = p[2];
       pa[3] = p[3];
       pa[4] = p[4];
       pa[5] = p[5];
       if(!(pa[0]&0x01)) {
         printf("Error - A logical address must have bit 0 of octet 0 set\n");
         exit(0);
       }
       lafbit = crcgen( &pa[0] );
       printf("   Input Physical Address: 0x[%02x.%02x.%02x.%02x.%02x.%02x]\n",
          pa[0], pa[1], pa[2], pa[3], pa[4], pa[5]);
       printf("   Calculated CRC        : 0x%08X\n",orgcrc);
       printf("   Hash (bit position)   : %d\n",lafbit);
       printf("   Logical Filter (68000): 0x[%02x.%02x.%02x.%02x.%02x.%02x.%02x,%02x]\n",
          laf[0]&0xff,laf[0]>>8,laf[1]&0xff,laf[1]>>8,
          laf[2]&0xff,laf[2]>>8,laf[3]&0xff,laf[3]>>8);
       printf("   Logical Filter (Lance): 0x[%02x.%02x.%02x.%02x.%02x.%02x.%02x,%02x]\n",
          laf[0]>>8,laf[0]&0xff,laf[1]>>8,laf[1]&0xff,
          laf[2]>>8,laf[2]&0xff,laf[3]>>8,laf[3]&0xff);
   }
}

/*
 * Ethernet specified CRC calculation
 */
crcgen( cp )
```


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```
register unsigned char *cp;
{
    register unsigned long c;
    register int i, len;

    c = *cp;
    len = 6;
    while (len-- > 0) {
        c = *cp;
        for (i = 0; i < 8; i++) {
            if ((c & 0x01) ^ (crc & 0x01)) {
                crc >>= 1;
                crc = crc ^ 0xedb88320; /* polynomial */
            } else
                crc >>= 1;
            c >>= 1;
        }
        cp++;
    }
    /* Just want the 6 most significant bits. */
    orgcrc = crc;
    crc = crc >> 26;

    /* Turn on the corresponding bit in the address filter. */
    laf[crc >> 4] |= 1 << (crc & 0xf);
    return(crc);
}
```


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APPENDIX C
VMEbus TIMING CONSIDERATIONS

The Eagle's BUSpacket Interface bursts preformatted data packets across the bus as fast as the system will allow. This appendix explains:

- how system considerations affect the speed of the BUSpacket Interface
- how to calculate bus speed when memory speed is known

There are two keys to the speed of the BUSpacket Interface: a FIFO buffer and a delay line-based asynchronous state machine. These sections of the Eagle hardware are depicted below:

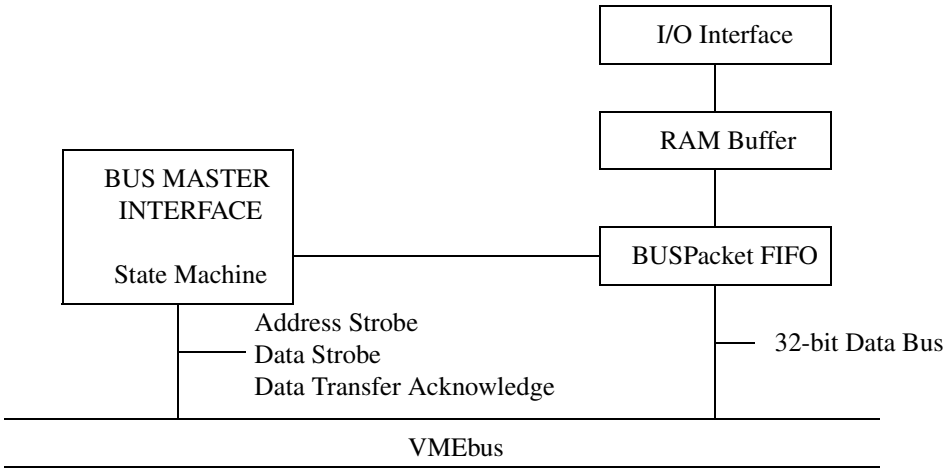


Figure C-46. Eagle-to-VMEbus Hardware Interface

THE FIFO BUFFER

The FIFO buffer of the BUSpacket Interface is a very high-speed FIFO that can write a BUSpacket to (or read a BUSpacket from) the Eagle's 512K communications buffer with a single, fast block transfer. The FIFO itself also has a very fast cycle time so that once it is connected to the VMEbus, the DMA can proceed at 30 Mbytes per second. Thus, the speed of the Eagle on the VMEbus is totally decoupled from the speed of its communications buffer.

When the Eagle is receiving data over the network, for example, the FIFO buffer is filled with a packet of data from the communications buffer prior to the Eagle requesting (and acquiring) the bus. Once the Eagle is on the bus, it transfers the packets across the bus (in DMA mode) at speeds in excess of 30 Mbytes per second. When the transfer is complete, the bus is immediately released for other users. This continues until all of the packets requested by the host have been transferred to system memory. During a DMA operation to/from the BUSpacket FIFO, no other sections of the Eagle are affected, and onboard processes such as caching continue to operate.

THE ASYNCHRONOUS STATE MACHINE

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The Eagle's DMA transfers are controlled by an asynchronous state machine that runs off of a tapped delay line. This setup enables the Eagle to synchronize VMEbus signals while avoiding metastable conditions. Since the VMEbus is an asynchronous bus, the tapped delay line state machine can essentially drive the bus as fast as system memory allows.

Once the Eagle acquires the use of the VMEbus as a Bus Master, it transfers data as fast as the rest of the system will allow. The timing diagram below shows the relationship between the various bus control signals.

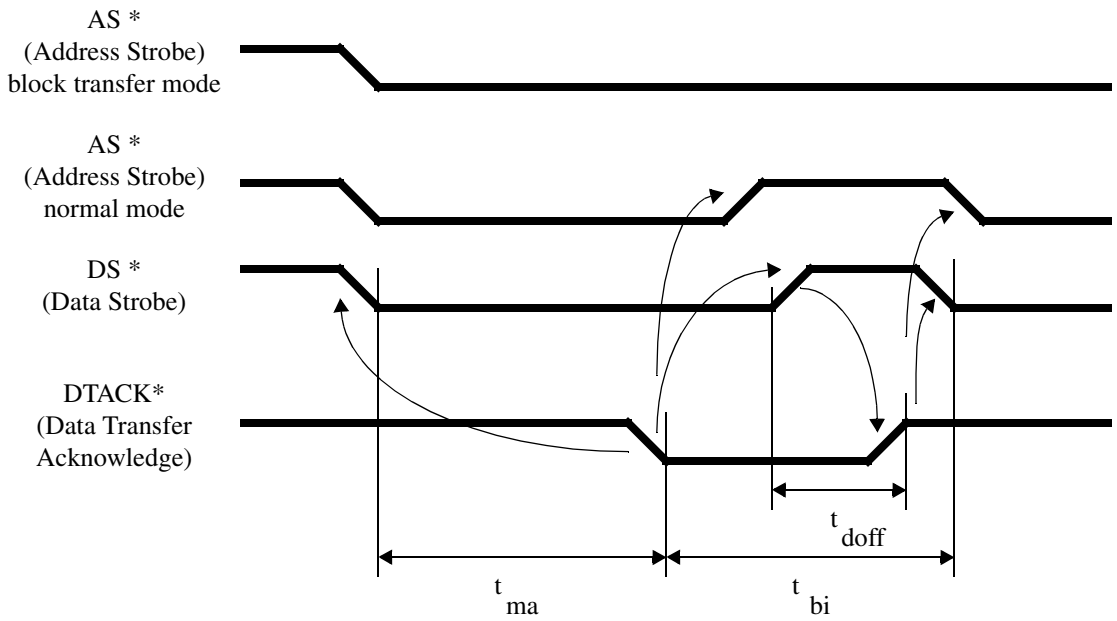


Figure C-47. VMEbus Timing Diagram

In calculating the data rate over the bus there are two major components that must be considered.

t_{bi} — time required by the BUSpacket Interface

t_{bi} is 100 nanoseconds and is largely a function of the BUSpacket Interface on the Eagle. However any t_{doff} (DTACK* turn off time) in excess of 20 nanoseconds also adds directly to the time required for each data transfer because it keeps the next bus cycle from starting. t_{doff} is strictly a function of the VMEbus slave.

t_{ma} — time required for memory access

t_{ma} (commonly called DTACK* time) is the memory access time of the VMEbus slave. t_{ma} also adds directly to the time required for each data transfer. The minimum t_{ma} in the VMEbus specification is 30 nanoseconds.

The total time for a single bus cycle (t_{cycle}) is the sum of t_{bi} and t_{ma}.

$$t_{bi} + t_{ma} = t_{cycle}$$

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Thus for a minimum t_{bi} and t_{ma}:

$$t_{cycle} = 100 \text{ ns} + 30 \text{ ns} = 130 \text{ ns}$$

Since four bytes (long word) are transferred every bus cycle, the bus data rate is:

$$(4 \text{ bytes}/130 \text{ ns}) = (30 \text{ Mbytes/second})$$

BUS PROPAGATION DELAY

Since the VMEbus is completely asynchronous, any propagation delay between the Bus Master and the Slave also affects the maximum data rate achievable by the BUSpacket Interface. In a typical system, propagation delay of Data Strobe [DS*] and Data Transfer Acknowledge [DTACK*] can range from one to 25 nanoseconds, depending upon bus loading and bus length.

Because the BUSpacket Interface drives the VMEbus faster than conventional DMA devices, the full speed advantage may not be realized unless particular attention is paid to the memory timing. Typical VMEbus memory boards have a DTACK* time (t_{ma}) of 250 to 500 nanoseconds. Even if t_{doff} is 40 nanoseconds or less, these long DTACKs limit the bus data rate to the range of 11.4 to 6.7 Mbytes per second.

OPTIMIZING DMA OPERATIONS

This section describes various techniques for maximizing the speed at which the Eagle transfers data across the VMEbus.

There are at least two basic approaches which can be used to achieve the 30 nanosecond DTACK* (t_{ma}) required to achieve a 30 Mbyte per second data rate with a BUSpacket Interface. (A 30 nanosecond DTACK* is the minimum allowed in the VMEbus specification.) One scheme, employed by some of the manufacturers that are using the VMEbus as an I/O bus for high-performance superminicomputers, is to build a very fast FIFO into the bus interface card that connects the VMEbus to the system bus of the host.

Users who employ the VMEbus as the main system bus can achieve this minimum DTACK* time by designing fast memory cards that use Block Mode on the VMEbus in conjunction with Page Mode timing of dynamic RAMs. Block mode on the VMEbus allows a block of data to be transferred to/from contiguous locations. Only the starting address is supplied to the slave. Once the Block Mode transfer is started, it is the slave's responsibility to increment the address. A dynamic RAM memory board can be designed to take advantage of this Block Mode by using what is commonly called the Page Mode of dynamic RAMs.

In Page Mode, dynamic RAMs latch the row address. Only the column address is changed. Thus, Page Mode allows faster operation of the RAMs because it does away with the multiplexing of the address lines on the RAMs themselves. In a simplistic sense then, there might only be one VMEbus address strobe [AS] generating one RAS at the start of a Block Mode transfer. All of the subsequent data transfers would then be accomplished without any VMEbus address strobe [AS] and without any RAS into the dynamic RAMs. The actual memory address would be generated locally on the memory card and strobed into the RAMs with only the CAS. (In actual operation, the number of bytes transferred per Block Mode transaction is limited by the VMEbus specification that Address Line 08 [A08] and above not be changed during a Block Mode transfer.)

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APPENDIX D
COMMUNICATIONS BUFFER BASE ADDRESS SWITCH SETTINGS

The following table shows all possible settings for switches SW3 1-8 and SW2 1-5. As discussed in Chapter 2, these switches are used to set the base address of the Eagle's 512 Kbyte communications buffer. For each entry in the table below, the first eight switch settings respectively correspond to switches 1 through 8 on SW3. The last five switch settings respectively correspond to switches 1 through 5 on SW2.

Note that "O" stands for a switch setting of ON (closed), and "F" stands for a setting of OFF (open). An OFF switch has a value of "1" and an ON switch has a value of "0".

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345	ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345	ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345	ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
00000000	OOOOOOOOOOOOO	01900000	OOOOOOOFFOOFO	03200000	OOOOOOFFOOFOO	04B00000	OOOOOFOOFOFFO
00080000	OOOOOOOOOOOOF	01980000	OOOOOOOFFOOFF	03280000	OOOOOOFFOOFOF	04B80000	OOOOOFOOFOFFF
00100000	OOOOOOOOOOOFO	01A00000	OOOOOOOFFOFOO	03300000	OOOOOOFFOOFFO	04C00000	OOOOOFOOFFOOO
00180000	OOOOOOOOOOOFF	01A80000	OOOOOOOFFOFOF	03380000	OOOOOOFFOOFFF	04C80000	OOOOOFOOFFOOF
00200000	OOOOOOOOOOFOO	01B00000	OOOOOOOFFOFFO	03400000	OOOOOOFFOFOOO	04D00000	OOOOOFOOFFOFO
00280000	OOOOOOOOOOFOF	01B80000	OOOOOOOFFOFFF	03480000	OOOOOOFFOFOOF	04D80000	OOOOOFOOFFOFF
00300000	OOOOOOOOOOFFO	01C00000	OOOOOOOFFFOOO	03500000	OOOOOOFFOFOFO	04E00000	OOOOOFOOFFFOO
00380000	OOOOOOOOOOFFF	01C80000	OOOOOOOFFFOOF	03580000	OOOOOOFFOFOFF	04E80000	OOOOOFOOFFFOF
00400000	OOOOOOOOOFOOO	01D00000	OOOOOOOFFFOFO	03600000	OOOOOOFFOFFOO	04F00000	OOOOOFOOFFFFO
00480000	OOOOOOOOOFOOF	01D80000	OOOOOOOFFFOFF	03680000	OOOOOOFFOFFOF	04F80000	OOOOOFOOFFFFF
00500000	OOOOOOOOOFOFO	01E00000	OOOOOOOFFFFOO	03700000	OOOOOOFFOFFFO	05000000	OOOOOFOFOOOOO
00580000	OOOOOOOOOFOFF	01E80000	OOOOOOOFFFFOF	03780000	OOOOOOFFOFFFF	05080000	OOOOOFOFOOOOF
00600000	OOOOOOOOOFFOO	01F00000	OOOOOOOFFFFFO	03800000	OOOOOOFFFOOOO	05100000	OOOOOFOFOOOFO
00680000	OOOOOOOOOFFOF	01F80000	OOOOOOOFFFFFF	03880000	OOOOOOFFFOOOF	05180000	OOOOOFOFOOOFF
00700000	OOOOOOOOOFFFO	02000000	OOOOOOFOOOOOO	03900000	OOOOOOFFFOOFO	05200000	OOOOOFOFOOFOO
00780000	OOOOOOOOOFFFF	02080000	OOOOOOFOOOOOF	03980000	OOOOOOFFFOOFF	05280000	OOOOOFOFOOFOF
00800000	OOOOOOOOFOOOO	02100000	OOOOOOFOOOOFO	03A00000	OOOOOOFFFOFOO	05300000	OOOOOFOFOOFFO
00880000	OOOOOOOOFOOOF	02180000	OOOOOOFOOOOFF	03A80000	OOOOOOFFFOFOF	05380000	OOOOOFOFOOFFF
00900000	OOOOOOOOFOOFO	02200000	OOOOOOFOOOFOO	03B00000	OOOOOOFFFOFFO	05400000	OOOOOFOFOFOOO
00980000	OOOOOOOOFOOFF	02280000	OOOOOOFOOOFOF	03B80000	OOOOOOFFFOFFF	05480000	OOOOOFOFOFOOF
00A00000	OOOOOOOOFOFOO	02300000	OOOOOOFOOOFFO	03C00000	OOOOOOFFFFOOO	05500000	OOOOOFOFOFOFO
00A80000	OOOOOOOOFOFOF	02380000	OOOOOOFOOOFFF	03C80000	OOOOOOFFFFOOF	05580000	OOOOOFOFOFOFF
00B00000	OOOOOOOOFOFFO	02400000	OOOOOOFOOFOOO	03D00000	OOOOOOFFFFOFO	05600000	OOOOOFOFOFFOO
00B80000	OOOOOOOOFOFFF	02480000	OOOOOOFOOFOOF	03D80000	OOOOOOFFFFOFF	05680000	OOOOOFOFOFFOF
00C00000	OOOOOOOOFFOOO	02500000	OOOOOOFOOFOFO	03E00000	OOOOOOFFFFFOO	05700000	OOOOOFOFOFFFO
00C80000	OOOOOOOOFFOOF	02580000	OOOOOOFOOFOFF	03E80000	OOOOOOFFFFFOF	05780000	OOOOOFOFOFFFF
00D00000	OOOOOOOOFFOFO	02600000	OOOOOOFOOFFOO	03F00000	OOOOOOFFFFFFO	05800000	OOOOOFOFFOOOO
00D80000	OOOOOOOOFFOFF	02680000	OOOOOOFOOFFOF	03F80000	OOOOOOFFFFFFF	05880000	OOOOOFOFFOOOF
00E00000	OOOOOOOOFFFOO	02700000	OOOOOOFOOFFFO	04000000	OOOOOFOOOOOOO	05900000	OOOOOFOFFOOFO
00E80000	OOOOOOOOFFFOF	02780000	OOOOOOFOOFFFF	04080000	OOOOOFOOOOOOF	05980000	OOOOOFOFFOOFF
00F00000	OOOOOOOOFFFFO	02800000	OOOOOOFOFOOOO	04100000	OOOOOFOOOOOFO	05A00000	OOOOOFOFFOFOO
00F80000	OOOOOOOOFFFFF	02880000	OOOOOOFOFOOOF	04180000	OOOOOFOOOOOFF	05A80000	OOOOOFOFFOFOF
01000000	OOOOOOOFOOOOO	02900000	OOOOOOFOFOOFO	04200000	OOOOOFOOOOFOO	05B00000	OOOOOFOFFOFFO
01080000	OOOOOOOFOOOOF	02980000	OOOOOOFOFOOFF	04280000	OOOOOFOOOOFOF	05B80000	OOOOOFOFFOFFF
01100000	OOOOOOOFOOOFO	02A00000	OOOOOOFOFOFOO	04300000	OOOOOFOOOOFFO	05C00000	OOOOOFOFFFOOO
01180000	OOOOOOOFOOOFF	02A80000	OOOOOOFOFOFOF	04380000	OOOOOFOOOOFFF	05C80000	OOOOOFOFFFOOF
01200000	OOOOOOOFOOFOO	02B00000	OOOOOOFOFOFFO	04400000	OOOOOFOOOFOOO	05D00000	OOOOOFOFFFOFO
01280000	OOOOOOOFOOFOF	02B80000	OOOOOOFOFOFFF	04480000	OOOOOFOOOFOOF	05D80000	OOOOOFOFFFOFF
01300000	OOOOOOOFOOFFO	02C00000	OOOOOOFOFFOOO	04500000	OOOOOFOOOFOFO	05E00000	OOOOOFOFFFFOO
01380000	OOOOOOOFOOFFF	02C80000	OOOOOOFOFFOOF	04580000	OOOOOFOOOFOFF	05E80000	OOOOOFOFFFFOF
01400000	OOOOOOOFOFOOO	02D00000	OOOOOOFOFFOFO	04600000	OOOOOFOOOFFOO	05F00000	OOOOOFOFFFFFO
01480000	OOOOOOOFOFOOF	02D80000	OOOOOOFOFFOFF	04680000	OOOOOFOOOFFOF	05F80000	OOOOOFOFFFFFF
01500000	OOOOOOOFOFOFO	02E00000	OOOOOOFOFFFOO	04700000	OOOOOFOOOFFFO	06000000	OOOOOFFOOOOOO
01580000	OOOOOOOFOFOFF	02E80000	OOOOOOFOFFFOF	04780000	OOOOOFOOOFFFF	06080000	OOOOOFFOOOOOF
01600000	OOOOOOOFOFFOO	02F00000	OOOOOOFOFFFFO	04800000	OOOOOFOOFOOOO	06100000	OOOOOFFOOOOFO
01680000	OOOOOOOFOFFOF	02F80000	OOOOOOFOFFFFF	04880000	OOOOOFOOFOOOF	06180000	OOOOOFFOOOOFF
01700000	OOOOOOOFOFFFO	03000000	OOOOOOFFOOOOO	04900000	OOOOOFOOFOOFO	06200000	OOOOOFFOOOFOO
01780000	OOOOOOOFOFFFF	03080000	OOOOOOFFOOOOF	04980000	OOOOOFOOFOOFF	06280000	OOOOOFFOOOFOF
01800000	OOOOOOOFFOOOO	03100000	OOOOOOFFOOOFO	04A00000	OOOOOFOOFOFOO	06300000	OOOOOFFOOOFFO
01880000	OOOOOOOFFOOOF	03180000	OOOOOOFFOOOFF	04A80000	OOOOOFOOFOFOF	06380000	OOOOOFFOOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
06400000	OOOOOFFOOFOOO
06480000	OOOOOFFOOFOOF
06500000	OOOOOFFOOFOFO
06580000	OOOOOFFOOFOFF
06600000	OOOOOFFOOFFOO
06680000	OOOOOFFOOFFOF
06700000	OOOOOFFOOFFFO
06780000	OOOOOFFOOFFFF
06800000	OOOOOFFOFOOOO
06880000	OOOOOFFOFOOOF
06900000	OOOOOFFOFOOFO
06980000	OOOOOFFOFOOFF
06A00000	OOOOOFFOFOFOO
06A80000	OOOOOFFOFOFOF
06B00000	OOOOOFFOFOFFO
06B80000	OOOOOFFOFOFFF
06C00000	OOOOOFFOFFOOO
06C80000	OOOOOFFOFFOOF
06D00000	OOOOOFFOFFOFO
06D80000	OOOOOFFOFFOFF
06E00000	OOOOOFFOFFFOO
06E80000	OOOOOFFOFFFOF
06F00000	OOOOOFFOFFFFO
06F80000	OOOOOFFOFFFFF
07000000	OOOOOFFFOOOOO
07080000	OOOOOFFFOOOOF
07100000	OOOOOFFFOOOFO
07180000	OOOOOFFFOOOFF
07200000	OOOOOFFFOOFOO
07280000	OOOOOFFFOOFOF
07300000	OOOOOFFFOOFFO
07380000	OOOOOFFFOOFFF
07400000	OOOOOFFFOFOOO
07480000	OOOOOFFFOFOOF
07500000	OOOOOFFFOFOFO
07580000	OOOOOFFFOFOFF
07600000	OOOOOFFFOFFOO
07680000	OOOOOFFFOFFOF
07700000	OOOOOFFFOFFFO
07780000	OOOOOFFFOFFFF
07800000	OOOOOFFFFOOOO
07880000	OOOOOFFFFOOOF
07900000	OOOOOFFFFOOFO
07980000	OOOOOFFFFOOFF
07A00000	OOOOOFFFFOFOO
07A80000	OOOOOFFFFOFOF
07B00000	OOOOOFFFFOFFO
07B80000	OOOOOFFFFOFFF
07C00000	OOOOOFFFFFOOO
07C80000	OOOOOFFFFFOOF
07D00000	OOOOOFFFFFOFO
07D80000	OOOOOFFFFFOFF
07E00000	OOOOOFFFFFFOO
07E80000	OOOOOFFFFFFOF
07F00000	OOOOOFFFFFFFO
07F80000	OOOOOFFFFFFFF
08000000	OOOOFOOOOOOOO
08080000	OOOOFOOOOOOOF
08100000	OOOOFOOOOOOFO
08180000	OOOOFOOOOOOFF
08200000	OOOOFOOOOOFOO
08280000	OOOOFOOOOOFOF
08300000	OOOOFOOOOOFFO
08380000	OOOOFOOOOOFFF
08400000	OOOOFOOOOFOOO
08480000	OOOOFOOOOFOOF
08500000	OOOOFOOOOFOFO
08580000	OOOOFOOOOFOFF
08600000	OOOOFOOOOFFOO
08680000	OOOOFOOOOFFOF
08700000	OOOOFOOOOFFFO
08780000	OOOOFOOOOFFFF
08800000	OOOOFOOOFOOOO
08880000	OOOOFOOOFOOOF
08900000	OOOOFOOOFOOFO
08980000	OOOOFOOOFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
08A00000	OOOOFOOOFOFOO
08A80000	OOOOFOOOFOFOF
08B00000	OOOOFOOOFOFFO
08B80000	OOOOFOOOFOFFF
08C00000	OOOOFOOOFFOOO
08C80000	OOOOFOOOFFOOF
08D00000	OOOOFOOOFFOFO
08D80000	OOOOFOOOFFOFF
08E00000	OOOOFOOOFFFOO
08E80000	OOOOFOOOFFFOF
08F00000	OOOOFOOOFFFFO
08F80000	OOOOFOOOFFFFF
09000000	OOOOFOOFOOOOO
09080000	OOOOFOOFOOOOF
09100000	OOOOFOOFOOOFO
09180000	OOOOFOOFOOOFF
09200000	OOOOFOOFOOFOO
09280000	OOOOFOOFOOFOF
09300000	OOOOFOOFOOFFO
09380000	OOOOFOOFOOFFF
09400000	OOOOFOOFOFOOO
09480000	OOOOFOOFOFOOF
09500000	OOOOFOOFOFOFO
09580000	OOOOFOOFOFOFF
09600000	OOOOFOOFOFFOO
09680000	OOOOFOOFOFFOF
09700000	OOOOFOOFOFFFO
09780000	OOOOFOOFOFFFF
09800000	OOOOFOOFFOOOO
09880000	OOOOFOOFFOOOF
09900000	OOOOFOOFFOOFO
09980000	OOOOFOOFFOOFF
09A00000	OOOOFOOFFOFOO
09A80000	OOOOFOOFFOFOF
09B00000	OOOOFOOFFOFFO
09B80000	OOOOFOOFFOFFF
09C00000	OOOOFOOFFFOOO
09C80000	OOOOFOOFFFOOF
09D00000	OOOOFOOFFFOFO
09D80000	OOOOFOOFFFOFF
09E00000	OOOOFOOFFFFOO
09E80000	OOOOFOOFFFFOF
09F00000	OOOOFOOFFFFFO
09F80000	OOOOFOOFFFFFF
0A000000	OOOOFOFOOOOOO
0A080000	OOOOFOFOOOOOF
0A100000	OOOOFOFOOOOFO
0A180000	OOOOFOFOOOOFF
0A200000	OOOOFOFOOOFOO
0A280000	OOOOFOFOOOFOF
0A300000	OOOOFOFOOOFFO
0A380000	OOOOFOFOOOFFF
0A400000	OOOOFOFOOFOOO
0A480000	OOOOFOFOOFOOF
0A500000	OOOOFOFOOFOFO
0A580000	OOOOFOFOOFOFF
0A600000	OOOOFOFOOFFOO
0A680000	OOOOFOFOOFFOF
0A700000	OOOOFOFOOFFFO
0A780000	OOOOFOFOOFFFF
0A800000	OOOOFOFOFOOOO
0A880000	OOOOFOFOFOOOF
0A900000	OOOOFOFOFOOFO
0A980000	OOOOFOFOFOOFF
0AA00000	OOOOFOFOFOFOO
0AA80000	OOOOFOFOFOFOF
0AB00000	OOOOFOFOFOFFO
0AB80000	OOOOFOFOFOFFF
0AC00000	OOOOFOFOFFOOO
0AC80000	OOOOFOFOFFOOF
0AD00000	OOOOFOFOFFOFO
0AD80000	OOOOFOFOFFOFF
0AE00000	OOOOFOFOFFFOO
0AE80000	OOOOFOFOFFFOF
0AF00000	OOOOFOFOFFFFO
0AF80000	OOOOFOFOFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
0B000000	OOOOFOFFOOOOO
0B080000	OOOOFOFFOOOOF
0B100000	OOOOFOFFOOOFO
0B180000	OOOOFOFFOOOFF
0B200000	OOOOFOFFOOFOO
0B280000	OOOOFOFFOOFOF
0B300000	OOOOFOFFOOFFO
0B380000	OOOOFOFFOOFFF
0B400000	OOOOFOFFOFOOO
0B480000	OOOOFOFFOFOOF
0B500000	OOOOFOFFOFOFO
0B580000	OOOOFOFFOFOFF
0B600000	OOOOFOFFOFFOO
0B680000	OOOOFOFFOFFOF
0B700000	OOOOFOFFOFFFO
0B780000	OOOOFOFFOFFFF
0B800000	OOOOFOFFFOOOO
0B880000	OOOOFOFFFOOOF
0B900000	OOOOFOFFFOOFO
0B980000	OOOOFOFFFOOFF
0BA00000	OOOOFOFFFOFOO
0BA80000	OOOOFOFFFOFOF
0BB00000	OOOOFOFFFOFFO
0BB80000	OOOOFOFFFOFFF
0BC00000	OOOOFOFFFFOOO
0BC80000	OOOOFOFFFFOOF
0BD00000	OOOOFOFFFFOFO
0BD80000	OOOOFOFFFFOFF
0BE00000	OOOOFOFFFFFOO
0BE80000	OOOOFOFFFFFOF
0BF00000	OOOOFOFFFFFFO
0BF80000	OOOOFOFFFFFFF
0C000000	OOOOFFOOOOOOO
0C080000	OOOOFFOOOOOOF
0C100000	OOOOFFOOOOOFO
0C180000	OOOOFFOOOOOFF
0C200000	OOOOFFOOOOFOO
0C280000	OOOOFFOOOOFOF
0C300000	OOOOFFOOOOFFO
0C380000	OOOOFFOOOOFFF
0C400000	OOOOFFOOOFOOO
0C480000	OOOOFFOOOFOOF
0C500000	OOOOFFOOOFOFO
0C580000	OOOOFFOOOFOFF
0C600000	OOOOFFOOOFFOO
0C680000	OOOOFFOOOFFOF
0C700000	OOOOFFOOOFFFO
0C780000	OOOOFFOOOFFFF
0C800000	OOOOFFOOFOOOO
0C880000	OOOOFFOOFOOOF
0C900000	OOOOFFOOFOOFO
0C980000	OOOOFFOOFOOFF
0CA00000	OOOOFFOOFOFOO
0CA80000	OOOOFFOOFOFOF
0CB00000	OOOOFFOOFOFFO
0CB80000	OOOOFFOOFOFFF
0CC00000	OOOOFFOOFFOOO
0CC80000	OOOOFFOOFFOOF
0CD00000	OOOOFFOOFFOFO
0CD80000	OOOOFFOOFFOFF
0CE00000	OOOOFFOOFFFOO
0CE80000	OOOOFFOOFFFOF
0CF00000	OOOOFFOOFFFFO
0CF80000	OOOOFFOOFFFFF
0D000000	OOOOFFOFOOOOO
0D080000	OOOOFFOFOOOOF
0D100000	OOOOFFOFOOOFO
0D180000	OOOOFFOFOOOFF
0D200000	OOOOFFOFOOFOO
0D280000	OOOOFFOFOOFOF
0D300000	OOOOFFOFOOFFO
0D380000	OOOOFFOFOOFFF
0D400000	OOOOFFOFOFOOO
0D480000	OOOOFFOFOFOOF
0D500000	OOOOFFOFOFOFO
0D580000	OOOOFFOFOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
0D600000	OOOOFFOFOFFOO
0D680000	OOOOFFOFOFFOF
0D700000	OOOOFFOFOFFFO
0D780000	OOOOFFOFOFFFF
0D800000	OOOOFFOFFOOOO
0D880000	OOOOFFOFFOOOF
0D900000	OOOOFFOFFOOFO
0D980000	OOOOFFOFFOOFF
0DA00000	OOOOFFOFFOFOO
0DA80000	OOOOFFOFFOFOF
0DB00000	OOOOFFOFFOFFO
0DB80000	OOOOFFOFFOFFF
0DC00000	OOOOFFOFFFOOO
0DC80000	OOOOFFOFFFOOF
0DD00000	OOOOFFOFFFOFO
0DD80000	OOOOFFOFFFOFF
0DE00000	OOOOFFOFFFFOO
0DE80000	OOOOFFOFFFFOF
0DF00000	OOOOFFOFFFFFO
0DF80000	OOOOFFOFFFFFF
0E000000	OOOOFFFOOOOOO
0E080000	OOOOFFFOOOOOF
0E100000	OOOOFFFOOOOFO
0E180000	OOOOFFFOOOOFF
0E200000	OOOOFFFOOOFOO
0E280000	OOOOFFFOOOFOF
0E300000	OOOOFFFOOOFFO
0E380000	OOOOFFFOOOFFF
0E400000	OOOOFFFOOFOOO
0E480000	OOOOFFFOOFOOF
0E500000	OOOOFFFOOFOFO
0E580000	OOOOFFFOOFOFF
0E600000	OOOOFFFOOFFOO
0E680000	OOOOFFFOOFFOF
0E700000	OOOOFFFOOFFFO
0E780000	OOOOFFFOOFFFF
0E800000	OOOOFFFOFOOOO
0E880000	OOOOFFFOFOOOF
0E900000	OOOOFFFOFOOFO
0E980000	OOOOFFFOFOOFF
0EA00000	OOOOFFFOFOFOO
0EA80000	OOOOFFFOFOFOF
0EB00000	OOOOFFFOFOFFO
0EB80000	OOOOFFFOFOFFF
0EC00000	OOOOFFFOFFOOO
0EC80000	OOOOFFFOFFOOF
0ED00000	OOOOFFFOFFOFO
0ED80000	OOOOFFFOFFOFF
0EE00000	OOOOFFFOFFFOO
0EE80000	OOOOFFFOFFFOF
0EF00000	OOOOFFFOFFFFO
0EF80000	OOOOFFFOFFFFF
0F000000	OOOOFFFFOOOOO
0F080000	OOOOFFFFOOOOF
0F100000	OOOOFFFFOOOFO
0F180000	OOOOFFFFOOOFF
0F200000	OOOOFFFFOOFOO
0F280000	OOOOFFFFOOFOF
0F300000	OOOOFFFFOOFFO
0F380000	OOOOFFFFOOFFF
0F400000	OOOOFFFFOFOOO
0F480000	OOOOFFFFOFOOF
0F500000	OOOOFFFFOFOFO
0F580000	OOOOFFFFOFOFF
0F600000	OOOOFFFFOFFOO
0F680000	OOOOFFFFOFFOF
0F700000	OOOOFFFFOFFFO
0F780000	OOOOFFFFOFFFF
0F800000	OOOOFFFFFOOOO
0F880000	OOOOFFFFFOOOF
0F900000	OOOOFFFFFOOFO
0F980000	OOOOFFFFFOOFF
0FA00000	OOOOFFFFFOFOO
0FA80000	OOOOFFFFFOFOF
0FB00000	OOOOFFFFFOFFO
0FB80000	OOOOFFFFFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
0FC00000	OOOOFFFFFFOOO
0FC80000	OOOOFFFFFFOOF
0FD00000	OOOOFFFFFFOFO
0FD80000	OOOOFFFFFFOFF
0FE00000	OOOOFFFFFFOO
0FE80000	OOOOFFFFFFOF
0FF00000	OOOOFFFFFFFO
0FF80000	OOOOFFFFFFFF
10000000	OOOFOOOOOOOOO
10080000	OOOFOOOOOOOOF
10100000	OOOFOOOOOOOFO
10180000	OOOFOOOOOOOFF
10200000	OOOFOOOOOOFOO
10280000	OOOFOOOOOOFOF
10300000	OOOFOOOOOOFFO
10380000	OOOFOOOOOOFFF
10400000	OOOFOOOOOFOOO
10480000	OOOFOOOOOFOOF
10500000	OOOFOOOOOFOFO
10580000	OOOFOOOOOFOFF
10600000	OOOFOOOOOFFOO
10680000	OOOFOOOOOFFOF
10700000	OOOFOOOOOFFFO
10780000	OOOFOOOOOFFFF
10800000	OOOFOOOOFOOOO
10880000	OOOFOOOOFOOOF
10900000	OOOFOOOOFOOFO
10980000	OOOFOOOOFOOFF
10A00000	OOOFOOOOFOFOO
10A80000	OOOFOOOOFOFOF
10B00000	OOOFOOOOFOFFO
10B80000	OOOFOOOOFOFFF
10C00000	OOOFOOOOFFOOO
10C80000	OOOFOOOOFFOOF
10D00000	OOOFOOOOFFOFO
10D80000	OOOFOOOOFFOFF
10E00000	OOOFOOOOFFFOO
10E80000	OOOFOOOOFFFOF
10F00000	OOOFOOOOFFFFO
10F80000	OOOFOOOOFFFFF
11000000	OOOFOOOFOOOOO
11080000	OOOFOOOFOOOOF
11100000	OOOFOOOFOOOFO
11180000	OOOFOOOFOOOFF
11200000	OOOFOOOFOOFOO
11280000	OOOFOOOFOOFOF
11300000	OOOFOOOFOOFFO
11380000	OOOFOOOFOOFFF
11400000	OOOFOOOFOFOOO
11480000	OOOFOOOFOFOOF
11500000	OOOFOOOFOFOFO
11580000	OOOFOOOFOFOFF
11600000	OOOFOOOFOFFOO
11680000	OOOFOOOFOFFOF
11700000	OOOFOOOFOFFFO
11780000	OOOFOOOFOFFFF
11800000	OOOFOOOFFOOOO
11880000	OOOFOOOFFOOOF
11900000	OOOFOOOFFOOFO
11980000	OOOFOOOFFOOFF
11A00000	OOOFOOOFFOFOO
11A80000	OOOFOOOFFOFOF
11B00000	OOOFOOOFFOFFO
11B80000	OOOFOOOFFOFFF
11C00000	OOOFOOOFFFOOO
11C80000	OOOFOOOFFFOOF
11D00000	OOOFOOOFFFOFO
11D80000	OOOFOOOFFFOFF
11E00000	OOOFOOOFFFFOO
11E80000	OOOFOOOFFFFOF
11F00000	OOOFOOOFFFFFO
11F80000	OOOFOOOFFFFFF
12000000	OOOFOOFOOOOOO
12080000	OOOFOOFOOOOOF
12100000	OOOFOOFOOOOFO
12180000	OOOFOOFOOOOFF
12200000	OOOFOOFOOOFOO
12280000	OOOFOOFOOOFOF
12300000	OOOFOOFOOOFFO
12380000	OOOFOOFOOOFFF
12400000	OOOFOOFOOFOOO
12480000	OOOFOOFOOFOOF
12500000	OOOFOOFOOFOFO
12580000	OOOFOOFOOFOFF
12600000	OOOFOOFOOFFOO
12680000	OOOFOOFOOFFOF
12700000	OOOFOOFOOFFFO
12780000	OOOFOOFOOFFFF
12800000	OOOFOOFOFOOOO
12880000	OOOFOOFOFOOOF
12900000	OOOFOOFOFOOFO
12980000	OOOFOOFOFOOFF
12A00000	OOOFOOFOFOFOO
12A80000	OOOFOOFOFOFOF
12B00000	OOOFOOFOFOFFO
12B80000	OOOFOOFOFOFFF
12C00000	OOOFOOFOFFOOO
12C80000	OOOFOOFOFFOOF
12D00000	OOOFOOFOFFOFO
12D80000	OOOFOOFOFFOFF
12E00000	OOOFOOFOFFFOO
12E80000	OOOFOOFOFFFOF
12F00000	OOOFOOFOFFFFO
12F80000	OOOFOOFOFFFFF
13000000	OOOFOOFFOOOOO
13080000	OOOFOOFFOOOOF
13100000	OOOFOOFFOOOFO
13180000	OOOFOOFFOOOFF
13200000	OOOFOOFFOOFOO
13280000	OOOFOOFFOOFOF
13300000	OOOFOOFFOOFFO
13380000	OOOFOOFFOOFFF
13400000	OOOFOOFFOFOOO
13480000	OOOFOOFFOFOOF
13500000	OOOFOOFFOFOFO
13580000	OOOFOOFFOFOFF
13600000	OOOFOOFFOFFOO
13680000	OOOFOOFFOFFOF
13700000	OOOFOOFFOFFFO
13780000	OOOFOOFFOFFFF
13800000	OOOFOOFFFOOOO
13880000	OOOFOOFFFOOOF
13900000	OOOFOOFFFOOFO
13980000	OOOFOOFFFOOFF
13A00000	OOOFOOFFFOFOO
13A80000	OOOFOOFFFOFOF
13B00000	OOOFOOFFFOFFO
13B80000	OOOFOOFFFOFFF
13C00000	OOOFOOFFFFOOO
13C80000	OOOFOOFFFFOOF
13D00000	OOOFOOFFFFOFO
13D80000	OOOFOOFFFFOFF
13E00000	OOOFOOFFFFFOO
13E80000	OOOFOOFFFFFOF
13F00000	OOOFOOFFFFFFO
13F80000	OOOFOOFFFFFFF
14000000	OOOFOFOOOOOOO
14080000	OOOFOFOOOOOOF
14100000	OOOFOFOOOOOFO
14180000	OOOFOFOOOOOFF
14200000	OOOFOFOOOOFOO
14280000	OOOFOFOOOOFOF
14300000	OOOFOFOOOOFFO
14380000	OOOFOFOOOOFFF
14400000	OOOFOFOOOFOOO
14480000	OOOFOFOOOFOOF
14500000	OOOFOFOOOFOFO
14580000	OOOFOFOOOFOFF
14600000	OOOFOFOOOFFOO
14680000	OOOFOFOOOFFOF
14700000	OOOFOFOOOFFFO
14780000	OOOFOFOOOFFFF
14800000	OOOFOFOOFOOOO
14880000	OOOFOFOOFOOOF
14900000	OOOFOFOOFOOFO
14980000	OOOFOFOOFOOFF
14A00000	OOOFOFOOFOFOO
14A80000	OOOFOFOOFOFOF
14B00000	OOOFOFOOFOFFO
14B80000	OOOFOFOOFOFFF
14C00000	OOOFOFOOFFOOO
14C80000	OOOFOFOOFFOOF
14D00000	OOOFOFOOFFOFO
14D80000	OOOFOFOOFFOFF
14E00000	OOOFOFOOFFFOO
14E80000	OOOFOFOOFFFOF
14F00000	OOOFOFOOFFFFO
14F80000	OOOFOFOOFFFFF
15000000	OOOFOFOFOOOOO
15080000	OOOFOFOFOOOOF
15100000	OOOFOFOFOOOFO
15180000	OOOFOFOFOOOFF
15200000	OOOFOFOFOOFOO
15280000	OOOFOFOFOOFOF
15300000	OOOFOFOFOOFFO
15380000	OOOFOFOFOOFFF
15400000	OOOFOFOFOFOOO
15480000	OOOFOFOFOFOOF
15500000	OOOFOFOFOFOFO
15580000	OOOFOFOFOFOFF
15600000	OOOFOFOFOFFOO
15680000	OOOFOFOFOFFOF
15700000	OOOFOFOFOFFFO
15780000	OOOFOFOFOFFFF
15800000	OOOFOFOFFOOOO
15880000	OOOFOFOFFOOOF
15900000	OOOFOFOFFOOFO
15980000	OOOFOFOFFOOFF
15A00000	OOOFOFOFFOFOO
15A80000	OOOFOFOFFOFOF
15B00000	OOOFOFOFFOFFO
15B80000	OOOFOFOFFOFFF
15C00000	OOOFOFOFFFOOO
15C80000	OOOFOFOFFFOOF
15D00000	OOOFOFOFFFOFO
15D80000	OOOFOFOFFFOFF
15E00000	OOOFOFOFFFFOO
15E80000	OOOFOFOFFFFOF
15F00000	OOOFOFOFFFFFO
15F80000	OOOFOFOFFFFFF
16000000	OOOFOFFOOOOOO
16080000	OOOFOFFOOOOOF
16100000	OOOFOFFOOOOFO
16180000	OOOFOFFOOOOFF
16200000	OOOFOFFOOOFOO
16280000	OOOFOFFOOOFOF
16300000	OOOFOFFOOOFFO
16380000	OOOFOFFOOOFFF
16400000	OOOFOFFOOFOOO
16480000	OOOFOFFOOFOOF
16500000	OOOFOFFOOFOFO
16580000	OOOFOFFOOFOFF
16600000	OOOFOFFOOFFOO
16680000	OOOFOFFOOFFOF
16700000	OOOFOFFOOFFFO
16780000	OOOFOFFOOFFFF
16800000	OOOFOFFOFOOOO
16880000	OOOFOFFOFOOOF
16900000	OOOFOFFOFOOFO
16980000	OOOFOFFOFOOFF
16A00000	OOOFOFFOFOFOO
16A80000	OOOFOFFOFOFOF
16B00000	OOOFOFFOFOFFO
16B80000	OOOFOFFOFOFFF
16C00000	OOOFOFFOFFOOO
16C80000	OOOFOFFOFFOOF
16D00000	OOOFOFFOFFOFO
16D80000	OOOFOFFOFFOFF
16E00000	OOOFOFFOFFFOO
16E80000	OOOFOFFOFFFOF
16F00000	OOOFOFFOFFFFO
16F80000	OOOFOFFOFFFFF
17000000	OOOFOFFFOOOOO
17080000	OOOFOFFFOOOOF
17100000	OOOFOFFFOOOFO
17180000	OOOFOFFFOOOFF
17200000	OOOFOFFFOOFOO
17280000	OOOFOFFFOOFOF
17300000	OOOFOFFFOOFFO
17380000	OOOFOFFFOOFFF
17400000	OOOFOFFFOFOOO
17480000	OOOFOFFFOFOOF
17500000	OOOFOFFFOFOFO
17580000	OOOFOFFFOFOFF
17600000	OOOFOFFFOFFOO
17680000	OOOFOFFFOFFOF
17700000	OOOFOFFFOFFFO
17780000	OOOFOFFFOFFFF
17800000	OOOFOFFFFOOOO
17880000	OOOFOFFFFOOOF
17900000	OOOFOFFFFOOFO
17980000	OOOFOFFFFOOFF
17A00000	OOOFOFFFFOFOO
17A80000	OOOFOFFFFOFOF
17B00000	OOOFOFFFFOFFO
17B80000	OOOFOFFFFOFFF
17C00000	OOOFOFFFFFOOO
17C80000	OOOFOFFFFFOOF
17D00000	OOOFOFFFFFOFO
17D80000	OOOFOFFFFFOFF
17E00000	OOOFOFFFFFFOO
17E80000	OOOFOFFFFFFOF
17F00000	OOOFOFFFFFFFO
17F80000	OOOFOFFFFFFFF
18000000	OOOFFOOOOOOOO
18080000	OOOFFOOOOOOOF
18100000	OOOFFOOOOOOFO
18180000	OOOFFOOOOOOFF
18200000	OOOFFOOOOOFOO
18280000	OOOFFOOOOOFOF
18300000	OOOFFOOOOOFFO
18380000	OOOFFOOOOOFFF
18400000	OOOFFOOOOFOOO
18480000	OOOFFOOOOFOOF
18500000	OOOFFOOOOFOFO
18580000	OOOFFOOOOFOFF
18600000	OOOFFOOOOFFOO
18680000	OOOFFOOOOFFOF
18700000	OOOFFOOOOFFFO
18780000	OOOFFOOOOFFFF
18800000	OOOFFOOOFOOOO
18880000	OOOFFOOOFOOOF
18900000	OOOFFOOOFOOFO
18980000	OOOFFOOOFOOFF
18A00000	OOOFFOOOFOFOO
18A80000	OOOFFOOOFOFOF
18B00000	OOOFFOOOFOFFO
18B80000	OOOFFOOOFOFFF
18C00000	OOOFFOOOFFOOO
18C80000	OOOFFOOOFFOOF
18D00000	OOOFFOOOFFOFO
18D80000	OOOFFOOOFFOFF
18E00000	OOOFFOOOFFFOO
18E80000	OOOFFOOOFFFOF
18F00000	OOOFFOOOFFFFO
18F80000	OOOFFOOOFFFFF
19000000	OOOFFOOFOOOOO
19080000	OOOFFOOFOOOOF
19100000	OOOFFOOFOOOFO
19180000	OOOFFOOFOOOFF
19200000	OOOFFOOFOOFOO
19280000	OOOFFOOFOOFOF
19300000	OOOFFOOFOOFFO
19380000	OOOFFOOFOOFFF

APPENDIX D
ADDRESS SWITCH SETTINGS

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
19400000	OOOFFOOFOFOOO
19480000	OOOFFOOFOFOOF
19500000	OOOFFOOFOFOFO
19580000	OOOFFOOFOFOFF
19600000	OOOFFOOFOFFOO
19680000	OOOFFOOFOFFOF
19700000	OOOFFOOFOFFFO
19780000	OOOFFOOFOFFFF
19800000	OOOFFOOFFOOOO
19880000	OOOFFOOFFOOOF
19900000	OOOFFOOFFOOFO
19980000	OOOFFOOFFOOFF
19A00000	OOOFFOOFFOFOO
19A80000	OOOFFOOFFOFOF
19B00000	OOOFFOOFFOFFO
19B80000	OOOFFOOFFOFFF
19C00000	OOOFFOOFFFOOO
19C80000	OOOFFOOFFFOOF
19D00000	OOOFFOOFFFOFO
19D80000	OOOFFOOFFFOFF
19E00000	OOOFFOOFFFFOO
19E80000	OOOFFOOFFFFOF
19F00000	OOOFFOOFFFFFO
19F80000	OOOFFOOFFFFFF
1A000000	OOOFFOFOOOOOO
1A080000	OOOFFOFOOOOOF
1A100000	OOOFFOFOOOOFO
1A180000	OOOFFOFOOOOFF
1A200000	OOOFFOFOOOFOO
1A280000	OOOFFOFOOOFOF
1A300000	OOOFFOFOOOFFO
1A380000	OOOFFOFOOOFFF
1A400000	OOOFFOFOOFOOO
1A480000	OOOFFOFOOFOOF
1A500000	OOOFFOFOOFOFO
1A580000	OOOFFOFOOFOFF
1A600000	OOOFFOFOOFFOO
1A680000	OOOFFOFOOFFOF
1A700000	OOOFFOFOOFFFO
1A780000	OOOFFOFOOFFFF
1A800000	OOOFFOFOFOOOO
1A880000	OOOFFOFOFOOOF
1A900000	OOOFFOFOFOOFO
1A980000	OOOFFOFOFOOFF
1AA00000	OOOFFOFOFOFOO
1AA80000	OOOFFOFOFOFOF
1AB00000	OOOFFOFOFOFFO
1AB80000	OOOFFOFOFOFFF
1AC00000	OOOFFOFOFFOOO
1AC80000	OOOFFOFOFFOOF
1AD00000	OOOFFOFOFFOFO
1AD80000	OOOFFOFOFFOFF
1AE00000	OOOFFOFOFFFOO
1AE80000	OOOFFOFOFFFOF
1AF00000	OOOFFOFOFFFFO
1AF80000	OOOFFOFOFFFFF
1B000000	OOOFFOFFOOOOO
1B080000	OOOFFOFFOOOOF
1B100000	OOOFFOFFOOOFO
1B180000	OOOFFOFFOOOFF
1B200000	OOOFFOFFOOFOO
1B280000	OOOFFOFFOOFOF
1B300000	OOOFFOFFOOFFO
1B380000	OOOFFOFFOOFFF
1B400000	OOOFFOFFOFOOO
1B480000	OOOFFOFFOFOOF
1B500000	OOOFFOFFOFOFO
1B580000	OOOFFOFFOFOFF
1B600000	OOOFFOFFOFFOO
1B680000	OOOFFOFFOFFOF
1B700000	OOOFFOFFOFFFO
1B780000	OOOFFOFFOFFFF
1B800000	OOOFFOFFFOOOO
1B880000	OOOFFOFFFOOOF
1B900000	OOOFFOFFFOOFO
1B980000	OOOFFOFFFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
1BA00000	OOOFFOFFFOFOO
1BA80000	OOOFFOFFFOFOF
1BB00000	OOOFFOFFFOFFO
1BB80000	OOOFFOFFFOFFF
1BC00000	OOOFFOFFFFOOO
1BC80000	OOOFFOFFFFOOF
1BD00000	OOOFFOFFFFOFO
1BD80000	OOOFFOFFFFOFF
1BE00000	OOOFFOFFFFFOO
1BE80000	OOOFFOFFFFFOF
1BF00000	OOOFFOFFFFFFO
1BF80000	OOOFFOFFFFFFF
1C000000	OOOFFFOOOOOOO
1C080000	OOOFFFOOOOOOF
1C100000	OOOFFFOOOOOFO
1C180000	OOOFFFOOOOOFF
1C200000	OOOFFFOOOOFOO
1C280000	OOOFFFOOOOFOF
1C300000	OOOFFFOOOOFFO
1C380000	OOOFFFOOOOFFF
1C400000	OOOFFFOOOFOOO
1C480000	OOOFFFOOOFOOF
1C500000	OOOFFFOOOFOFO
1C580000	OOOFFFOOOFOFF
1C600000	OOOFFFOOOFFOO
1C680000	OOOFFFOOOFFOF
1C700000	OOOFFFOOOFFFO
1C780000	OOOFFFOOOFFFF
1C800000	OOOFFFOOFOOOO
1C880000	OOOFFFOOFOOOF
1C900000	OOOFFFOOFOOFO
1C980000	OOOFFFOOFOOFF
1CA00000	OOOFFFOOFOFOO
1CA80000	OOOFFFOOFOFOF
1CB00000	OOOFFFOOFOFFO
1CB80000	OOOFFFOOFOFFF
1CC00000	OOOFFFOOFFOOO
1CC80000	OOOFFFOOFFOOF
1CD00000	OOOFFFOOFFOFO
1CD80000	OOOFFFOOFFOFF
1CE00000	OOOFFFOOFFFOO
1CE80000	OOOFFFOOFFFOF
1CF00000	OOOFFFOOFFFFO
1CF80000	OOOFFFOOFFFFF
1D000000	OOOFFFOFOOOOO
1D080000	OOOFFFOFOOOOF
1D100000	OOOFFFOFOOOFO
1D180000	OOOFFFOFOOOFF
1D200000	OOOFFFOFOOFOO
1D280000	OOOFFFOFOOFOF
1D300000	OOOFFFOFOOFFO
1D380000	OOOFFFOFOOFFF
1D400000	OOOFFFOFOFOOO
1D480000	OOOFFFOFOFOOF
1D500000	OOOFFFOFOFOFO
1D580000	OOOFFFOFOFOFF
1D600000	OOOFFFOFOFFOO
1D680000	OOOFFFOFOFFOF
1D700000	OOOFFFOFOFFFO
1D780000	OOOFFFOFOFFFF
1D800000	OOOFFFOFFOOOO
1D880000	OOOFFFOFFOOOF
1D900000	OOOFFFOFFOOFO
1D980000	OOOFFFOFFOOFF
1DA00000	OOOFFFOFFOFOO
1DA80000	OOOFFFOFFOFOF
1DB00000	OOOFFFOFFOFFO
1DB80000	OOOFFFOFFOFFF
1DC00000	OOOFFFOFFFOOO
1DC80000	OOOFFFOFFFOOF
1DD00000	OOOFFFOFFFOFO
1DD80000	OOOFFFOFFFOFF
1DE00000	OOOFFFOFFFFOO
1DE80000	OOOFFFOFFFFOF
1DF00000	OOOFFFOFFFFFO
1DF80000	OOOFFFOFFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
1E000000	OOOFFFFOOOOOO
1E080000	OOOFFFFOOOOOF
1E100000	OOOFFFFOOOOFO
1E180000	OOOFFFFOOOOFF
1E200000	OOOFFFFOOOFOO
1E280000	OOOFFFFOOOFOF
1E300000	OOOFFFFOOOFFO
1E380000	OOOFFFFOOOFFF
1E400000	OOOFFFFOOFOOO
1E480000	OOOFFFFOOFOOF
1E500000	OOOFFFFOOFOFO
1E580000	OOOFFFFOOFOFF
1E600000	OOOFFFFOOFFOO
1E680000	OOOFFFFOOFFOF
1E700000	OOOFFFFOOFFFO
1E780000	OOOFFFFOOFFFF
1E800000	OOOFFFFOFOOOO
1E880000	OOOFFFFOFOOOF
1E900000	OOOFFFFOFOOFO
1E980000	OOOFFFFOFOOFF
1EA00000	OOOFFFFOFOFOO
1EA80000	OOOFFFFOFOFOF
1EB00000	OOOFFFFOFOFFO
1EB80000	OOOFFFFOFOFFF
1EC00000	OOOFFFFOFFOOO
1EC80000	OOOFFFFOFFOOF
1ED00000	OOOFFFFOFFOFO
1ED80000	OOOFFFFOFFOFF
1EE00000	OOOFFFFOFFFOO
1EE80000	OOOFFFFOFFFOF
1EF00000	OOOFFFFOFFFFO
1EF80000	OOOFFFFOFFFFF
1F000000	OOOFFFFFOOOOO
1F080000	OOOFFFFFOOOOF
1F100000	OOOFFFFFOOOFO
1F180000	OOOFFFFFOOOFF
1F200000	OOOFFFFFOOFOO
1F280000	OOOFFFFFOOFOF
1F300000	OOOFFFFFOOFFO
1F380000	OOOFFFFFOOFFF
1F400000	OOOFFFFFOFOOO
1F480000	OOOFFFFFOFOOF
1F500000	OOOFFFFFOFOFO
1F580000	OOOFFFFFOFOFF
1F600000	OOOFFFFFOFFOO
1F680000	OOOFFFFFOFFOF
1F700000	OOOFFFFFOFFFO
1F780000	OOOFFFFFOFFFF
1F800000	OOOFFFFFFOOOO
1F880000	OOOFFFFFFOOOF
1F900000	OOOFFFFFFOOFO
1F980000	OOOFFFFFFOOFF
1FA00000	OOOFFFFFFOFOO
1FA80000	OOOFFFFFFOFOF
1FB00000	OOOFFFFFFOFFO
1FB80000	OOOFFFFFFOFFF
1FC00000	OOOFFFFFFFOOO
1FC80000	OOOFFFFFFFOOF
1FD00000	OOOFFFFFFFOFO
1FD80000	OOOFFFFFFFOFF
1FE00000	OOOFFFFFFFFOO
1FE80000	OOOFFFFFFFFOF
1FF00000	OOOFFFFFFFFFO
1FF80000	OOOFFFFFFFFFF
20000000	OOFOOOOOOOOOO
20080000	OOFOOOOOOOOOF
20100000	OOFOOOOOOOOFO
20180000	OOFOOOOOOOOFF
20200000	OOFOOOOOOOFOO
20280000	OOFOOOOOOOFOF
20300000	OOFOOOOOOOFFO
20380000	OOFOOOOOOOFFF
20400000	OOFOOOOOOFOOO
20480000	OOFOOOOOOFOOF
20500000	OOFOOOOOOFOFO
20580000	OOFOOOOOOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
20600000	OOFOOOOOOFFOO
20680000	OOFOOOOOOFFOF
20700000	OOFOOOOOOFFFO
20780000	OOFOOOOOOFFFF
20800000	OOFOOOOOFOOOO
20880000	OOFOOOOOFOOOF
20900000	OOFOOOOOFOOFO
20980000	OOFOOOOOFOOFF
20A00000	OOFOOOOOFOFOO
20A80000	OOFOOOOOFOFOF
20B00000	OOFOOOOOFOFFO
20B80000	OOFOOOOOFOFFF
20C00000	OOFOOOOOFFOOO
20C80000	OOFOOOOOFFOOF
20D00000	OOFOOOOOFFOFO
20D80000	OOFOOOOOFFOFF
20E00000	OOFOOOOOFFFOO
20E80000	OOFOOOOOFFFOF
20F00000	OOFOOOOOFFFFO
20F80000	OOFOOOOOFFFFF
21000000	OOFOOOOFOOOOO
21080000	OOFOOOOFOOOOF
21100000	OOFOOOOFOOOFO
21180000	OOFOOOOFOOOFF
21200000	OOFOOOOFOOFOO
21280000	OOFOOOOFOOFOF
21300000	OOFOOOOFOOFFO
21380000	OOFOOOOFOOFFF
21400000	OOFOOOOFOFOOO
21480000	OOFOOOOFOFOOF
21500000	OOFOOOOFOFOFO
21580000	OOFOOOOFOFOFF
21600000	OOFOOOOFOFFOO
21680000	OOFOOOOFOFFOF
21700000	OOFOOOOFOFFFO
21780000	OOFOOOOFOFFFF
21800000	OOFOOOOFFOOOO
21880000	OOFOOOOFFOOOF
21900000	OOFOOOOFFOOFO
21980000	OOFOOOOFFOOFF
21A00000	OOFOOOOFFOFOO
21A80000	OOFOOOOFFOFOF
21B00000	OOFOOOOFFOFFO
21B80000	OOFOOOOFFOFFF
21C00000	OOFOOOOFFFOOO
21C80000	OOFOOOOFFFOOF
21D00000	OOFOOOOFFFOFO
21D80000	OOFOOOOFFFOFF
21E00000	OOFOOOOFFFFOO
21E80000	OOFOOOOFFFFOF
21F00000	OOFOOOOFFFFFO
21F80000	OOFOOOOFFFFFF
22000000	OOFOOOFOOOOOO
22080000	OOFOOOFOOOOOF
22100000	OOFOOOFOOOOFO
22180000	OOFOOOFOOOOFF
22200000	OOFOOOFOOOFOO
22280000	OOFOOOFOOOFOF
22300000	OOFOOOFOOOFFO
22380000	OOFOOOFOOOFFF
22400000	OOFOOOFOOFOOO
22480000	OOFOOOFOOFOOF
22500000	OOFOOOFOOFOFO
22580000	OOFOOOFOOFOFF
22600000	OOFOOOFOOFFOO
22680000	OOFOOOFOOFFOF
22700000	OOFOOOFOOFFFO
22780000	OOFOOOFOOFFFF
22800000	OOFOOOFOFOOOO
22880000	OOFOOOFOFOOOF
22900000	OOFOOOFOFOOFO
22980000	OOFOOOFOFOOFF
22A00000	OOFOOOFOFOFOO
22A80000	OOFOOOFOFOFOF
22B00000	OOFOOOFOFOFFO
22B80000	OOFOOOFOFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
22C00000	OOFOOOFOFFOOO
22C80000	OOFOOOFOFFOOF
22D00000	OOFOOOFOFFOFO
22D80000	OOFOOOFOFFOFF
22E00000	OOFOOOFOFFFOO
22E80000	OOFOOOFOFFFOF
22F00000	OOFOOOFOFFFFO
22F80000	OOFOOOFOFFFFF
23000000	OOFOOOFFOOOOO
23080000	OOFOOOFFOOOOF
23100000	OOFOOOFFOOOFO
23180000	OOFOOOFFOOOFF
23200000	OOFOOOFFOOFOO
23280000	OOFOOOFFOOFOF
23300000	OOFOOOFFOOFFO
23380000	OOFOOOFFOOFFF
23400000	OOFOOOFFOFOOO
23480000	OOFOOOFFOFOOF
23500000	OOFOOOFFOFOFO
23580000	OOFOOOFFOFOFF
23600000	OOFOOOFFOFFOO
23680000	OOFOOOFFOFFOF
23700000	OOFOOOFFOFFFO
23780000	OOFOOOFFOFFFF
23800000	OOFOOOFFFOOOO
23880000	OOFOOOFFFOOOF
23900000	OOFOOOFFFOOFO
23980000	OOFOOOFFFOOFF
23A00000	OOFOOOFFFOFOO
23A80000	OOFOOOFFFOFOF
23B00000	OOFOOOFFFOFFO
23B80000	OOFOOOFFFOFFF
23C00000	OOFOOOFFFFOOO
23C80000	OOFOOOFFFFOOF
23D00000	OOFOOOFFFFOFO
23D80000	OOFOOOFFFFOFF
23E00000	OOFOOOFFFFFOO
23E80000	OOFOOOFFFFFOF
23F00000	OOFOOOFFFFFFO
23F80000	OOFOOOFFFFFFF
24000000	OOFOOFOOOOOOO
24080000	OOFOOFOOOOOOF
24100000	OOFOOFOOOOOFO
24180000	OOFOOFOOOOOFF
24200000	OOFOOFOOOOFOO
24280000	OOFOOFOOOOFOF
24300000	OOFOOFOOOOFFO
24380000	OOFOOFOOOOFFF
24400000	OOFOOFOOOFOOO
24480000	OOFOOFOOOFOOF
24500000	OOFOOFOOOFOFO
24580000	OOFOOFOOOFOFF
24600000	OOFOOFOOOFFOO
24680000	OOFOOFOOOFFOF
24700000	OOFOOFOOOFFFO
24780000	OOFOOFOOOFFFF
24800000	OOFOOFOOFOOOO
24880000	OOFOOFOOFOOOF
24900000	OOFOOFOOFOOFO
24980000	OOFOOFOOFOOFF
24A00000	OOFOOFOOFOFOO
24A80000	OOFOOFOOFOFOF
24B00000	OOFOOFOOFOFFO
24B80000	OOFOOFOOFOFFF
24C00000	OOFOOFOOFFOOO
24C80000	OOFOOFOOFFOOF
24D00000	OOFOOFOOFFOFO
24D80000	OOFOOFOOFFOFF
24E00000	OOFOOFOOFFFOO
24E80000	OOFOOFOOFFFOF
24F00000	OOFOOFOOFFFFO
24F80000	OOFOOFOOFFFFF
25000000	OOFOOFOFOOOOO
25080000	OOFOOFOFOOOOF
25100000	OOFOOFOFOOOFO
25180000	OOFOOFOFOOOFF
25200000	OOFOOFOFOOFOO
25280000	OOFOOFOFOOFOF
25300000	OOFOOFOFOOFFO
25380000	OOFOOFOFOOFFF
25400000	OOFOOFOFOFOOO
25480000	OOFOOFOFOFOOF
25500000	OOFOOFOFOFOFO
25580000	OOFOOFOFOFOFF
25600000	OOFOOFOFOFFOO
25680000	OOFOOFOFOFFOF
25700000	OOFOOFOFOFFFO
25780000	OOFOOFOFOFFFF
25800000	OOFOOFOFFOOOO
25880000	OOFOOFOFFOOOF
25900000	OOFOOFOFFOFFO
25980000	OOFOOFOFFOFFF
25A00000	OOFOOFOFFFOOO
25A80000	OOFOOFOFFFOOF
25B00000	OOFOOFOFFFOFO
25B80000	OOFOOFOFFFOFF
25C00000	OOFOOFOFFFFOO
25C80000	OOFOOFOFFFFOF
25D00000	OOFOOFOFFFFFO
25D80000	OOFOOFOFFFFFF
25E00000	OOFOOFOFFFFOO
25E80000	OOFOOFOFFFFOF
25F00000	OOFOOFOFFFFFO
25F80000	OOFOOFOFFFFFF
26000000	OOFOOFFOOOOOO
26080000	OOFOOFFOOOOOF
26100000	OOFOOFFOOOOFO
26180000	OOFOOFFOOOOFF
26200000	OOFOOFFOOOFOO
26280000	OOFOOFFOOOFOF
26300000	OOFOOFFOOOFFO
26380000	OOFOOFFOOOFFF
26400000	OOFOOFFOOFOOO
26480000	OOFOOFFOOFOOF
26500000	OOFOOFFOOFOFO
26580000	OOFOOFFOOFOFF
26600000	OOFOOFFOOFFOO
26680000	OOFOOFFOOFFOF
26700000	OOFOOFFOOFFFO
26780000	OOFOOFFOOFFFF
26800000	OOFOOFFOFOOOO
26880000	OOFOOFFOFOOOF
26900000	OOFOOFFOFOOFO
26980000	OOFOOFFOFOOFF
26A00000	OOFOOFFOFOFOO
26A80000	OOFOOFFOFOFOF
26B00000	OOFOOFFOFOFFO
26B80000	OOFOOFFOFOFFF
26C00000	OOFOOFFOFFOOO
26C80000	OOFOOFFOFFOOF
26D00000	OOFOOFFOFFOFO
26D80000	OOFOOFFOFFOFF
26E00000	OOFOOFFOFFFOO
26E80000	OOFOOFFOFFFOF
26F00000	OOFOOFFOFFFFO
26F80000	OOFOOFFOFFFFF
27000000	OOFOOFFFOOOOO
27080000	OOFOOFFFOOOOF
27100000	OOFOOFFFOOOFO
27180000	OOFOOFFFOOOFF
27200000	OOFOOFFFOOFOO
27280000	OOFOOFFFOOFOF
27300000	OOFOOFFFOOFFO
27380000	OOFOOFFFOOFFF
27400000	OOFOOFFFOFOOO
27480000	OOFOOFFFOFOOF
27500000	OOFOOFFFOFOFO
27580000	OOFOOFFFOFOFF
27600000	OOFOOFFFOFFOO
27680000	OOFOOFFFOFFOF
27700000	OOFOOFFFOFFFO
27780000	OOFOOFFFOFFFF
27800000	OOFOOFFFFOOOO
27880000	OOFOOFFFFOOOF
27900000	OOFOOFFFFOOFO
27980000	OOFOOFFFFOOFF
27A00000	OOFOOFFFFOFOO
27A80000	OOFOOFFFFOFOF
27B00000	OOFOOFFFFOFFO
27B80000	OOFOOFFFFOFFF
27C00000	OOFOOFFFFFOOO
27C80000	OOFOOFFFFFOOF
27D00000	OOFOOFFFFFOFO
27D80000	OOFOOFFFFFOFF
27E00000	OOFOOFFFFFFOO
27E80000	OOFOOFFFFFFOF
27F00000	OOFOOFFFFFFFO
27F80000	OOFOOFFFFFFFF
28000000	OOFOFOOOOOOOO
28080000	OOFOFOOOOOOOF
28100000	OOFOFOOOOOOFO
28180000	OOFOFOOOOOOFF
28200000	OOFOFOOOOOFOO
28280000	OOFOFOOOOOFOF
28300000	OOFOFOOOOOFFO
28380000	OOFOFOOOOOFFF
28400000	OOFOFOOOOFOOO
28480000	OOFOFOOOOFOOF
28500000	OOFOFOOOOFOFO
28580000	OOFOFOOOOFOFF
28600000	OOFOFOOOOFFOO
28680000	OOFOFOOOOFFOF
28700000	OOFOFOOOOFFFO
28780000	OOFOFOOOOFFFF
28800000	OOFOFOOOFOOOO
28880000	OOFOFOOOFOOOF
28900000	OOFOFOOOFOOFO
28980000	OOFOFOOOFOOFF
28A00000	OOFOFOOOFOFOO
28A80000	OOFOFOOOFOFOF
28B00000	OOFOFOOOFOFFO
28B80000	OOFOFOOOFOFFF
28C00000	OOFOFOOOFFOOO
28C80000	OOFOFOOOFFOOF
28D00000	OOFOFOOOFFOFO
28D80000	OOFOFOOOFFOFF
28E00000	OOFOFOOOFFFOO
28E80000	OOFOFOOOFFFOF
28F00000	OOFOFOOOFFFFO
28F80000	OOFOFOOOFFFFF
29000000	OOFOFOOFOOOOO
29080000	OOFOFOOFOOOOF
29100000	OOFOFOOFOOOFO
29180000	OOFOFOOFOOOFF
29200000	OOFOFOOFOOFOO
29280000	OOFOFOOFOOFOF
29300000	OOFOFOOFOOFFO
29380000	OOFOFOOFOOFFF
29400000	OOFOFOOFOFOOO
29480000	OOFOFOOFOFOOF
29500000	OOFOFOOFOFOFO
29580000	OOFOFOOFOFOFF
29600000	OOFOFOOFOFFOO
29680000	OOFOFOOFOFFOF
29700000	OOFOFOOFOFFFO
29780000	OOFOFOOFOFFFF
29800000	OOFOFOOFFOOOO
29880000	OOFOFOOFFOOOF
29900000	OOFOFOOFFOOFO
29980000	OOFOFOOFFOOFF
29A00000	OOFOFOOFFOFOO
29A80000	OOFOFOOFFOFOF
29B00000	OOFOFOOFFOFFO
29B80000	OOFOFOOFFOFFF
29C00000	OOFOFOOFFFOOO
29C80000	OOFOFOOFFFOOF
29D00000	OOFOFOOFFFOFO
29D80000	OOFOFOOFFFOFF
29E00000	OOFOFOOFFFFOO
29E80000	OOFOFOOFFFFOF
29F00000	OOFOFOOFFFFFO
29F80000	OOFOFOOFFFFFF
2A000000	OOFOFOFOOOOOO
2A080000	OOFOFOFOOOOOF
2A100000	OOFOFOFOOOOFO
2A180000	OOFOFOFOOOOFF
2A200000	OOFOFOFOOOFOO
2A280000	OOFOFOFOOOFOF
2A300000	OOFOFOFOOOFFO
2A380000	OOFOFOFOOOFFF
2A400000	OOFOFOFOOFOOO
2A480000	OOFOFOFOOFOOF
2A500000	OOFOFOFOOFOFO
2A580000	OOFOFOFOOFOFF
2A600000	OOFOFOFOOFFOO
2A680000	OOFOFOFOOFFOF
2A700000	OOFOFOFOOFFFO
2A780000	OOFOFOFOOFFFF
2A800000	OOFOFOFOFOOOO
2A880000	OOFOFOFOFOOOF
2A900000	OOFOFOFOFOOFO
2A980000	OOFOFOFOFOOFF
2AA00000	OOFOFOFOFOFOO
2AA80000	OOFOFOFOFOFOF
2AB00000	OOFOFOFOFOFFO
2AB80000	OOFOFOFOFOFFF
2AC00000	OOFOFOFOFFOOO
2AC80000	OOFOFOFOFFOOF
2AD00000	OOFOFOFOFFOFO
2AD80000	OOFOFOFOFFOFF
2AE00000	OOFOFOFOFFFOO
2AE80000	OOFOFOFOFFFOF
2AF00000	OOFOFOFOFFFFO
2AF80000	OOFOFOFOFFFFF
2B000000	OOFOFOFFOOOOO
2B080000	OOFOFOFFOOOOF
2B100000	OOFOFOFFOOOFO
2B180000	OOFOFOFFOOOFF
2B200000	OOFOFOFFOOFOO
2B280000	OOFOFOFFOOFOF
2B300000	OOFOFOFFOOFFO
2B380000	OOFOFOFFOOFFF
2B400000	OOFOFOFFOFOOO
2B480000	OOFOFOFFOFOOF
2B500000	OOFOFOFFOFOFO
2B580000	OOFOFOFFOFOFF
2B600000	OOFOFOFFOFFOO
2B680000	OOFOFOFFOFFOF
2B700000	OOFOFOFFOFFFO
2B780000	OOFOFOFFOFFFF
2B800000	OOFOFOFFFOOOO
2B880000	OOFOFOFFFOOOF
2B900000	OOFOFOFFFOOFO
2B980000	OOFOFOFFFOOFF
2BA00000	OOFOFOFFFOFOO
2BA80000	OOFOFOFFFOFOF
2BB00000	OOFOFOFFFOFFO
2BB80000	OOFOFOFFFOFFF
2BC00000	OOFOFOFFFFOOO
2BC80000	OOFOFOFFFFOOF
2BD00000	OOFOFOFFFFOFO
2BD80000	OOFOFOFFFFOFF
2BE00000	OOFOFOFFFFFOO
2BE80000	OOFOFOFFFFFOF
2BF00000	OOFOFOFFFFFFO
2BF80000	OOFOFOFFFFFFF
2C000000	OOFOFFOOOOOOO
2C080000	OOFOFFOOOOOOF
2C100000	OOFOFFOOOOOFO
2C180000	OOFOFFOOOOOFF
2C200000	OOFOFFOOOOFOO
2C280000	OOFOFFOOOOFOF
2C300000	OOFOFFOOOOFFO
2C380000	OOFOFFOOOOFFF

APPENDIX D
ADDRESS SWITCH SETTINGS

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
2C400000	OOFOFFOOOFOOO
2C480000	OOFOFFOOOFOOF
2C500000	OOFOFFOOOFOFO
2C580000	OOFOFFOOOFOFF
2C600000	OOFOFFOOOFFOO
2C680000	OOFOFFOOOFFOF
2C700000	OOFOFFOOOFFFO
2C780000	OOFOFFOOOFFFF
2C800000	OOFOFFOOFOOOO
2C880000	OOFOFFOOFOOOF
2C900000	OOFOFFOOFOOFO
2C980000	OOFOFFOOFOOFF
2CA00000	OOFOFFOOFOFOO
2CA80000	OOFOFFOOFOFOF
2CB00000	OOFOFFOOFOFFO
2CB80000	OOFOFFOOFOFFF
2CC00000	OOFOFFOOFFOOO
2CC80000	OOFOFFOOFFOOF
2CD00000	OOFOFFOOFFOFO
2CD80000	OOFOFFOOFFOFF
2CE00000	OOFOFFOOFFFOO
2CE80000	OOFOFFOOFFFOF
2CF00000	OOFOFFOOFFFFO
2CF80000	OOFOFFOOFFFFF
2D000000	OOFOFFOFOOOOO
2D080000	OOFOFFOFOOOOF
2D100000	OOFOFFOFOOOFO
2D180000	OOFOFFOFOOOFF
2D200000	OOFOFFOFOOFOO
2D280000	OOFOFFOFOOFOF
2D300000	OOFOFFOFOOFFO
2D380000	OOFOFFOFOOFFF
2D400000	OOFOFFOFOFOOO
2D480000	OOFOFFOFOFOOF
2D500000	OOFOFFOFOFOFO
2D580000	OOFOFFOFOFOFF
2D600000	OOFOFFOFOFFOO
2D680000	OOFOFFOFOFFOF
2D700000	OOFOFFOFOFFFO
2D780000	OOFOFFOFOFFFF
2D800000	OOFOFFOFFOOOO
2D880000	OOFOFFOFFOOOF
2D900000	OOFOFFOFFOOFO
2D980000	OOFOFFOFFOOFF
2DA00000	OOFOFFOFFOFOO
2DA80000	OOFOFFOFFOFOF
2DB00000	OOFOFFOFFOFFO
2DB80000	OOFOFFOFFOFFF
2DC00000	OOFOFFOFFFOOO
2DC80000	OOFOFFOFFFOOF
2DD00000	OOFOFFOFFFOFO
2DD80000	OOFOFFOFFFOFF
2DE00000	OOFOFFOFFFFOO
2DE80000	OOFOFFOFFFFOF
2DF00000	OOFOFFOFFFFFO
2DF80000	OOFOFFOFFFFFF
2E000000	OOFOFFFOOOOOO
2E080000	OOFOFFFOOOOOF
2E100000	OOFOFFFOOOOFO
2E180000	OOFOFFFOOOOFF
2E200000	OOFOFFFOOOFOO
2E280000	OOFOFFFOOOFOF
2E300000	OOFOFFFOOOFFO
2E380000	OOFOFFFOOOFFF
2E400000	OOFOFFFOOFOOO
2E480000	OOFOFFFOOFOOF
2E500000	OOFOFFFOOFOFO
2E580000	OOFOFFFOOFOFF
2E600000	OOFOFFFOOFFOO
2E680000	OOFOFFFOOFFOF
2E700000	OOFOFFFOOFFFO
2E780000	OOFOFFFOOFFFF
2E800000	OOFOFFFOFOOOO
2E880000	OOFOFFFOFOOOF
2E900000	OOFOFFFOFOOFO
2E980000	OOFOFFFOFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
2EA00000	OOFOFFFOFOFOO
2EA80000	OOFOFFFOFOFOF
2EB00000	OOFOFFFOFOFFO
2EB80000	OOFOFFFOFOFFF
2EC00000	OOFOFFFOFFOOO
2EC80000	OOFOFFFOFFOOF
2ED00000	OOFOFFFOFFOFO
2ED80000	OOFOFFFOFFOFF
2EE00000	OOFOFFFOFFFOO
2EE80000	OOFOFFFOFFFOF
2EF00000	OOFOFFFOFFFFO
2EF80000	OOFOFFFOFFFFF
2F000000	OOFOFFFFOOOOO
2F080000	OOFOFFFFOOOOF
2F100000	OOFOFFFFOOOFO
2F180000	OOFOFFFFOOOFF
2F200000	OOFOFFFFOOFOO
2F280000	OOFOFFFFOOFOF
2F300000	OOFOFFFFOOFFO
2F380000	OOFOFFFFOOFFF
2F400000	OOFOFFFFOFOOO
2F480000	OOFOFFFFOFOOF
2F500000	OOFOFFFFOFOFO
2F580000	OOFOFFFFOFOFF
2F600000	OOFOFFFFOFFOO
2F680000	OOFOFFFFOFFOF
2F700000	OOFOFFFFOFFFO
2F780000	OOFOFFFFOFFFF
2F800000	OOFOFFFFFOOOO
2F880000	OOFOFFFFFOOOF
2F900000	OOFOFFFFFOOFO
2F980000	OOFOFFFFFOOFF
2FA00000	OOFOFFFFFOFOO
2FA80000	OOFOFFFFFOFOF
2FB00000	OOFOFFFFFOFFO
2FB80000	OOFOFFFFFOFFF
2FC00000	OOFOFFFFFFOOO
2FC80000	OOFOFFFFFFOOF
2FD00000	OOFOFFFFFFOFO
2FD80000	OOFOFFFFFFOFF
2FE00000	OOFOFFFFFFFOO
2FE80000	OOFOFFFFFFFOF
2FF00000	OOFOFFFFFFFFO
2FF80000	OOFOFFFFFFFFF
30000000	OOFFOOOOOOOOO
30080000	OOFFOOOOOOOOF
30100000	OOFFOOOOOOOFO
30180000	OOFFOOOOOOOFF
30200000	OOFFOOOOOOFOO
30280000	OOFFOOOOOOFOF
30300000	OOFFOOOOOOFFO
30380000	OOFFOOOOOOFFF
30400000	OOFFOOOOOFOOO
30480000	OOFFOOOOOFOOF
30500000	OOFFOOOOOFOFO
30580000	OOFFOOOOOFOFF
30600000	OOFFOOOOOFFOO
30680000	OOFFOOOOOFFOF
30700000	OOFFOOOOOFFFO
30780000	OOFFOOOOOFFFF
30800000	OOFFOOOOFOOOO
30880000	OOFFOOOOFOOOF
30900000	OOFFOOOOFOOFO
30980000	OOFFOOOOFOOFF
30A00000	OOFFOOOOFOFOO
30A80000	OOFFOOOOFOFOF
30B00000	OOFFOOOOFOFFO
30B80000	OOFFOOOOFOFFF
30C00000	OOFFOOOOFFOOO
30C80000	OOFFOOOOFFOOF
30D00000	OOFFOOOOFFOFO
30D80000	OOFFOOOOFFOFF
30E00000	OOFFOOOOFFFOO
30E80000	OOFFOOOOFFFOF
30F00000	OOFFOOOOFFFFO
30F80000	OOFFOOOOFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
31000000	OOFFOOOFOOOOO
31080000	OOFFOOOFOOOOF
31100000	OOFFOOOFOOOFO
31180000	OOFFOOOFOOOFF
31200000	OOFFOOOFOOFOO
31280000	OOFFOOOFOOFOF
31300000	OOFFOOOFOOFFO
31380000	OOFFOOOFOOFFF
31400000	OOFFOOOFOFOOO
31480000	OOFFOOOFOFOOF
31500000	OOFFOOOFOFOFO
31580000	OOFFOOOFOFOFF
31600000	OOFFOOOFOFFOO
31680000	OOFFOOOFOFFOF
31700000	OOFFOOOFOFFFO
31780000	OOFFOOOFOFFFF
31800000	OOFFOOOFFOOOO
31880000	OOFFOOOFFOOOF
31900000	OOFFOOOFFOOFO
31980000	OOFFOOOFFOOFF
31A00000	OOFFOOOFFOFOO
31A80000	OOFFOOOFFOFOF
31B00000	OOFFOOOFFOFFO
31B80000	OOFFOOOFFOFFF
31C00000	OOFFOOOFFFOOO
31C80000	OOFFOOOFFFOOF
31D00000	OOFFOOOFFFOFO
31D80000	OOFFOOOFFFOFF
31E00000	OOFFOOOFFFFOO
31E80000	OOFFOOOFFFFOF
31F00000	OOFFOOOFFFFFO
31F80000	OOFFOOOFFFFFF
32000000	OOFFOOFOOOOOO
32080000	OOFFOOFOOOOOF
32100000	OOFFOOFOOOOFO
32180000	OOFFOOFOOOOFF
32200000	OOFFOOFOOOFOO
32280000	OOFFOOFOOOFOF
32300000	OOFFOOFOOOFFO
32380000	OOFFOOFOOOFFF
32400000	OOFFOOFOOFOOO
32480000	OOFFOOFOOFOOF
32500000	OOFFOOFOOFOFO
32580000	OOFFOOFOOFOFF
32600000	OOFFOOFOOFFOO
32680000	OOFFOOFOOFFOF
32700000	OOFFOOFOOFFFO
32780000	OOFFOOFOOFFFF
32800000	OOFFOOFOFOOOO
32880000	OOFFOOFOFOOOF
32900000	OOFFOOFOFOOFO
32980000	OOFFOOFOFOOFF
32A00000	OOFFOOFOFOFOO
32A80000	OOFFOOFOFOFOF
32B00000	OOFFOOFOFOFFO
32B80000	OOFFOOFOFOFFF
32C00000	OOFFOOFOFFOOO
32C80000	OOFFOOFOFFOOF
32D00000	OOFFOOFOFFOFO
32D80000	OOFFOOFOFFOFF
32E00000	OOFFOOFOFFFOO
32E80000	OOFFOOFOFFFOF
32F00000	OOFFOOFOFFFFO
32F80000	OOFFOOFOFFFFF
33000000	OOFFOOFFOOOOO
33080000	OOFFOOFFOOOOF
33100000	OOFFOOFFOOOFO
33180000	OOFFOOFFOOOFF
33200000	OOFFOOFFOOFOO
33280000	OOFFOOFFOOFOF
33300000	OOFFOOFFOOFFO
33380000	OOFFOOFFOOFFF
33400000	OOFFOOFFOFOOO
33480000	OOFFOOFFOFOOF
33500000	OOFFOOFFOFOFO
33580000	OOFFOOFFOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
33600000	OOFFOOFFOFFOO
33680000	OOFFOOFFOFFOF
33700000	OOFFOOFFOFFFO
33780000	OOFFOOFFOFFFF
33800000	OOFFOOFFFOOOO
33880000	OOFFOOFFFOOOF
33900000	OOFFOOFFFOOFO
33980000	OOFFOOFFFOOFF
33A00000	OOFFOOFFFOFOO
33A80000	OOFFOOFFFOFOF
33B00000	OOFFOOFFFOFFO
33B80000	OOFFOOFFFOFFF
33C00000	OOFFOOFFFFOOO
33C80000	OOFFOOFFFFOOF
33D00000	OOFFOOFFFFOFO
33D80000	OOFFOOFFFFOFF
33E00000	OOFFOOFFFFFOO
33E80000	OOFFOOFFFFFOF
33F00000	OOFFOOFFFFFFO
33F80000	OOFFOOFFFFFFF
34000000	OOFFOFOOOOOOO
34080000	OOFFOFOOOOOOF
34100000	OOFFOFOOOOOFO
34180000	OOFFOFOOOOOFF
34200000	OOFFOFOOOOFOO
34280000	OOFFOFOOOOFOF
34300000	OOFFOFOOOOFFO
34380000	OOFFOFOOOOFFF
34400000	OOFFOFOOOFOOO
34480000	OOFFOFOOOFOOF
34500000	OOFFOFOOOFOFO
34580000	OOFFOFOOOFOFF
34600000	OOFFOFOOOFFOO
34680000	OOFFOFOOOFFOF
34700000	OOFFOFOOOFFFO
34780000	OOFFOFOOOFFFF
34800000	OOFFOFOOFOOOO
34880000	OOFFOFOOFOOOF
34900000	OOFFOFOOFOOFO
34980000	OOFFOFOOFOOFF
34A00000	OOFFOFOOFOFOO
34A80000	OOFFOFOOFOFOF
34B00000	OOFFOFOOFOFFO
34B80000	OOFFOFOOFOFFF
34C00000	OOFFOFOOFFOOO
34C80000	OOFFOFOOFFOOF
34D00000	OOFFOFOOFFOFO
34D80000	OOFFOFOOFFOFF
34E00000	OOFFOFOOFFFOO
34E80000	OOFFOFOOFFFOF
34F00000	OOFFOFOOFFFFO
34F80000	OOFFOFOOFFFFF
35000000	OOFFOFOFOOOOO
35080000	OOFFOFOFOOOOF
35100000	OOFFOFOFOOOFO
35180000	OOFFOFOFOOOFF
35200000	OOFFOFOFOOFOO
35280000	OOFFOFOFOOFOF
35300000	OOFFOFOFOOFFO
35380000	OOFFOFOFOOFFF
35400000	OOFFOFOFOFOOO
35480000	OOFFOFOFOFOOF
35500000	OOFFOFOFOFOFO
35580000	OOFFOFOFOFOFF
35600000	OOFFOFOFOFFOO
35680000	OOFFOFOFOFFOF
35700000	OOFFOFOFOFFFO
35780000	OOFFOFOFOFFFF
35800000	OOFFOFOFFOOOO
35880000	OOFFOFOFFOOOF
35900000	OOFFOFOFFOOFO
35980000	OOFFOFOFFOOFF
35A00000	OOFFOFOFFOFOO
35A80000	OOFFOFOFFOFOF
35B00000	OOFFOFOFFOFFO
35B80000	OOFFOFOFFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
35C00000	OOFFOFOFFFOOO
35C80000	OOFFOFOFFFOOF
35D00000	OOFFOFOFFFOFO
35D80000	OOFFOFOFFFOFF
35E00000	OOFFOFOFFFFOO
35E80000	OOFFOFOFFFFOF
35F00000	OOFFOFOFFFFFO
35F80000	OOFFOFOFFFFFF
36000000	OOFFOFFOOOOOO
36080000	OOFFOFFOOOOOF
36100000	OOFFOFFOOOOFO
36180000	OOFFOFFOOOOFF
36200000	OOFFOFFOOOFOO
36280000	OOFFOFFOOOFOF
36300000	OOFFOFFOOOFFO
36380000	OOFFOFFOOOFFF
36400000	OOFFOFFOOFOOO
36480000	OOFFOFFOOFOOF
36500000	OOFFOFFOOFOFO
36580000	OOFFOFFOOFOFF
36600000	OOFFOFFOOFFOO
36680000	OOFFOFFOOFFOF
36700000	OOFFOFFOOFFFO
36780000	OOFFOFFOOFFFF
36800000	OOFFOFFOFOOOO
36880000	OOFFOFFOFOOOF
36900000	OOFFOFFOFOOFO
36980000	OOFFOFFOFOOFF
36A00000	OOFFOFFOFOFOO
36A80000	OOFFOFFOFOFOF
36B00000	OOFFOFFOFOFFO
36B80000	OOFFOFFOFOFFF
36C00000	OOFFOFFOFFOOO
36C80000	OOFFOFFOFFOOF
36D00000	OOFFOFFOFFOFO
36D80000	OOFFOFFOFFOFF
36E00000	OOFFOFFOFFFOO
36E80000	OOFFOFFOFFFOF
36F00000	OOFFOFFOFFFFO
36F80000	OOFFOFFOFFFFF
37000000	OOFFOFFFOOOOO
37080000	OOFFOFFFOOOOF
37100000	OOFFOFFFOOOFO
37180000	OOFFOFFFOOOFF
37200000	OOFFOFFFOOFOO
37280000	OOFFOFFFOOFOF
37300000	OOFFOFFFOOFFO
37380000	OOFFOFFFOOFFF
37400000	OOFFOFFFOFOOO
37480000	OOFFOFFFOFOOF
37500000	OOFFOFFFOFOFO
37580000	OOFFOFFFOFOFF
37600000	OOFFOFFFOFFOO
37680000	OOFFOFFFOFFOF
37700000	OOFFOFFFOFFFO
37780000	OOFFOFFFOFFFF
37800000	OOFFOFFFFOOOO
37880000	OOFFOFFFFOOOF
37900000	OOFFOFFFFOOFO
37980000	OOFFOFFFFOOFF
37A00000	OOFFOFFFFOFOO
37A80000	OOFFOFFFFOFOF
37B00000	OOFFOFFFFOFFO
37B80000	OOFFOFFFFOFFF
37C00000	OOFFOFFFFFOOO
37C80000	OOFFOFFFFFOOF
37D00000	OOFFOFFFFFOFO
37D80000	OOFFOFFFFFOFF
37E00000	OOFFOFFFFFFOO
37E80000	OOFFOFFFFFFOF
37F00000	OOFFOFFFFFFFO
37F80000	OOFFOFFFFFFFF
38000000	OOFFFOOOOOOOO
38080000	OOFFFOOOOOOOF
38100000	OOFFFOOOOOOFO
38180000	OOFFFOOOOOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
38200000	OOFFFOOOOOFOO
38280000	OOFFFOOOOOFOF
38300000	OOFFFOOOOOFFO
38380000	OOFFFOOOOOFFF
38400000	OOFFFOOOOFOOO
38480000	OOFFFOOOOFOOF
38500000	OOFFFOOOOFOFO
38580000	OOFFFOOOOFOFF
38600000	OOFFFOOOOFFOO
38680000	OOFFFOOOOFFOF
38700000	OOFFFOOOOFFFO
38780000	OOFFFOOOOFFFF
38800000	OOFFFOOOFOOOO
38880000	OOFFFOOOFOOOF
38900000	OOFFFOOOFOOFO
38980000	OOFFFOOOFOOFF
38A00000	OOFFFOOOFOFOO
38A80000	OOFFFOOOFOFOF
38B00000	OOFFFOOOFOFFO
38B80000	OOFFFOOOFOFFF
38C00000	OOFFFOOOFFOOO
38C80000	OOFFFOOOFFOOF
38D00000	OOFFFOOOFFOFO
38D80000	OOFFFOOOFFOFF
38E00000	OOFFFOOOFFFOO
38E80000	OOFFFOOOFFFOF
38F00000	OOFFFOOOFFFFO
38F80000	OOFFFOOOFFFFF
39000000	OOFFFOOFOOOOO
39080000	OOFFFOOFOOOOF
39100000	OOFFFOOFOOOFO
39180000	OOFFFOOFOOOFF
39200000	OOFFFOOFOOFOO
39280000	OOFFFOOFOOFOF
39300000	OOFFFOOFOOFFO
39380000	OOFFFOOFOOFFF
39400000	OOFFFOOFOFOOO
39480000	OOFFFOOFOFOOF
39500000	OOFFFOOFOFOFO
39580000	OOFFFOOFOFOFF
39600000	OOFFFOOFOFFOO
39680000	OOFFFOOFOFFOF
39700000	OOFFFOOFOFFFO
39780000	OOFFFOOFOFFFF
39800000	OOFFFOOFFOOOO
39880000	OOFFFOOFFOOOF
39900000	OOFFFOOFFOOFO
39980000	OOFFFOOFFOOFF
39A00000	OOFFFOOFFOFOO
39A80000	OOFFFOOFFOFOF
39B00000	OOFFFOOFFOFFO
39B80000	OOFFFOOFFOFFF
39C00000	OOFFFOOFFFOOO
39C80000	OOFFFOOFFFOOF
39D00000	OOFFFOOFFFOFO
39D80000	OOFFFOOFFFOFF
39E00000	OOFFFOOFFFFOO
39E80000	OOFFFOOFFFFOF
39F00000	OOFFFOOFFFFFO
39F80000	OOFFFOOFFFFFF
3A000000	OOFFFOFOOOOOO
3A080000	OOFFFOFOOOOOF
3A100000	OOFFFOFOOOOFO
3A180000	OOFFFOFOOOOFF
3A200000	OOFFFOFOOOFOO
3A280000	OOFFFOFOOOFOF
3A300000	OOFFFOFOOOFFO
3A380000	OOFFFOFOOOFFF
3A400000	OOFFFOFOOFOOO
3A480000	OOFFFOFOOFOOF
3A500000	OOFFFOFOOFOFO
3A580000	OOFFFOFOOFOFF
3A600000	OOFFFOFOOFFOO
3A680000	OOFFFOFOOFFOF
3A700000	OOFFFOFOOFFFO
3A780000	OOFFFOFOOFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
3A800000	OOFFFOFOFOOOO
3A880000	OOFFFOFOFOOOF
3A900000	OOFFFOFOFOOFO
3A980000	OOFFFOFOFOOFF
3AA00000	OOFFFOFOFOFOO
3AA80000	OOFFFOFOFOFOF
3AB00000	OOFFFOFOFOFFO
3AB80000	OOFFFOFOFOFFF
3AC00000	OOFFFOFOFFOOO
3AC80000	OOFFFOFOFFOOF
3AD00000	OOFFFOFOFFOFO
3AD80000	OOFFFOFOFFOFF
3AE00000	OOFFFOFOFFFOO
3AE80000	OOFFFOFOFFFOF
3AF00000	OOFFFOFOFFFFO
3AF80000	OOFFFOFOFFFFF
3B000000	OOFFFOFFOOOOO
3B080000	OOFFFOFFOOOOF
3B100000	OOFFFOFFOOOFO
3B180000	OOFFFOFFOOOFF
3B200000	OOFFFOFFOOFOO
3B280000	OOFFFOFFOOFOF
3B300000	OOFFFOFFOOFFO
3B380000	OOFFFOFFOOFFF
3B400000	OOFFFOFFOFOOO
3B480000	OOFFFOFFOFOOF
3B500000	OOFFFOFFOFOFO
3B580000	OOFFFOFFOFOFF
3B600000	OOFFFOFFOFFOO
3B680000	OOFFFOFFOFFOF
3B700000	OOFFFOFFOFFFO
3B780000	OOFFFOFFOFFFF
3B800000	OOFFFOFFFOOOO
3B880000	OOFFFOFFFOOOF
3B900000	OOFFFOFFFOOFO
3B980000	OOFFFOFFFOOFF
3BA00000	OOFFFOFFFOFOO
3BA80000	OOFFFOFFFOFOF
3BB00000	OOFFFOFFFOFFO
3BB80000	OOFFFOFFFOFFF
3BC00000	OOFFFOFFFFOOO
3BC80000	OOFFFOFFFFOOF
3BD00000	OOFFFOFFFFOFO
3BD80000	OOFFFOFFFFOFF
3BE00000	OOFFFOFFFFFOO
3BE80000	OOFFFOFFFFFOF
3BF00000	OOFFFOFFFFFFO
3BF80000	OOFFFOFFFFFFF
3C000000	OOFFFFOOOOOOO
3C080000	OOFFFFOOOOOOF
3C100000	OOFFFFOOOOOFO
3C180000	OOFFFFOOOOOFF
3C200000	OOFFFFOOOOFOO
3C280000	OOFFFFOOOOFOF
3C300000	OOFFFFOOOOFFO
3C380000	OOFFFFOOOOFFF
3C400000	OOFFFFOOOFOOO
3C480000	OOFFFFOOOFOOF
3C500000	OOFFFFOOOFOFO
3C580000	OOFFFFOOOFOFF
3C600000	OOFFFFOOOFFOO
3C680000	OOFFFFOOOFFOF
3C700000	OOFFFFOOOFFFO
3C780000	OOFFFFOOOFFFF
3C800000	OOFFFFOOFOOOO
3C880000	OOFFFFOOFOOOF
3C900000	OOFFFFOOFOOFO
3C980000	OOFFFFOOFOOFF
3CA00000	OOFFFFOOFOFOO
3CA80000	OOFFFFOOFOFOF
3CB00000	OOFFFFOOFOFFO
3CB80000	OOFFFFOOFOFFF
3CC00000	OOFFFFOOFFOOO
3CC80000	OOFFFFOOFFOOF
3CD00000	OOFFFFOOFFOFO
3CD80000	OOFFFFOOFFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
3CE00000	OOFFFFOOFFFOO
3CE80000	OOFFFFOOFFFOF
3CF00000	OOFFFFOOFFFFO
3CF80000	OOFFFFOOFFFFF
3D000000	OOFFFFOFOOOOO
3D080000	OOFFFFOFOOOOF
3D100000	OOFFFFOFOOOFO
3D180000	OOFFFFOFOOOFF
3D200000	OOFFFFOFOOFOO
3D280000	OOFFFFOFOOFOF
3D300000	OOFFFFOFOOFFO
3D380000	OOFFFFOFOOFFF
3D400000	OOFFFFOFOFOOO
3D480000	OOFFFFOFOFOOF
3D500000	OOFFFFOFOFOFO
3D580000	OOFFFFOFOFOFF
3D600000	OOFFFFOFOFFOO
3D680000	OOFFFFOFOFFOF
3D700000	OOFFFFOFOFFFO
3D780000	OOFFFFOFOFFFF
3D800000	OOFFFFOFFOOOO
3D880000	OOFFFFOFFOOOF
3D900000	OOFFFFOFFOOFO
3D980000	OOFFFFOFFOOFF
3DA00000	OOFFFFOFFOFOO
3DA80000	OOFFFFOFFOFOF
3DB00000	OOFFFFOFFOFFO
3DB80000	OOFFFFOFFOFFF
3DC00000	OOFFFFOFFFOOO
3DC80000	OOFFFFOFFFOOF
3DD00000	OOFFFFOFFFOFO
3DD80000	OOFFFFOFFFOFF
3DE00000	OOFFFFOFFFFOO
3DE80000	OOFFFFOFFFFOF
3DF00000	OOFFFFOFFFFFO
3DF80000	OOFFFFOFFFFFF
3E000000	OOFFFFFOOOOOO
3E080000	OOFFFFFOOOOOF
3E100000	OOFFFFFOOOOFO
3E180000	OOFFFFFOOOOFF
3E200000	OOFFFFFOOOFOO
3E280000	OOFFFFFOOOFOF
3E300000	OOFFFFFOOOFFO
3E380000	OOFFFFFOOOFFF
3E400000	OOFFFFFOOFOOO
3E480000	OOFFFFFOOFOOF
3E500000	OOFFFFFOOFOFO
3E580000	OOFFFFFOOFOFF
3E600000	OOFFFFFOOFFOO
3E680000	OOFFFFFOOFFOF
3E700000	OOFFFFFOOFFFO
3E780000	OOFFFFFOOFFFF
3E800000	OOFFFFFOFOOOO
3E880000	OOFFFFFOFOOOF
3E900000	OOFFFFFOFOOFO
3E980000	OOFFFFFOFOOFF
3EA00000	OOFFFFFOFOFOO
3EA80000	OOFFFFFOFOFOF
3EB00000	OOFFFFFOFOFFO
3EB80000	OOFFFFFOFOFFF
3EC00000	OOFFFFFOFFOOO
3EC80000	OOFFFFFOFFOOF
3ED00000	OOFFFFFOFFOFO
3ED80000	OOFFFFFOFFOFF
3EE00000	OOFFFFFOFFFOO
3EE80000	OOFFFFFOFFFOF
3EF00000	OOFFFFFOFFFFO
3EF80000	OOFFFFFOFFFFF
3F000000	OOFFFFFFOOOOO
3F080000	OOFFFFFFOOOOF
3F100000	OOFFFFFFOOOFO
3F180000	OOFFFFFFOOOFF
3F200000	OOFFFFFFOOFOO
3F280000	OOFFFFFFOOFOF
3F300000	OOFFFFFFOOFFO
3F380000	OOFFFFFFOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
3F400000	OOFFFFFFOFOOO
3F480000	OOFFFFFFOFOOF
3F500000	OOFFFFFFOFOFO
3F580000	OOFFFFFFOFOFF
3F600000	OOFFFFFFOFFOO
3F680000	OOFFFFFFOFFOF
3F700000	OOFFFFFFOFFFO
3F780000	OOFFFFFFOFFFF
3F800000	OOFFFFFFFOOOO
3F880000	OOFFFFFFFOOOF
3F900000	OOFFFFFFFOOFO
3F980000	OOFFFFFFFOOFF
3FA00000	OOFFFFFFFOFOO
3FA80000	OOFFFFFFFOFOF
3FB00000	OOFFFFFFFOFFO
3FB80000	OOFFFFFFFOFFF
3FC00000	OOFFFFFFFFOOO
3FC80000	OOFFFFFFFFOOF
3FD00000	OOFFFFFFFFOFO
3FD80000	OOFFFFFFFFOFF
3FE00000	OOFFFFFFFFFOO
3FE80000	OOFFFFFFFFFOF
3FF00000	OOFFFFFFFFFFO
3FF80000	OOFFFFFFFFFFF
40000000	OFOOOOOOOOOOO
40080000	OFOOOOOOOOOOF
40100000	OFOOOOOOOOOFO
40180000	OFOOOOOOOOOFF
40200000	OFOOOOOOOOFOO
40280000	OFOOOOOOOOFOF
40300000	OFOOOOOOOOFFO
40380000	OFOOOOOOOOFFF
40400000	OFOOOOOOOFOOO
40480000	OFOOOOOOOFOOF
40500000	OFOOOOOOOFOFO
40580000	OFOOOOOOOFOFF
40600000	OFOOOOOOOFFOO
40680000	OFOOOOOOOFFOF
40700000	OFOOOOOOOFFFO
40780000	OFOOOOOOOFFFF
40800000	OFOOOOOOFOOOO
40880000	OFOOOOOOFOOOF
40900000	OFOOOOOOFOOFO
40980000	OFOOOOOOFOOFF
40A00000	OFOOOOOOFOFOO
40A80000	OFOOOOOOFOFOF
40B00000	OFOOOOOOFOFFO
40B80000	OFOOOOOOFOFFF
40C00000	OFOOOOOOFFOOO
40C80000	OFOOOOOOFFOOF
40D00000	OFOOOOOOFFOFO
40D80000	OFOOOOOOFFOFF
40E00000	OFOOOOOOFFFOO
40E80000	OFOOOOOOFFFOF
40F00000	OFOOOOOOFFFFO
40F80000	OFOOOOOOFFFFF
41000000	OFOOOOOFOOOOO
41080000	OFOOOOOFOOOOF
41100000	OFOOOOOFOOOFO
41180000	OFOOOOOFOOOFF
41200000	OFOOOOOFOOFOO
41280000	OFOOOOOFOOFOF
41300000	OFOOOOOFOOFFO
41380000	OFOOOOOFOOFFF
41400000	OFOOOOOFOFOOO
41480000	OFOOOOOFOFOOF
41500000	OFOOOOOFOFOFO
41580000	OFOOOOOFOFOFF
41600000	OFOOOOOFOFFOO
41680000	OFOOOOOFOFFOF
41700000	OFOOOOOFOFFFO
41780000	OFOOOOOFOFFFF
41800000	OFOOOOOFFOOOO
41880000	OFOOOOOFFOOOF
41900000	OFOOOOOFFOOFO
41980000	OFOOOOOFFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
41A00000	OFOOOOOFFOFOO
41A80000	OFOOOOOFFOFOF
41B00000	OFOOOOOFFOFFO
41B80000	OFOOOOOFFOFFF
41C00000	OFOOOOOFFFOOO
41C80000	OFOOOOOFFFOOF
41D00000	OFOOOOOFFFOFO
41D80000	OFOOOOOFFFOFF
41E00000	OFOOOOOFFFFOO
41E80000	OFOOOOOFFFFOF
41F00000	OFOOOOOFFFFFO
41F80000	OFOOOOOFFFFFF
42000000	OFOOOOFOOOOOO
42080000	OFOOOOFOOOOOF
42100000	OFOOOOFOOOOFO
42180000	OFOOOOFOOOOFF
42200000	OFOOOOFOOOFOO
42280000	OFOOOOFOOOFOF
42300000	OFOOOOFOOOFFO
42380000	OFOOOOFOOOFFF
42400000	OFOOOOFOOFOOO
42480000	OFOOOOFOOFOOF
42500000	OFOOOOFOOFOFO
42580000	OFOOOOFOOFOFF
42600000	OFOOOOFOOFFOO
42680000	OFOOOOFOOFFOF
42700000	OFOOOOFOOFFFO
42780000	OFOOOOFOOFFFF
42800000	OFOOOOFOFOOOO
42880000	OFOOOOFOFOOOF
42900000	OFOOOOFOFOOFO
42980000	OFOOOOFOFOOFF
42A00000	OFOOOOFOFOFOO
42A80000	OFOOOOFOFOFOF
42B00000	OFOOOOFOFOFFO
42B80000	OFOOOOFOFOFFF
42C00000	OFOOOOFOFFOOO
42C80000	OFOOOOFOFFOOF
42D00000	OFOOOOFOFFOFO
42D80000	OFOOOOFOFFOFF
42E00000	OFOOOOFOFFFOO
42E80000	OFOOOOFOFFFOF
42F00000	OFOOOOFOFFFFO
42F80000	OFOOOOFOFFFFF
43000000	OFOOOOFFOOOOO
43080000	OFOOOOFFOOOOF
43100000	OFOOOOFFOOOFO
43180000	OFOOOOFFOOOFF
43200000	OFOOOOFFOOFOO
43280000	OFOOOOFFOOFOF
43300000	OFOOOOFFOOFFO
43380000	OFOOOOFFOOFFF
43400000	OFOOOOFFOFOOO
43480000	OFOOOOFFOFOOF
43500000	OFOOOOFFOFOFO
43580000	OFOOOOFFOFOFF
43600000	OFOOOOFFOFFOO
43680000	OFOOOOFFOFFOF
43700000	OFOOOOFFOFFFO
43780000	OFOOOOFFOFFFF
43800000	OFOOOOFFFOOOO
43880000	OFOOOOFFFOOOF
43900000	OFOOOOFFFOOFO
43980000	OFOOOOFFFOOFF
43A00000	OFOOOOFFFOFOO
43A80000	OFOOOOFFFOFOF
43B00000	OFOOOOFFFOFFO
43B80000	OFOOOOFFFOFFF
43C00000	OFOOOOFFFFOOO
43C80000	OFOOOOFFFFOOF
43D00000	OFOOOOFFFFOFO
43D80000	OFOOOOFFFFOFF
43E00000	OFOOOOFFFFFOO
43E80000	OFOOOOFFFFFOF
43F00000	OFOOOOFFFFFFO
43F80000	OFOOOOFFFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
44000000	OFOOOFOOOOOOO
44080000	OFOOOFOOOOOOF
44100000	OFOOOFOOOOOFO
44180000	OFOOOFOOOOOFF
44200000	OFOOOFOOOOFOO
44280000	OFOOOFOOOOFOF
44300000	OFOOOFOOOOFFO
44380000	OFOOOFOOOOFFF
44400000	OFOOOFOOOFOOO
44480000	OFOOOFOOOFOOF
44500000	OFOOOFOOOFOFO
44580000	OFOOOFOOOFOFF
44600000	OFOOOFOOOFFOO
44680000	OFOOOFOOOFFOF
44700000	OFOOOFOOOFFFO
44780000	OFOOOFOOOFFFF
44800000	OFOOOFOOFOOOO
44880000	OFOOOFOOFOOOF
44900000	OFOOOFOOFOOFO
44980000	OFOOOFOOFOOFF
44A00000	OFOOOFOOFOFOO
44A80000	OFOOOFOOFOFOF
44B00000	OFOOOFOOFOFFO
44B80000	OFOOOFOOFOFFF
44C00000	OFOOOFOOFFOOO
44C80000	OFOOOFOOFFOOF
44D00000	OFOOOFOOFFOFO
44D80000	OFOOOFOOFFOFF
44E00000	OFOOOFOOFFFOO
44E80000	OFOOOFOOFFFOF
44F00000	OFOOOFOOFFFFO
44F80000	OFOOOFOOFFFFF
45000000	OFOOOFOFOOOOO
45080000	OFOOOFOFOOOOF
45100000	OFOOOFOFOOOFO
45180000	OFOOOFOFOOOFF
45200000	OFOOOFOFOOFOO
45280000	OFOOOFOFOOFOF
45300000	OFOOOFOFOOFFO
45380000	OFOOOFOFOOFFF
45400000	OFOOOFOFOFOOO
45480000	OFOOOFOFOFOOF
45500000	OFOOOFOFOFOFO
45580000	OFOOOFOFOFOFF
45600000	OFOOOFOFOFFOO
45680000	OFOOOFOFOFFOF
45700000	OFOOOFOFOFFFO
45780000	OFOOOFOFOFFFF
45800000	OFOOOFOFFOOOO
45880000	OFOOOFOFFOOOF
45900000	OFOOOFOFFOOFO
45980000	OFOOOFOFFOOFF
45A00000	OFOOOFOFFOFOO
45A80000	OFOOOFOFFOFOF
45B00000	OFOOOFOFFOFFO
45B80000	OFOOOFOFFOFFF
45C00000	OFOOOFOFFFOOO
45C80000	OFOOOFOFFFOOF
45D00000	OFOOOFOFFFOFO
45D80000	OFOOOFOFFFOFF
45E00000	OFOOOFOFFFFOO
45E80000	OFOOOFOFFFFOF
45F00000	OFOOOFOFFFFFO
45F80000	OFOOOFOFFFFFF
46000000	OFOOOFFOOOOOO
46080000	OFOOOFFOOOOOF
46100000	OFOOOFFOOOOFO
46180000	OFOOOFFOOOOFF
46200000	OFOOOFFOOOFOO
46280000	OFOOOFFOOOFOF
46300000	OFOOOFFOOOFFO
46380000	OFOOOFFOOOFFF
46400000	OFOOOFFOOFOOO
46480000	OFOOOFFOOFOOF
46500000	OFOOOFFOOFOFO
46580000	OFOOOFFOOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
46600000	OFOOOFFOOFFOO
46680000	OFOOOFFOOFFOF
46700000	OFOOOFFOOFFFO
46780000	OFOOOFFOOFFFF
46800000	OFOOOFFOFOOOO
46880000	OFOOOFFOFOOOF
46900000	OFOOOFFOFOOFO
46980000	OFOOOFFOFOOFF
46A00000	OFOOOFFOFOFOO
46A80000	OFOOOFFOFOFOF
46B00000	OFOOOFFOFOFFO
46B80000	OFOOOFFOFOFFF
46C00000	OFOOOFFOFFOOO
46C80000	OFOOOFFOFFOOF
46D00000	OFOOOFFOFFOFO
46D80000	OFOOOFFOFFOFF
46E00000	OFOOOFFOFFFOO
46E80000	OFOOOFFOFFFOF
46F00000	OFOOOFFOFFFFO
46F80000	OFOOOFFOFFFFF
47000000	OFOOOFFFOOOOO
47080000	OFOOOFFFOOOOF
47100000	OFOOOFFFOOOFO
47180000	OFOOOFFFOOOFF
47200000	OFOOOFFFOOFOO
47280000	OFOOOFFFOOFOF
47300000	OFOOOFFFOOFFO
47380000	OFOOOFFFOOFFF
47400000	OFOOOFFFOFOOO
47480000	OFOOOFFFOFOOF
47500000	OFOOOFFFOFOFO
47580000	OFOOOFFFOFOFF
47600000	OFOOOFFFOFFOO
47680000	OFOOOFFFOFFOF
47700000	OFOOOFFFOFFFO
47780000	OFOOOFFFOFFFF
47800000	OFOOOFFFFOOOO
47880000	OFOOOFFFFOOOF
47900000	OFOOOFFFFOOFO
47980000	OFOOOFFFFOOFF
47A00000	OFOOOFFFFOFOO
47A80000	OFOOOFFFFOFOF
47B00000	OFOOOFFFFOFFO
47B80000	OFOOOFFFFOFFF
47C00000	OFOOOFFFFFOOO
47C80000	OFOOOFFFFFOOF
47D00000	OFOOOFFFFFOFO
47D80000	OFOOOFFFFFOFF
47E00000	OFOOOFFFFFFOO
47E80000	OFOOOFFFFFFOF
47F00000	OFOOOFFFFFFFO
47F80000	OFOOOFFFFFFFF
48000000	OFOOFOOOOOOOO
48080000	OFOOFOOOOOOOF
48100000	OFOOFOOOOOOFO
48180000	OFOOFOOOOOOFF
48200000	OFOOFOOOOOFOO
48280000	OFOOFOOOOOFOF
48300000	OFOOFOOOOOFFO
48380000	OFOOFOOOOOFFF
48400000	OFOOFOOOOFOOO
48480000	OFOOFOOOOFOOF
48500000	OFOOFOOOOFOFO
48580000	OFOOFOOOOFOFF
48600000	OFOOFOOOOFFOO
48680000	OFOOFOOOOFFOF
48700000	OFOOFOOOOFFFO
48780000	OFOOFOOOOFFFF
48800000	OFOOFOOOFOOOO
48880000	OFOOFOOOFOOOF
48900000	OFOOFOOOFOOFO
48980000	OFOOFOOOFOOFF
48A00000	OFOOFOOOFOFOO
48A80000	OFOOFOOOFOFOF
48B00000	OFOOFOOOFOFFO
48B80000	OFOOFOOOFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
48C00000	OFOOFOOOFFOOO
48C80000	OFOOFOOOFFOOF
48D00000	OFOOFOOOFFOFO
48D80000	OFOOFOOOFFOFF
48E00000	OFOOFOOOFFFOO
48E80000	OFOOFOOOFFFOF
48F00000	OFOOFOOOFFFFO
48F80000	OFOOFOOOFFFFF
49000000	OFOOFOOFOOOOO
49080000	OFOOFOOFOOOOF
49100000	OFOOFOOFOOOFO
49180000	OFOOFOOFOOOFF
49200000	OFOOFOOFOOFOO
49280000	OFOOFOOFOOFOF
49300000	OFOOFOOFOOFFO
49380000	OFOOFOOFOOFFF
49400000	OFOOFOOFOFOOO
49480000	OFOOFOOFOFOOF
49500000	OFOOFOOFOFOFO
49580000	OFOOFOOFOFOFF
49600000	OFOOFOOFOFFOO
49680000	OFOOFOOFOFFOF
49700000	OFOOFOOFOFFFO
49780000	OFOOFOOFOFFFF
49800000	OFOOFOOFFOOOO
49880000	OFOOFOOFFOOOF
49900000	OFOOFOOFFOOFO
49980000	OFOOFOOFFOOFF
49A00000	OFOOFOOFFOFOO
49A80000	OFOOFOOFFOFOF
49B00000	OFOOFOOFFOFFO
49B80000	OFOOFOOFFOFFF
49C00000	OFOOFOOFFFOOO
49C80000	OFOOFOOFFFOOF
49D00000	OFOOFOOFFFOFO
49D80000	OFOOFOOFFFOFF
49E00000	OFOOFOOFFFFOO
49E80000	OFOOFOOFFFFOF
49F00000	OFOOFOOFFFFFO
49F80000	OFOOFOOFFFFFF
4A000000	OFOOFOFOOOOOO
4A080000	OFOOFOFOOOOOF
4A100000	OFOOFOFOOOOFO
4A180000	OFOOFOFOOOOFF
4A200000	OFOOFOFOOOFOO
4A280000	OFOOFOFOOOFOF
4A300000	OFOOFOFOOOFFO
4A380000	OFOOFOFOOOFFF
4A400000	OFOOFOFOOFOOO
4A480000	OFOOFOFOOFOOF
4A500000	OFOOFOFOOFOFO
4A580000	OFOOFOFOOFOFF
4A600000	OFOOFOFOOFFOO
4A680000	OFOOFOFOOFFOF
4A700000	OFOOFOFOOFFFO
4A780000	OFOOFOFOOFFFF
4A800000	OFOOFOFOFOOOO
4A880000	OFOOFOFOFOOOF
4A900000	OFOOFOFOFOOFO
4A980000	OFOOFOFOFOOFF
4AA00000	OFOOFOFOFOFOO
4AA80000	OFOOFOFOFOFOF
4AB00000	OFOOFOFOFOFFO
4AB80000	OFOOFOFOFOFFF
4AC00000	OFOOFOFOFFOOO
4AC80000	OFOOFOFOFFOOF
4AD00000	OFOOFOFOFFOFO
4AD80000	OFOOFOFOFFOFF
4AE00000	OFOOFOFOFFFOO
4AE80000	OFOOFOFOFFFOF
4AF00000	OFOOFOFOFFFFO
4AF80000	OFOOFOFOFFFFF
4B000000	OFOOFOFFOOOOO
4B080000	OFOOFOFFOOOOF
4B100000	OFOOFOFFOOOFO
4B180000	OFOOFOFFOOOFF
4B200000	OFOOFOFFOOFOO
4B280000	OFOOFOFFOOFOF
4B300000	OFOOFOFFOOFFO
4B380000	OFOOFOFFOOFFF
4B400000	OFOOFOFFOFOOO
4B480000	OFOOFOFFOFOOF
4B500000	OFOOFOFFOFOFO
4B580000	OFOOFOFFOFOFF
4B600000	OFOOFOFFOFFOO
4B680000	OFOOFOFFOFFOF
4B700000	OFOOFOFFOFFFO
4B780000	OFOOFOFFOFFFF
4B800000	OFOOFOFFFOOOO
4B880000	OFOOFOFFFOOOF
4B900000	OFOOFOFFFOOFO
4B980000	OFOOFOFFFOOFF
4BA00000	OFOOFOFFFOFOO
4BA80000	OFOOFOFFFOFOF
4BB00000	OFOOFOFFFOFFO
4BB80000	OFOOFOFFFOFFF
4BC00000	OFOOFOFFFFOOO
4BC80000	OFOOFOFFFFOOF
4BD00000	OFOOFOFFFFOFO
4BD80000	OFOOFOFFFFOFF
4BE00000	OFOOFOFFFFFOO
4BE80000	OFOOFOFFFFFOF
4BF00000	OFOOFOFFFFFFO
4BF80000	OFOOFOFFFFFFF
4C000000	OFOOFFOOOOOOO
4C080000	OFOOFFOOOOOOF
4C100000	OFOOFFOOOOOFO
4C180000	OFOOFFOOOOOFF
4C200000	OFOOFFOOOOFOO
4C280000	OFOOFFOOOOFOF
4C300000	OFOOFFOOOOFFO
4C380000	OFOOFFOOOOFFF
4C400000	OFOOFFOOOFOOO
4C480000	OFOOFFOOOFOOF
4C500000	OFOOFFOOOFOFO
4C580000	OFOOFFOOOFOFF
4C600000	OFOOFFOOOFFOO
4C680000	OFOOFFOOOFFOF
4C700000	OFOOFFOOOFFFO
4C780000	OFOOFFOOOFFFF
4C800000	OFOOFFOOFOOOO
4C880000	OFOOFFOOFOOOF
4C900000	OFOOFFOOFOOFO
4C980000	OFOOFFOOFOOFF
4CA00000	OFOOFFOOFOFOO
4CA80000	OFOOFFOOFOFOF
4CB00000	OFOOFFOOFOFFO
4CB80000	OFOOFFOOFOFFF
4CC00000	OFOOFFOOFFOOO
4CC80000	OFOOFFOOFFOOF
4CD00000	OFOOFFOOFFOFO
4CD80000	OFOOFFOOFFOFF
4CE00000	OFOOFFOOFFFOO
4CE80000	OFOOFFOOFFFOF
4CF00000	OFOOFFOOFFFFO
4CF80000	OFOOFFOOFFFFF
4D000000	OFOOFFOFOOOOO
4D080000	OFOOFFOFOOOOF
4D100000	OFOOFFOFOOOFO
4D180000	OFOOFFOFOOOFF
4D200000	OFOOFFOFOOFOO
4D280000	OFOOFFOFOOFOF
4D300000	OFOOFFOFOOFFO
4D380000	OFOOFFOFOOFFF
4D400000	OFOOFFOFOFOOO
4D480000	OFOOFFOFOFOOF
4D500000	OFOOFFOFOFOFO
4D580000	OFOOFFOFOFOFF
4D600000	OFOOFFOFOFFOO
4D680000	OFOOFFOFOFFOF
4D700000	OFOOFFOFOFFFO
4D780000	OFOOFFOFOFFFF
4D800000	OFOOFFOFFOOOO
4D880000	OFOOFFOFFOOOF
4D900000	OFOOFFOFFOOFO
4D980000	OFOOFFOFFOOFF
4DA00000	OFOOFFOFFOFOO
4DA80000	OFOOFFOFFOFOF
4DB00000	OFOOFFOFFOFFO
4DB80000	OFOOFFOFFOFFF
4DC00000	OFOOFFOFFFOOO
4DC80000	OFOOFFOFFFOOF
4DD00000	OFOOFFOFFFOFO
4DD80000	OFOOFFOFFFOFF
4DE00000	OFOOFFOFFFFOO
4DE80000	OFOOFFOFFFFOF
4DF00000	OFOOFFOFFFFFO
4DF80000	OFOOFFOFFFFFF
4E000000	OFOOFFFOOOOOO
4E080000	OFOOFFFOOOOOF
4E100000	OFOOFFFOOOOFO
4E180000	OFOOFFFOOOOFF
4E200000	OFOOFFFOOOFOO
4E280000	OFOOFFFOOOFOF
4E300000	OFOOFFFOOOFFO
4E380000	OFOOFFFOOOFFF
4E400000	OFOOFFFOOFOOO
4E480000	OFOOFFFOOFOOF
4E500000	OFOOFFFOOFOFO
4E580000	OFOOFFFOOFOFF
4E600000	OFOOFFFOOFFOO
4E680000	OFOOFFFOOFFOF
4E700000	OFOOFFFOOFFFO
4E780000	OFOOFFFOOFFFF
4E800000	OFOOFFFOFOOOO
4E880000	OFOOFFFOFOOOF
4E900000	OFOOFFFOFOOFO
4E980000	OFOOFFFOFOOFF
4EA00000	OFOOFFFOFOFOO
4EA80000	OFOOFFFOFOFOF
4EB00000	OFOOFFFOFOFFO
4EB80000	OFOOFFFOFOFFF
4EC00000	OFOOFFFOFFOOO
4EC80000	OFOOFFFOFFOOF
4ED00000	OFOOFFFOFFOFO
4ED80000	OFOOFFFOFFOFF
4EE00000	OFOOFFFOFFFOO
4EE80000	OFOOFFFOFFFOF
4EF00000	OFOOFFFOFFFFO
4EF80000	OFOOFFFOFFFFF
4F000000	OFOOFFFFOOOOO
4F080000	OFOOFFFFOOOOF
4F100000	OFOOFFFFOOOFO
4F180000	OFOOFFFFOOOFF
4F200000	OFOOFFFFOOFOO
4F280000	OFOOFFFFOOFOF
4F300000	OFOOFFFFOOFFO
4F380000	OFOOFFFFOOFFF
4F400000	OFOOFFFFOFOOO
4F480000	OFOOFFFFOFOOF
4F500000	OFOOFFFFOFOFO
4F580000	OFOOFFFFOFOFF
4F600000	OFOOFFFFOFFOO
4F680000	OFOOFFFFOFFOF
4F700000	OFOOFFFFOFFFO
4F780000	OFOOFFFFOFFFF
4F800000	OFOOFFFFFOOOO
4F880000	OFOOFFFFFOOOF
4F900000	OFOOFFFFFOOFO
4F980000	OFOOFFFFFOOFF
4FA00000	OFOOFFFFFOFOO
4FA80000	OFOOFFFFFOFOF
4FB00000	OFOOFFFFFOFFO
4FB80000	OFOOFFFFFOFFF
4FC00000	OFOOFFFFFFOOO
4FC80000	OFOOFFFFFFOOF
4FD00000	OFOOFFFFFFOFO
4FD80000	OFOOFFFFFFOFF
4FE00000	OFOOFFFFFFFOO
4FE80000	OFOOFFFFFFFOF
4FF00000	OFOOFFFFFFFFO
4FF80000	OFOOFFFFFFFFF
50000000	OFOFOOOOOOOOO
50080000	OFOFOOOOOOOOF
50100000	OFOFOOOOOOOFO
50180000	OFOFOOOOOOOFF
50200000	OFOFOOOOOOFOO
50280000	OFOFOOOOOOFOF
50300000	OFOFOOOOOOFFO
50380000	OFOFOOOOOOFFF
50400000	OFOFOOOOOFOOO
50480000	OFOFOOOOOFOOF
50500000	OFOFOOOOOFOFO
50580000	OFOFOOOOOFOFF
50600000	OFOFOOOOOFFOO
50680000	OFOFOOOOOFFOF
50700000	OFOFOOOOOFFFO
50780000	OFOFOOOOOFFFF
50800000	OFOFOOOOFOOOO
50880000	OFOFOOOOFOOOF
50900000	OFOFOOOOFOOFO
50980000	OFOFOOOOFOOFF
50A00000	OFOFOOOOFOFOO
50A80000	OFOFOOOOFOFOF
50B00000	OFOFOOOOFOFFO
50B80000	OFOFOOOOFOFFF
50C00000	OFOFOOOOFFOOO
50C80000	OFOFOOOOFFOOF
50D00000	OFOFOOOOFFOFO
50D80000	OFOFOOOOFFOFF
50E00000	OFOFOOOOFFFOO
50E80000	OFOFOOOOFFFOF
50F00000	OFOFOOOOFFFFO
50F80000	OFOFOOOOFFFFF
51000000	OFOFOOOFOOOOO
51080000	OFOFOOOFOOOOF
51100000	OFOFOOOFOOOFO
51180000	OFOFOOOFOOOFF
51200000	OFOFOOOFOOFOO
51280000	OFOFOOOFOOFOF
51300000	OFOFOOOFOOFFO
51380000	OFOFOOOFOOFFF
51400000	OFOFOOOFOFOOO
51480000	OFOFOOOFOFOOF
51500000	OFOFOOOFOFOFO
51580000	OFOFOOOFOFOFF
51600000	OFOFOOOFOFFOO
51680000	OFOFOOOFOFFOF
51700000	OFOFOOOFOFFFO
51780000	OFOFOOOFOFFFF
51800000	OFOFOOOFFOOOO
51880000	OFOFOOOFFOOOF
51900000	OFOFOOOFFOOFO
51980000	OFOFOOOFFOOFF
51A00000	OFOFOOOFFOFOO
51A80000	OFOFOOOFFOFOF
51B00000	OFOFOOOFFOFFO
51B80000	OFOFOOOFFOFFF
51C00000	OFOFOOOFFFOOO
51C80000	OFOFOOOFFFOOF
51D00000	OFOFOOOFFFOFO
51D80000	OFOFOOOFFFOFF
51E00000	OFOFOOOFFFFOO
51E80000	OFOFOOOFFFFOF
51F00000	OFOFOOOFFFFFO
51F80000	OFOFOOOFFFFFF
52000000	OFOFOOFOOOOOO
52080000	OFOFOOFOOOOOF
52100000	OFOFOOFOOOOFO
52180000	OFOFOOFOOOOFF
52200000	OFOFOOFOOOFOO
52280000	OFOFOOFOOOFOF
52300000	OFOFOOFOOOFFO
52380000	OFOFOOFOOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
52400000	OFOFOOFOOFOOO
52480000	OFOFOOFOOFOOF
52500000	OFOFOOFOOFOFO
52580000	OFOFOOFOOFOFF
52600000	OFOFOOFOOFFOO
52680000	OFOFOOFOOFFOF
52700000	OFOFOOFOOFFFO
52780000	OFOFOOFOOFFFF
52800000	OFOFOOFOFOOOO
52880000	OFOFOOFOFOOOF
52900000	OFOFOOFOFOOFO
52980000	OFOFOOFOFOOFF
52A00000	OFOFOOFOFOFOO
52A80000	OFOFOOFOFOFOF
52B00000	OFOFOOFOFOFFO
52B80000	OFOFOOFOFOFFF
52C00000	OFOFOOFOFFOOO
52C80000	OFOFOOFOFFOOF
52D00000	OFOFOOFOFFOFO
52D80000	OFOFOOFOFFOFF
52E00000	OFOFOOFOFFFOO
52E80000	OFOFOOFOFFFOF
52F00000	OFOFOOFOFFFFO
52F80000	OFOFOOFOFFFFF
53000000	OFOFOOFFOOOOO
53080000	OFOFOOFFOOOOF
53100000	OFOFOOFFOOOFO
53180000	OFOFOOFFOOOFF
53200000	OFOFOOFFOOFOO
53280000	OFOFOOFFOOFOF
53300000	OFOFOOFFOOFFO
53380000	OFOFOOFFOOFFF
53400000	OFOFOOFFOFOOO
53480000	OFOFOOFFOFOOF
53500000	OFOFOOFFOFOFO
53580000	OFOFOOFFOFOFF
53600000	OFOFOOFFOFFOO
53680000	OFOFOOFFOFFOF
53700000	OFOFOOFFOFFFO
53780000	OFOFOOFFOFFFF
53800000	OFOFOOFFFOOOO
53880000	OFOFOOFFFOOOF
53900000	OFOFOOFFFOOFO
53980000	OFOFOOFFFOOFF
53A00000	OFOFOOFFFOFOO
53A80000	OFOFOOFFFOFOF
53B00000	OFOFOOFFFOFFO
53B80000	OFOFOOFFFOFFF
53C00000	OFOFOOFFFFOOO
53C80000	OFOFOOFFFFOOF
53D00000	OFOFOOFFFFOFO
53D80000	OFOFOOFFFFOFF
53E00000	OFOFOOFFFFFOO
53E80000	OFOFOOFFFFFOF
53F00000	OFOFOOFFFFFFO
53F80000	OFOFOOFFFFFFF
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54080000	OFOFOFOOOOOOF
54100000	OFOFOFOOOOOFO
54180000	OFOFOFOOOOOFF
54200000	OFOFOFOOOOFOO
54280000	OFOFOFOOOOFOF
54300000	OFOFOFOOOOFFO
54380000	OFOFOFOOOOFFF
54400000	OFOFOFOOOFOOO
54480000	OFOFOFOOOFOOF
54500000	OFOFOFOOOFOFO
54580000	OFOFOFOOOFOFF
54600000	OFOFOFOOOFFOO
54680000	OFOFOFOOOFFOF
54700000	OFOFOFOOOFFFO
54780000	OFOFOFOOOFFFF
54800000	OFOFOFOOFOOOO
54880000	OFOFOFOOFOOOF
54900000	OFOFOFOOFOOFO
54980000	OFOFOFOOFOOFF
54A00000	OFOFOFOOFOFOO
54A80000	OFOFOFOOFOFOF
54B00000	OFOFOFOOFOFFO
54B80000	OFOFOFOOFOFFF
54C00000	OFOFOFOOFFOOO
54C80000	OFOFOFOOFFOOF
54D00000	OFOFOFOOFFOFO
54D80000	OFOFOFOOFFOFF
54E00000	OFOFOFOOFFFOO
54E80000	OFOFOFOOFFFOF
54F00000	OFOFOFOOFFFFO
54F80000	OFOFOFOOFFFFF
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55080000	OFOFOFOFOOOOF
55100000	OFOFOFOFOOOFO
55180000	OFOFOFOFOOOFF
55200000	OFOFOFOFOOFOO
55280000	OFOFOFOFOOFOF
55300000	OFOFOFOFOOFFO
55380000	OFOFOFOFOOFFF
55400000	OFOFOFOFOFOOO
55480000	OFOFOFOFOFOOF
55500000	OFOFOFOFOFOFO
55580000	OFOFOFOFOFOFF
55600000	OFOFOFOFOFFOO
55680000	OFOFOFOFOFFOF
55700000	OFOFOFOFOFFFO
55780000	OFOFOFOFOFFFF
55800000	OFOFOFOFFOOOO
55880000	OFOFOFOFFOOOF
55900000	OFOFOFOFFOOFO
55980000	OFOFOFOFFOOFF
55A00000	OFOFOFOFFOFOO
55A80000	OFOFOFOFFOFOF
55B00000	OFOFOFOFFOFFO
55B80000	OFOFOFOFFOFFF
55C00000	OFOFOFOFFFOOO
55C80000	OFOFOFOFFFOOF
55D00000	OFOFOFOFFFOFO
55D80000	OFOFOFOFFFOFF
55E00000	OFOFOFOFFFFOO
55E80000	OFOFOFOFFFFOF
55F00000	OFOFOFOFFFFFO
55F80000	OFOFOFOFFFFFF
56000000	OFOFOFFOOOOOO
56080000	OFOFOFFOOOOOF
56100000	OFOFOFFOOOOFO
56180000	OFOFOFFOOOOFF
56200000	OFOFOFFOOOFOO
56280000	OFOFOFFOOOFOF
56300000	OFOFOFFOOOFFO
56380000	OFOFOFFOOOFFF
56400000	OFOFOFFOOFOOO
56480000	OFOFOFFOOFOOF
56500000	OFOFOFFOOFOFO
56580000	OFOFOFFOOFOFF
56600000	OFOFOFFOOFFOO
56680000	OFOFOFFOOFFOF
56700000	OFOFOFFOOFFFO
56780000	OFOFOFFOOFFFF
56800000	OFOFOFFOFOOOO
56880000	OFOFOFFOFOOOF
56900000	OFOFOFFOFOOFO
56980000	OFOFOFFOFOOFF
56A00000	OFOFOFFOFOFOO
56A80000	OFOFOFFOFOFOF
56B00000	OFOFOFFOFOFFO
56B80000	OFOFOFFOFOFFF
56C00000	OFOFOFFOFFOOO
56C80000	OFOFOFFOFFOOF
56D00000	OFOFOFFOFFOFO
56D80000	OFOFOFFOFFOFF
56E00000	OFOFOFFOFFFOO
56E80000	OFOFOFFOFFFOF
56F00000	OFOFOFFOFFFFO
56F80000	OFOFOFFOFFFFF
57000000	OFOFOFFFOOOOO
57080000	OFOFOFFFOOOOF
57100000	OFOFOFFFOOOFO
57180000	OFOFOFFFOOOFF
57200000	OFOFOFFFOOFOO
57280000	OFOFOFFFOOFOF
57300000	OFOFOFFFOOFFO
57380000	OFOFOFFFOOFFF
57400000	OFOFOFFFOFOOO
57480000	OFOFOFFFOFOOF
57500000	OFOFOFFFOFOFO
57580000	OFOFOFFFOFOFF
57600000	OFOFOFFFOFFOO
57680000	OFOFOFFFOFFOF
57700000	OFOFOFFFOFFFO
57780000	OFOFOFFFOFFFF
57800000	OFOFOFFFFOOOO
57880000	OFOFOFFFFOOOF
57900000	OFOFOFFFFOOFO
57980000	OFOFOFFFFOOFF
57A00000	OFOFOFFFFOFOO
57A80000	OFOFOFFFFOFOF
57B00000	OFOFOFFFFOFFO
57B80000	OFOFOFFFFOFFF
57C00000	OFOFOFFFFFOOO
57C80000	OFOFOFFFFFOOF
57D00000	OFOFOFFFFFOFO
57D80000	OFOFOFFFFFOFF
57E00000	OFOFOFFFFFFOO
57E80000	OFOFOFFFFFFOF
57F00000	OFOFOFFFFFFFO
57F80000	OFOFOFFFFFFFF
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58080000	OFOFFOOOOOOOF
58100000	OFOFFOOOOOOFO
58180000	OFOFFOOOOOOFF
58200000	OFOFFOOOOOFOO
58280000	OFOFFOOOOOFOF
58300000	OFOFFOOOOOFFO
58380000	OFOFFOOOOOFFF
58400000	OFOFFOOOOFOOO
58480000	OFOFFOOOOFOOF
58500000	OFOFFOOOOFOFO
58580000	OFOFFOOOOFOFF
58600000	OFOFFOOOOFFOO
58680000	OFOFFOOOOFFOF
58700000	OFOFFOOOOFFFO
58780000	OFOFFOOOOFFFF
58800000	OFOFFOOOFOOOO
58880000	OFOFFOOOFOOOF
58900000	OFOFFOOOFOOFO
58980000	OFOFFOOOFOOFF
58A00000	OFOFFOOOFOFOO
58A80000	OFOFFOOOFOFOF
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58C00000	OFOFFOOOFFOOO
58C80000	OFOFFOOOFFOOF
58D00000	OFOFFOOOFFOFO
58D80000	OFOFFOOOFFOFF
58E00000	OFOFFOOOFFFOO
58E80000	OFOFFOOOFFFOF
58F00000	OFOFFOOOFFFFO
58F80000	OFOFFOOOFFFFF
59000000	OFOFFOOFOOOOO
59080000	OFOFFOOFOOOOF
59100000	OFOFFOOFOOOFO
59180000	OFOFFOOFOOOFF
59200000	OFOFFOOFOOFOO
59280000	OFOFFOOFOOFOF
59300000	OFOFFOOFOOFFO
59380000	OFOFFOOFOOFFF
59400000	OFOFFOOFOFOOO
59480000	OFOFFOOFOFOOF
59500000	OFOFFOOFOFOFO
59580000	OFOFFOOFOFOFF
59600000	OFOFFOOFOFFOO
59680000	OFOFFOOFOFFOF
59700000	OFOFFOOFOFFFO
59780000	OFOFFOOFOFFFF
59800000	OFOFFOOFFOOOO
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59900000	OFOFFOOFFOOFO
59980000	OFOFFOOFFOOFF
59A00000	OFOFFOOFFOFOO
59A80000	OFOFFOOFFOFOF
59B00000	OFOFFOOFFOFFO
59B80000	OFOFFOOFFOFFF
59C00000	OFOFFOOFFFOOO
59C80000	OFOFFOOFFFOOF
59D00000	OFOFFOOFFFOFO
59D80000	OFOFFOOFFFOFF
59E00000	OFOFFOOFFFFOO
59E80000	OFOFFOOFFFFOF
59F00000	OFOFFOOFFFFFO
59F80000	OFOFFOOFFFFFF
5A000000	OFOFFOFOOOOOO
5A080000	OFOFFOFOOOOOF
5A100000	OFOFFOFOOOOFO
5A180000	OFOFFOFOOOOFF
5A200000	OFOFFOFOOOFOO
5A280000	OFOFFOFOOOFOF
5A300000	OFOFFOFOOOFFO
5A380000	OFOFFOFOOOFFF
5A400000	OFOFFOFOOFOOO
5A480000	OFOFFOFOOFOOF
5A500000	OFOFFOFOOFOFO
5A580000	OFOFFOFOOFOFF
5A600000	OFOFFOFOOFFOO
5A680000	OFOFFOFOOFFOF
5A700000	OFOFFOFOOFFFO
5A780000	OFOFFOFOOFFFF
5A800000	OFOFFOFOFOOOO
5A880000	OFOFFOFOFOOOF
5A900000	OFOFFOFOFOOFO
5A980000	OFOFFOFOFOOFF
5AA00000	OFOFFOFOFOFOO
5AA80000	OFOFFOFOFOFOF
5AB00000	OFOFFOFOFOFFO
5AB80000	OFOFFOFOFOFFF
5AC00000	OFOFFOFOFFOOO
5AC80000	OFOFFOFOFFOOF
5AD00000	OFOFFOFOFFOFO
5AD80000	OFOFFOFOFFOFF
5AE00000	OFOFFOFOFFFOO
5AE80000	OFOFFOFOFFFOF
5AF00000	OFOFFOFOFFFFO
5AF80000	OFOFFOFOFFFFF
5B000000	OFOFFOFFOOOOO
5B080000	OFOFFOFFOOOOF
5B100000	OFOFFOFFOOOFO
5B180000	OFOFFOFFOOOFF
5B200000	OFOFFOFFOOFOO
5B280000	OFOFFOFFOOFOF
5B300000	OFOFFOFFOOFFO
5B380000	OFOFFOFFOOFFF
5B400000	OFOFFOFFOFOOO
5B480000	OFOFFOFFOFOOF
5B500000	OFOFFOFFOFOFO
5B580000	OFOFFOFFOFOFF
5B600000	OFOFFOFFOFFOO
5B680000	OFOFFOFFOFFOF
5B700000	OFOFFOFFOFFFO
5B780000	OFOFFOFFOFFFF
5B800000	OFOFFOFFFOOOO
5B880000	OFOFFOFFFOOOF
5B900000	OFOFFOFFFOOFO
5B980000	OFOFFOFFFOOFF
5BA00000	OFOFFOFFFOFOO
5BA80000	OFOFFOFFFOFOF
5BB00000	OFOFFOFFFOFFO
5BB80000	OFOFFOFFFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
5BC00000	OFOFFOFFFFOOO
5BC80000	OFOFFOFFFFOOF
5BD00000	OFOFFOFFFFOFO
5BD80000	OFOFFOFFFFOFF
5BE00000	OFOFFOFFFFFOO
5BE80000	OFOFFOFFFFFOF
5BF00000	OFOFFOFFFFFFO
5BF80000	OFOFFOFFFFFFF
5C000000	OFOFFFOOOOOOO
5C080000	OFOFFFOOOOOOF
5C100000	OFOFFFOOOOOFO
5C180000	OFOFFFOOOOOFF
5C200000	OFOFFFOOOOFOO
5C280000	OFOFFFOOOOFOF
5C300000	OFOFFFOOOOFFO
5C380000	OFOFFFOOOOFFF
5C400000	OFOFFFOOOFOOO
5C480000	OFOFFFOOOFOOF
5C500000	OFOFFFOOOFOFO
5C580000	OFOFFFOOOFOFF
5C600000	OFOFFFOOOFFOO
5C680000	OFOFFFOOOFFOF
5C700000	OFOFFFOOOFFFO
5C780000	OFOFFFOOOFFFF
5C800000	OFOFFFOOFOOOO
5C880000	OFOFFFOOFOOOF
5C900000	OFOFFFOOFOOFO
5C980000	OFOFFFOOFOOFF
5CA00000	OFOFFFOOFOFOO
5CA80000	OFOFFFOOFOFOF
5CB00000	OFOFFFOOFOFFO
5CB80000	OFOFFFOOFOFFF
5CC00000	OFOFFFOOFFOOO
5CC80000	OFOFFFOOFFOOF
5CD00000	OFOFFFOOFFOFO
5CD80000	OFOFFFOOFFOFF
5CE00000	OFOFFFOOFFFOO
5CE80000	OFOFFFOOFFFOF
5CF00000	OFOFFFOOFFFFO
5CF80000	OFOFFFOOFFFFF
5D000000	OFOFFFOFOOOOO
5D080000	OFOFFFOFOOOOF
5D100000	OFOFFFOFOOOFO
5D180000	OFOFFFOFOOOFF
5D200000	OFOFFFOFOOFOO
5D280000	OFOFFFOFOOFOF
5D300000	OFOFFFOFOOFFO
5D380000	OFOFFFOFOOFFF
5D400000	OFOFFFOFOFOOO
5D480000	OFOFFFOFOFOOF
5D500000	OFOFFFOFOFOFO
5D580000	OFOFFFOFOFOFF
5D600000	OFOFFFOFOFFOO
5D680000	OFOFFFOFOFFOF
5D700000	OFOFFFOFOFFFO
5D780000	OFOFFFOFOFFFF
5D800000	OFOFFFOFFOOOO
5D880000	OFOFFFOFFOOOF
5D900000	OFOFFFOFFOOFO
5D980000	OFOFFFOFFOOFF
5DA00000	OFOFFFOFFOFOO
5DA80000	OFOFFFOFFOFOF
5DB00000	OFOFFFOFFOFFO
5DB80000	OFOFFFOFFOFFF
5DC00000	OFOFFFOFFFOOO
5DC80000	OFOFFFOFFFOOF
5DD00000	OFOFFFOFFFOFO
5DD80000	OFOFFFOFFFOFF
5DE00000	OFOFFFOFFFFOO
5DE80000	OFOFFFOFFFFOF
5DF00000	OFOFFFOFFFFFO
5DF80000	OFOFFFOFFFFFF
5E000000	OFOFFFFOOOOOO
5E080000	OFOFFFFOOOOOF
5E100000	OFOFFFFOOOOFO
5E180000	OFOFFFFOOOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
5E200000	OFOFFFFOOOFOO
5E280000	OFOFFFFOOOFOF
5E300000	OFOFFFFOOOFFO
5E380000	OFOFFFFOOOFFF
5E400000	OFOFFFFOOFOOO
5E480000	OFOFFFFOOFOOF
5E500000	OFOFFFFOOFOFO
5E580000	OFOFFFFOOFOFF
5E600000	OFOFFFFOOFFOO
5E680000	OFOFFFFOOFFOF
5E700000	OFOFFFFOOFFFO
5E780000	OFOFFFFOOFFFF
5E800000	OFOFFFFOFOOOO
5E880000	OFOFFFFOFOOOF
5E900000	OFOFFFFOFOOFO
5E980000	OFOFFFFOFOOFF
5EA00000	OFOFFFFOFOFOO
5EA80000	OFOFFFFOFOFOF
5EB00000	OFOFFFFOFOFFO
5EB80000	OFOFFFFOFOFFF
5EC00000	OFOFFFFOFFOOO
5EC80000	OFOFFFFOFFOOF
5ED00000	OFOFFFFOFFOFO
5ED80000	OFOFFFFOFFOFF
5EE00000	OFOFFFFOFFFOO
5EE80000	OFOFFFFOFFFOF
5EF00000	OFOFFFFOFFFFO
5EF80000	OFOFFFFOFFFFF
5F000000	OFOFFFFFOOOOO
5F080000	OFOFFFFFOOOOF
5F100000	OFOFFFFFOOOFO
5F180000	OFOFFFFFOOOFF
5F200000	OFOFFFFFOOFOO
5F280000	OFOFFFFFOOFOF
5F300000	OFOFFFFFOOFFO
5F380000	OFOFFFFFOOFFF
5F400000	OFOFFFFFOFOOO
5F480000	OFOFFFFFOFOOF
5F500000	OFOFFFFFOFOFO
5F580000	OFOFFFFFOFOFF
5F600000	OFOFFFFFOFFOO
5F680000	OFOFFFFFOFFOF
5F700000	OFOFFFFFOFFFO
5F780000	OFOFFFFFOFFFF
5F800000	OFOFFFFFFOOOO
5F880000	OFOFFFFFFOOOF
5F900000	OFOFFFFFFOOFO
5F980000	OFOFFFFFFOOFF
5FA00000	OFOFFFFFFOFOO
5FA80000	OFOFFFFFFOFOF
5FB00000	OFOFFFFFFOFFO
5FB80000	OFOFFFFFFOFFF
5FC00000	OFOFFFFFFFOOO
5FC80000	OFOFFFFFFFOOF
5FD00000	OFOFFFFFFFOFO
5FD80000	OFOFFFFFFFOFF
5FE00000	OFOFFFFFFFFOO
5FE80000	OFOFFFFFFFFOF
5FF00000	OFOFFFFFFFFFO
5FF80000	OFOFFFFFFFFFF
60000000	OFFOOOOOOOOOO
60080000	OFFOOOOOOOOOF
60100000	OFFOOOOOOOOFO
60180000	OFFOOOOOOOOFF
60200000	OFFOOOOOOOFOO
60280000	OFFOOOOOOOFOF
60300000	OFFOOOOOOOFFO
60380000	OFFOOOOOOOFFF
60400000	OFFOOOOOOFOOO
60480000	OFFOOOOOOFOOF
60500000	OFFOOOOOOFOFO
60580000	OFFOOOOOOFOFF
60600000	OFFOOOOOOFFOO
60680000	OFFOOOOOOFFOF
60700000	OFFOOOOOOFFFO
60780000	OFFOOOOOOFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
60800000	OFFOOOOOFOOOO
60880000	OFFOOOOOFOOOF
60900000	OFFOOOOOFOOFO
60980000	OFFOOOOOFOOFF
60A00000	OFFOOOOOFOFOO
60A80000	OFFOOOOOFOFOF
60B00000	OFFOOOOOFOFFO
60B80000	OFFOOOOOFOFFF
60C00000	OFFOOOOOFFOOO
60C80000	OFFOOOOOFFOOF
60D00000	OFFOOOOOFFOFO
60D80000	OFFOOOOOFFOFF
60E00000	OFFOOOOOFFFOO
60E80000	OFFOOOOOFFFOF
60F00000	OFFOOOOOFFFFO
60F80000	OFFOOOOOFFFFF
61000000	OFFOOOOFOOOOO
61080000	OFFOOOOFOOOOF
61100000	OFFOOOOFOOOFO
61180000	OFFOOOOFOOOFF
61200000	OFFOOOOFOOFOO
61280000	OFFOOOOFOOFOF
61300000	OFFOOOOFOOFFO
61380000	OFFOOOOFOOFFF
61400000	OFFOOOOFOFOOO
61480000	OFFOOOOFOFOOF
61500000	OFFOOOOFOFOFO
61580000	OFFOOOOFOFOFF
61600000	OFFOOOOFOFFOO
61680000	OFFOOOOFOFFOF
61700000	OFFOOOOFOFFFO
61780000	OFFOOOOFOFFFF
61800000	OFFOOOOFFOOOO
61880000	OFFOOOOFFOOOF
61900000	OFFOOOOFFOOFO
61980000	OFFOOOOFFOOFF
61A00000	OFFOOOOFFOFOO
61A80000	OFFOOOOFFOFOF
61B00000	OFFOOOOFFOFFO
61B80000	OFFOOOOFFOFFF
61C00000	OFFOOOOFFFOOO
61C80000	OFFOOOOFFFOOF
61D00000	OFFOOOOFFFOFO
61D80000	OFFOOOOFFFOFF
61E00000	OFFOOOOFFFFOO
61E80000	OFFOOOOFFFFOF
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61F80000	OFFOOOOFFFFFF
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62080000	OFFOOOFOOOOOF
62100000	OFFOOOFOOOOFO
62180000	OFFOOOFOOOOFF
62200000	OFFOOOFOOOFOO
62280000	OFFOOOFOOOFOF
62300000	OFFOOOFOOOFFO
62380000	OFFOOOFOOOFFF
62400000	OFFOOOFOOFOOO
62480000	OFFOOOFOOFOOF
62500000	OFFOOOFOOFOFO
62580000	OFFOOOFOOFOFF
62600000	OFFOOOFOOFFOO
62680000	OFFOOOFOOFFOF
62700000	OFFOOOFOOFFFO
62780000	OFFOOOFOOFFFF
62800000	OFFOOOFOFOOOO
62880000	OFFOOOFOFOOOF
62900000	OFFOOOFOFOOFO
62980000	OFFOOOFOFOOFF
62A00000	OFFOOOFOFOFOO
62A80000	OFFOOOFOFOFOF
62B00000	OFFOOOFOFOFFO
62B80000	OFFOOOFOFOFFF
62C00000	OFFOOOFOFFOOO
62C80000	OFFOOOFOFFOOF
62D00000	OFFOOOFOFFOFO
62D80000	OFFOOOFOFFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
62E00000	OFFOOOFOFFFOO
62E80000	OFFOOOFOFFFOF
62F00000	OFFOOOFOFFFFO
62F80000	OFFOOOFOFFFFF
63000000	OFFOOOFFOOOOO
63080000	OFFOOOFFOOOOF
63100000	OFFOOOFFOOOFO
63180000	OFFOOOFFOOOFF
63200000	OFFOOOFFOOFOO
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63300000	OFFOOOFFOOFFO
63380000	OFFOOOFFOOFFF
63400000	OFFOOOFFOFOOO
63480000	OFFOOOFFOFOOF
63500000	OFFOOOFFOFOFO
63580000	OFFOOOFFOFOFF
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63700000	OFFOOOFFOFFFO
63780000	OFFOOOFFOFFFF
63800000	OFFOOOFFFOOOO
63880000	OFFOOOFFFOOOF
63900000	OFFOOOFFFOOFO
63980000	OFFOOOFFFOOFF
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63A80000	OFFOOOFFFOFOF
63B00000	OFFOOOFFFOFFO
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63C80000	OFFOOOFFFFOOF
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63E00000	OFFOOOFFFFFOO
63E80000	OFFOOOFFFFFOF
63F00000	OFFOOOFFFFFFO
63F80000	OFFOOOFFFFFFF
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64180000	OFFOOFOOOOOFF
64200000	OFFOOFOOOOFOO
64280000	OFFOOFOOOOFOF
64300000	OFFOOFOOOOFFO
64380000	OFFOOFOOOOFFF
64400000	OFFOOFOOOFOOO
64480000	OFFOOFOOOFOOF
64500000	OFFOOFOOOFOFO
64580000	OFFOOFOOOFOFF
64600000	OFFOOFOOOFFOO
64680000	OFFOOFOOOFFOF
64700000	OFFOOFOOOFFFO
64780000	OFFOOFOOOFFFF
64800000	OFFOOFOOFOOOO
64880000	OFFOOFOOFOOOF
64900000	OFFOOFOOFOOFO
64980000	OFFOOFOOFOOFF
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64B00000	OFFOOFOOFOFFO
64B80000	OFFOOFOOFOFFF
64C00000	OFFOOFOOFFOOO
64C80000	OFFOOFOOFFOOF
64D00000	OFFOOFOOFFOFO
64D80000	OFFOOFOOFFOFF
64E00000	OFFOOFOOFFFOO
64E80000	OFFOOFOOFFFOF
64F00000	OFFOOFOOFFFFO
64F80000	OFFOOFOOFFFFF
65000000	OFFOOFOFOOOOO
65080000	OFFOOFOFOOOOF
65100000	OFFOOFOFOOOFO
65180000	OFFOOFOFOOOFF
65200000	OFFOOFOFOOFOO
65280000	OFFOOFOFOOFOF
65300000	OFFOOFOFOOFFO
65380000	OFFOOFOFOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
65400000	OFFOOFOFOFOOO
65480000	OFFOOFOFOFOOF
65500000	OFFOOFOFOFOFO
65580000	OFFOOFOFOFOFF
65600000	OFFOOFOFOFFOO
65680000	OFFOOFOFOFFOF
65700000	OFFOOFOFOFFFO
65780000	OFFOOFOFOFFFF
65800000	OFFOOFOFFOOOO
65880000	OFFOOFOFFOOOF
65900000	OFFOOFOFFOOFO
65980000	OFFOOFOFFOOFF
65A00000	OFFOOFOFFOFOO
65A80000	OFFOOFOFFOFOF
65B00000	OFFOOFOFFOFFO
65B80000	OFFOOFOFFOFFF
65C00000	OFFOOFOFFFOOO
65C80000	OFFOOFOFFFOOF
65D00000	OFFOOFOFFFOFO
65D80000	OFFOOFOFFFOFF
65E00000	OFFOOFOFFFFOO
65E80000	OFFOOFOFFFFOF
65F00000	OFFOOFOFFFFFO
65F80000	OFFOOFOFFFFFF
66000000	OFFOOFFOOOOOO
66080000	OFFOOFFOOOOOF
66100000	OFFOOFFOOOOFO
66180000	OFFOOFFOOOOFF
66200000	OFFOOFFOOOFOO
66280000	OFFOOFFOOOFOF
66300000	OFFOOFFOOOFFO
66380000	OFFOOFFOOOFFF
66400000	OFFOOFFOOFOOO
66480000	OFFOOFFOOFOOF
66500000	OFFOOFFOOFOFO
66580000	OFFOOFFOOFOFF
66600000	OFFOOFFOOFFOO
66680000	OFFOOFFOOFFOF
66700000	OFFOOFFOOFFFO
66780000	OFFOOFFOOFFFF
66800000	OFFOOFFOFOOOO
66880000	OFFOOFFOFOOOF
66900000	OFFOOFFOFOOFO
66980000	OFFOOFFOFOOFF
66A00000	OFFOOFFOFOFOO
66A80000	OFFOOFFOFOFOF
66B00000	OFFOOFFOFOFFO
66B80000	OFFOOFFOFOFFF
66C00000	OFFOOFFOFFOOO
66C80000	OFFOOFFOFFOOF
66D00000	OFFOOFFOFFOFO
66D80000	OFFOOFFOFFOFF
66E00000	OFFOOFFOFFFOO
66E80000	OFFOOFFOFFFOF
66F00000	OFFOOFFOFFFFO
66F80000	OFFOOFFOFFFFF
67000000	OFFOOFFFOOOOO
67080000	OFFOOFFFOOOOF
67100000	OFFOOFFFOOOFO
67180000	OFFOOFFFOOOFF
67200000	OFFOOFFFOOFOO
67280000	OFFOOFFFOOFOF
67300000	OFFOOFFFOOFFO
67380000	OFFOOFFFOOFFF
67400000	OFFOOFFFOFOOO
67480000	OFFOOFFFOFOOF
67500000	OFFOOFFFOFOFO
67580000	OFFOOFFFOFOFF
67600000	OFFOOFFFOFFOO
67680000	OFFOOFFFOFFOF
67700000	OFFOOFFFOFFFO
67780000	OFFOOFFFOFFFF
67800000	OFFOOFFFFOOOO
67880000	OFFOOFFFFOOOF
67900000	OFFOOFFFFOOFO
67980000	OFFOOFFFFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
67A00000	OFFOOFFFFOFOO
67A80000	OFFOOFFFFOFOF
67B00000	OFFOOFFFFOFFO
67B80000	OFFOOFFFFOFFF
67C00000	OFFOOFFFFFOOO
67C80000	OFFOOFFFFFOOF
67D00000	OFFOOFFFFFOFO
67D80000	OFFOOFFFFFOFF
67E00000	OFFOOFFFFFFOO
67E80000	OFFOOFFFFFFOF
67F00000	OFFOOFFFFFFFO
67F80000	OFFOOFFFFFFFF
68000000	OFFOFOOOOOOOO
68080000	OFFOFOOOOOOOF
68100000	OFFOFOOOOOOFO
68180000	OFFOFOOOOOOFF
68200000	OFFOFOOOOOFOO
68280000	OFFOFOOOOOFOF
68300000	OFFOFOOOOOFFO
68380000	OFFOFOOOOOFFF
68400000	OFFOFOOOOFOOO
68480000	OFFOFOOOOFOOF
68500000	OFFOFOOOOFOFO
68580000	OFFOFOOOOFOFF
68600000	OFFOFOOOOFFOO
68680000	OFFOFOOOOFFOF
68700000	OFFOFOOOOFFFO
68780000	OFFOFOOOOFFFF
68800000	OFFOFOOOFOOOO
68880000	OFFOFOOOFOOOF
68900000	OFFOFOOOFOOFO
68980000	OFFOFOOOFOOFF
68A00000	OFFOFOOOFOFOO
68A80000	OFFOFOOOFOFOF
68B00000	OFFOFOOOFOFFO
68B80000	OFFOFOOOFOFFF
68C00000	OFFOFOOOFFOOO
68C80000	OFFOFOOOFFOOF
68D00000	OFFOFOOOFFOFO
68D80000	OFFOFOOOFFOFF
68E00000	OFFOFOOOFFFOO
68E80000	OFFOFOOOFFFOF
68F00000	OFFOFOOOFFFFO
68F80000	OFFOFOOOFFFFF
69000000	OFFOFOOFOOOOO
69080000	OFFOFOOFOOOOF
69100000	OFFOFOOFOOOFO
69180000	OFFOFOOFOOOFF
69200000	OFFOFOOFOOFOO
69280000	OFFOFOOFOOFOF
69300000	OFFOFOOFOOFFO
69380000	OFFOFOOFOOFFF
69400000	OFFOFOOFOFOOO
69480000	OFFOFOOFOFOOF
69500000	OFFOFOOFOFOFO
69580000	OFFOFOOFOFOFF
69600000	OFFOFOOFOFFOO
69680000	OFFOFOOFOFFOF
69700000	OFFOFOOFOFFFO
69780000	OFFOFOOFOFFFF
69800000	OFFOFOOFFOOOO
69880000	OFFOFOOFFOOOF
69900000	OFFOFOOFFOOFO
69980000	OFFOFOOFFOOFF
69A00000	OFFOFOOFFOFOO
69A80000	OFFOFOOFFOFOF
69B00000	OFFOFOOFFOFFO
69B80000	OFFOFOOFFOFFF
69C00000	OFFOFOOFFFOOO
69C80000	OFFOFOOFFFOOF
69D00000	OFFOFOOFFFOFO
69D80000	OFFOFOOFFFOFF
69E00000	OFFOFOOFFFFOO
69E80000	OFFOFOOFFFFOF
69F00000	OFFOFOOFFFFFO
69F80000	OFFOFOOFFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
6A000000	OFFOFOFOOOOOO
6A080000	OFFOFOFOOOOOF
6A100000	OFFOFOFOOOOFO
6A180000	OFFOFOFOOOOFF
6A200000	OFFOFOFOOOFOO
6A280000	OFFOFOFOOOFOF
6A300000	OFFOFOFOOOFFO
6A380000	OFFOFOFOOOFFF
6A400000	OFFOFOFOOFOOO
6A480000	OFFOFOFOOFOOF
6A500000	OFFOFOFOOFOFO
6A580000	OFFOFOFOOFOFF
6A600000	OFFOFOFOOFFOO
6A680000	OFFOFOFOOFFOF
6A700000	OFFOFOFOOFFFO
6A780000	OFFOFOFOOFFFF
6A800000	OFFOFOFOFOOOO
6A880000	OFFOFOFOFOOOF
6A900000	OFFOFOFOFOOFO
6A980000	OFFOFOFOFOOFF
6AA00000	OFFOFOFOFOFOO
6AA80000	OFFOFOFOFOFOF
6AB00000	OFFOFOFOFOFFO
6AB80000	OFFOFOFOFOFFF
6AC00000	OFFOFOFOFFOOO
6AC80000	OFFOFOFOFFOOF
6AD00000	OFFOFOFOFFOFO
6AD80000	OFFOFOFOFFOFF
6AE00000	OFFOFOFOFFFOO
6AE80000	OFFOFOFOFFFOF
6AF00000	OFFOFOFOFFFFO
6AF80000	OFFOFOFOFFFFF
6B000000	OFFOFOFFOOOOO
6B080000	OFFOFOFFOOOOF
6B100000	OFFOFOFFOOOFO
6B180000	OFFOFOFFOOOFF
6B200000	OFFOFOFFOOFOO
6B280000	OFFOFOFFOOFOF
6B300000	OFFOFOFFOOFFO
6B380000	OFFOFOFFOOFFF
6B400000	OFFOFOFFOFOOO
6B480000	OFFOFOFFOFOOF
6B500000	OFFOFOFFOFOFO
6B580000	OFFOFOFFOFOFF
6B600000	OFFOFOFFOFFOO
6B680000	OFFOFOFFOFFOF
6B700000	OFFOFOFFOFFFO
6B780000	OFFOFOFFOFFFF
6B800000	OFFOFOFFFOOOO
6B880000	OFFOFOFFFOOOF
6B900000	OFFOFOFFFOOFO
6B980000	OFFOFOFFFOOFF
6BA00000	OFFOFOFFFOFOO
6BA80000	OFFOFOFFFOFOF
6BB00000	OFFOFOFFFOFFO
6BB80000	OFFOFOFFFOFFF
6BC00000	OFFOFOFFFFOOO
6BC80000	OFFOFOFFFFOOF
6BD00000	OFFOFOFFFFOFO
6BD80000	OFFOFOFFFFOFF
6BE00000	OFFOFOFFFFFOO
6BE80000	OFFOFOFFFFFOF
6BF00000	OFFOFOFFFFFFO
6BF80000	OFFOFOFFFFFFF
6C000000	OFFOFFOOOOOOO
6C080000	OFFOFFOOOOOOF
6C100000	OFFOFFOOOOOFO
6C180000	OFFOFFOOOOOFF
6C200000	OFFOFFOOOOFOO
6C280000	OFFOFFOOOOFOF
6C300000	OFFOFFOOOOFFO
6C380000	OFFOFFOOOOFFF
6C400000	OFFOFFOOOFOOO
6C480000	OFFOFFOOOFOOF
6C500000	OFFOFFOOOFOFO
6C580000	OFFOFFOOOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
6C600000	OFFOFFOOOFFOO
6C680000	OFFOFFOOOFFOF
6C700000	OFFOFFOOOFFFO
6C780000	OFFOFFOOOFFFF
6C800000	OFFOFFOOFOOOO
6C880000	OFFOFFOOFOOOF
6C900000	OFFOFFOOFOOFO
6C980000	OFFOFFOOFOOFF
6CA00000	OFFOFFOOFOFOO
6CA80000	OFFOFFOOFOFOF
6CB00000	OFFOFFOOFOFFO
6CB80000	OFFOFFOOFOFFF
6CC00000	OFFOFFOOFFOOO
6CC80000	OFFOFFOOFFOOF
6CD00000	OFFOFFOOFFOFO
6CD80000	OFFOFFOOFFOFF
6CE00000	OFFOFFOOFFFOO
6CE80000	OFFOFFOOFFFOF
6CF00000	OFFOFFOOFFFFO
6CF80000	OFFOFFOOFFFFF
6D000000	OFFOFFOFOOOOO
6D080000	OFFOFFOFOOOOF
6D100000	OFFOFFOFOOOFO
6D180000	OFFOFFOFOOOFF
6D200000	OFFOFFOFOOFOO
6D280000	OFFOFFOFOOFOF
6D300000	OFFOFFOFOOFFO
6D380000	OFFOFFOFOOFFF
6D400000	OFFOFFOFOFOOO
6D480000	OFFOFFOFOFOOF
6D500000	OFFOFFOFOFOFO
6D580000	OFFOFFOFOFOFF
6D600000	OFFOFFOFOFFOO
6D680000	OFFOFFOFOFFOF
6D700000	OFFOFFOFOFFFO
6D780000	OFFOFFOFOFFFF
6D800000	OFFOFFOFFOOOO
6D880000	OFFOFFOFFOOOF
6D900000	OFFOFFOFFOOFO
6D980000	OFFOFFOFFOOFF
6DA00000	OFFOFFOFFOFOO
6DA80000	OFFOFFOFFOFOF
6DB00000	OFFOFFOFFOFFO
6DB80000	OFFOFFOFFOFFF
6DC00000	OFFOFFOFFFOOO
6DC80000	OFFOFFOFFFOOF
6DD00000	OFFOFFOFFFOFO
6DD80000	OFFOFFOFFFOFF
6DE00000	OFFOFFOFFFFOO
6DE80000	OFFOFFOFFFFOF
6DF00000	OFFOFFOFFFFFO
6DF80000	OFFOFFOFFFFFF
6E000000	OFFOFFFOOOOOO
6E080000	OFFOFFFOOOOOF
6E100000	OFFOFFFOOOOFO
6E180000	OFFOFFFOOOOFF
6E200000	OFFOFFFOOOFOO
6E280000	OFFOFFFOOOFOF
6E300000	OFFOFFFOOOFFO
6E380000	OFFOFFFOOOFFF
6E400000	OFFOFFFOOFOOO
6E480000	OFFOFFFOOFOOF
6E500000	OFFOFFFOOFOFO
6E580000	OFFOFFFOOFOFF
6E600000	OFFOFFFOOFFOO
6E680000	OFFOFFFOOFFOF
6E700000	OFFOFFFOOFFFO
6E780000	OFFOFFFOOFFFF
6E800000	OFFOFFFOFOOOO
6E880000	OFFOFFFOFOOOF
6E900000	OFFOFFFOFOOFO
6E980000	OFFOFFFOFOOFF
6EA00000	OFFOFFFOFOFOO
6EA80000	OFFOFFFOFOFOF
6EB00000	OFFOFFFOFOFFO
6EB80000	OFFOFFFOFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
6EC00000	OFFOFFFOFFOOO
6EC80000	OFFOFFFOFFOOF
6ED00000	OFFOFFFOFFOFO
6ED80000	OFFOFFFOFFOFF
6EE00000	OFFOFFFOFFFOO
6EE80000	OFFOFFFOFFFOF
6EF00000	OFFOFFFOFFFFO
6EF80000	OFFOFFFOFFFFF
6F000000	OFFOFFFFOOOOO
6F080000	OFFOFFFFOOOOF
6F100000	OFFOFFFFOOOFO
6F180000	OFFOFFFFOOOFF
6F200000	OFFOFFFFOOFOO
6F280000	OFFOFFFFOOFOF
6F300000	OFFOFFFFOOFFO
6F380000	OFFOFFFFOOFFF
6F400000	OFFOFFFFOFOOO
6F480000	OFFOFFFFOFOOF
6F500000	OFFOFFFFOFOFO
6F580000	OFFOFFFFOFOFF
6F600000	OFFOFFFFOFFOO
6F680000	OFFOFFFFOFFOF
6F700000	OFFOFFFFOFFFO
6F780000	OFFOFFFFOFFFF
6F800000	OFFOFFFFFOOOO
6F880000	OFFOFFFFFOOOF
6F900000	OFFOFFFFFOOFO
6F980000	OFFOFFFFFOOFF
6FA00000	OFFOFFFFFOFOO
6FA80000	OFFOFFFFFOFOF
6FB00000	OFFOFFFFFOFFO
6FB80000	OFFOFFFFFOFFF
6FC00000	OFFOFFFFFFOOO
6FC80000	OFFOFFFFFFOOF
6FD00000	OFFOFFFFFFOFO
6FD80000	OFFOFFFFFFOFF
6FE00000	OFFOFFFFFFFOO
6FE80000	OFFOFFFFFFFOF
6FF00000	OFFOFFFFFFFFO
6FF80000	OFFOFFFFFFFFF
70000000	OFFFOOOOOOOOO
70080000	OFFFOOOOOOOOF
70100000	OFFFOOOOOOOFO
70180000	OFFFOOOOOOOFF
70200000	OFFFOOOOOOFOO
70280000	OFFFOOOOOOFOF
70300000	OFFFOOOOOOFFO
70380000	OFFFOOOOOOFFF
70400000	OFFFOOOOOFOOO
70480000	OFFFOOOOOFOOF
70500000	OFFFOOOOOFOFO
70580000	OFFFOOOOOFOFF
70600000	OFFFOOOOOFFOO
70680000	OFFFOOOOOFFOF
70700000	OFFFOOOOOFFFO
70780000	OFFFOOOOOFFFF
70800000	OFFFOOOOFOOOO
70880000	OFFFOOOOFOOOF
70900000	OFFFOOOOFOOFO
70980000	OFFFOOOOFOOFF
70A00000	OFFFOOOOFOFOO
70A80000	OFFFOOOOFOFOF
70B00000	OFFFOOOOFOFFO
70B80000	OFFFOOOOFOFFF
70C00000	OFFFOOOOFFOOO
70C80000	OFFFOOOOFFOOF
70D00000	OFFFOOOOFFOFO
70D80000	OFFFOOOOFFOFF
70E00000	OFFFOOOOFFFOO
70E80000	OFFFOOOOFFFOF
70F00000	OFFFOOOOFFFFO
70F80000	OFFFOOOOFFFFF
71000000	OFFFOOOFOOOOO
71080000	OFFFOOOFOOOOF
71100000	OFFFOOOFOOOFO
71180000	OFFFOOOFOOOFF
71200000	OFFFOOOFOOFOO
71280000	OFFFOOOFOOFOF
71300000	OFFFOOOFOOFFO
71380000	OFFFOOOFOOFFF
71400000	OFFFOOOFOFOOO
71480000	OFFFOOOFOFOOF
71500000	OFFFOOOFOFOFO
71580000	OFFFOOOFOFOFF
71600000	OFFFOOOFOFFOO
71680000	OFFFOOOFOFFOF
71700000	OFFFOOOFOFFFO
71780000	OFFFOOOFOFFFF
71800000	OFFFOOOFFOOOO
71880000	OFFFOOOFFOOOF
71900000	OFFFOOOFFOOFO
71980000	OFFFOOOFFOOFF
71A00000	OFFFOOOFFOFOO
71A80000	OFFFOOOFFOFOF
71B00000	OFFFOOOFFOFFO
71B80000	OFFFOOOFFOFFF
71C00000	OFFFOOOFFFOOO
71C80000	OFFFOOOFFFOOF
71D00000	OFFFOOOFFFOFO
71D80000	OFFFOOOFFFOFF
71E00000	OFFFOOOFFFFOO
71E80000	OFFFOOOFFFFOF
71F00000	OFFFOOOFFFFFO
71F80000	OFFFOOOFFFFFF
72000000	OFFFOOFOOOOOO
72080000	OFFFOOFOOOOOF
72100000	OFFFOOFOOOOFO
72180000	OFFFOOFOOOOFF
72200000	OFFFOOFOOOFOO
72280000	OFFFOOFOOOFOF
72300000	OFFFOOFOOOFFO
72380000	OFFFOOFOOOFFF
72400000	OFFFOOFOOFOOO
72480000	OFFFOOFOOFOOF
72500000	OFFFOOFOOFOFO
72580000	OFFFOOFOOFOFF
72600000	OFFFOOFOOFFOO
72680000	OFFFOOFOOFFOF
72700000	OFFFOOFOOFFFO
72780000	OFFFOOFOOFFFF
72800000	OFFFOOFOFOOOO
72880000	OFFFOOFOFOOOF
72900000	OFFFOOFOFOOFO
72980000	OFFFOOFOFOOFF
72A00000	OFFFOOFOFOFOO
72A80000	OFFFOOFOFOFOF
72B00000	OFFFOOFOFOFFO
72B80000	OFFFOOFOFOFFF
72C00000	OFFFOOFOFFOOO
72C80000	OFFFOOFOFFOOF
72D00000	OFFFOOFOFFOFO
72D80000	OFFFOOFOFFOFF
72E00000	OFFFOOFOFFFOO
72E80000	OFFFOOFOFFFOF
72F00000	OFFFOOFOFFFFO
72F80000	OFFFOOFOFFFFF
73000000	OFFFOOFFOOOOO
73080000	OFFFOOFFOOOOF
73100000	OFFFOOFFOOOFO
73180000	OFFFOOFFOOOFF
73200000	OFFFOOFFOOFOO
73280000	OFFFOOFFOOFOF
73300000	OFFFOOFFOOFFO
73380000	OFFFOOFFOOFFF
73400000	OFFFOOFFOFOOO
73480000	OFFFOOFFOFOOF
73500000	OFFFOOFFOFOFO
73580000	OFFFOOFFOFOFF
73600000	OFFFOOFFOFFOO
73680000	OFFFOOFFOFFOF
73700000	OFFFOOFFOFFFO
73780000	OFFFOOFFOFFFF
73800000	OFFFOOFFFOOOO
73880000	OFFFOOFFFOOOF
73900000	OFFFOOFFFOOFO
73980000	OFFFOOFFFOOFF
73A00000	OFFFOOFFFOFOO
73A80000	OFFFOOFFFOFOF
73B00000	OFFFOOFFFOFFO
73B80000	OFFFOOFFFOFFF
73C00000	OFFFOOFFFFOOO
73C80000	OFFFOOFFFFOOF
73D00000	OFFFOOFFFFOFO
73D80000	OFFFOOFFFFOFF
73E00000	OFFFOOFFFFFOO
73E80000	OFFFOOFFFFFOF
73F00000	OFFFOOFFFFFFO
73F80000	OFFFOOFFFFFFF
74000000	OFFFOFOOOOOOO
74080000	OFFFOFOOOOOOF
74100000	OFFFOFOOOOOFO
74180000	OFFFOFOOOOOFF
74200000	OFFFOFOOOOFOO
74280000	OFFFOFOOOOFOF
74300000	OFFFOFOOOOFFO
74380000	OFFFOFOOOOFFF
74400000	OFFFOFOOOFOOO
74480000	OFFFOFOOOFOOF
74500000	OFFFOFOOOFOFO
74580000	OFFFOFOOOFOFF
74600000	OFFFOFOOOFFOO
74680000	OFFFOFOOOFFOF
74700000	OFFFOFOOOFFFO
74780000	OFFFOFOOOFFFF
74800000	OFFFOFOOFOOOO
74880000	OFFFOFOOFOOOF
74900000	OFFFOFOOFOOFO
74980000	OFFFOFOOFOOFF
74A00000	OFFFOFOOFOFOO
74A80000	OFFFOFOOFOFOF
74B00000	OFFFOFOOFOFFO
74B80000	OFFFOFOOFOFFF
74C00000	OFFFOFOOFFOOO
74C80000	OFFFOFOOFFOOF
74D00000	OFFFOFOOFFOFO
74D80000	OFFFOFOOFFOFF
74E00000	OFFFOFOOFFFOO
74E80000	OFFFOFOOFFFOF
74F00000	OFFFOFOOFFFFO
74F80000	OFFFOFOOFFFFF
75000000	OFFFOFOFOOOOO
75080000	OFFFOFOFOOOOF
75100000	OFFFOFOFOOOFO
75180000	OFFFOFOFOOOFF
75200000	OFFFOFOFOOFOO
75280000	OFFFOFOFOOFOF
75300000	OFFFOFOFOOFFO
75380000	OFFFOFOFOOFFF
75400000	OFFFOFOFOFOOO
75480000	OFFFOFOFOFOOF
75500000	OFFFOFOFOFOFO
75580000	OFFFOFOFOFOFF
75600000	OFFFOFOFOFFOO
75680000	OFFFOFOFOFFOF
75700000	OFFFOFOFOFFFO
75780000	OFFFOFOFOFFFF
75800000	OFFFOFOFFOOOO
75880000	OFFFOFOFFOOOF
75900000	OFFFOFOFFOOFO
75980000	OFFFOFOFFOOFF
75A00000	OFFFOFOFFOFOO
75A80000	OFFFOFOFFOFOF
75B00000	OFFFOFOFFOFFO
75B80000	OFFFOFOFFOFFF
75C00000	OFFFOFOFFFOOO
75C80000	OFFFOFOFFFOOF
75D00000	OFFFOFOFFFOFO
75D80000	OFFFOFOFFFOFF
75E00000	OFFFOFOFFFFOO
75E80000	OFFFOFOFFFFOF
75F00000	OFFFOFOFFFFFO
75F80000	OFFFOFOFFFFFF
76000000	OFFFOFFOOOOOO
76080000	OFFFOFFOOOOOF
76100000	OFFFOFFOOOOFO
76180000	OFFFOFFOOOOFF
76200000	OFFFOFFOOOFOO
76280000	OFFFOFFOOOFOF
76300000	OFFFOFFOOOFFO
76380000	OFFFOFFOOOFFF
76400000	OFFFOFFOOFOOO
76480000	OFFFOFFOOFOOF
76500000	OFFFOFFOOFOFO
76580000	OFFFOFFOOFOFF
76600000	OFFFOFFOOFFOO
76680000	OFFFOFFOOFFOF
76700000	OFFFOFFOOFFFO
76780000	OFFFOFFOOFFFF
76800000	OFFFOFFOFOOOO
76880000	OFFFOFFOFOOOF
76900000	OFFFOFFOFOOFO
76980000	OFFFOFFOFOOFF
76A00000	OFFFOFFOFOFOO
76A80000	OFFFOFFOFOFOF
76B00000	OFFFOFFOFOFFO
76B80000	OFFFOFFOFOFFF
76C00000	OFFFOFFOFFOOO
76C80000	OFFFOFFOFFOOF
76D00000	OFFFOFFOFFOFO
76D80000	OFFFOFFOFFOFF
76E00000	OFFFOFFOFFFOO
76E80000	OFFFOFFOFFFOF
76F00000	OFFFOFFOFFFFO
76F80000	OFFFOFFOFFFFF
77000000	OFFFOFFFOOOOO
77080000	OFFFOFFFOOOOF
77100000	OFFFOFFFOOOFO
77180000	OFFFOFFFOOOFF
77200000	OFFFOFFFOOFOO
77280000	OFFFOFFFOOFOF
77300000	OFFFOFFFOOFFO
77380000	OFFFOFFFOOFFF
77400000	OFFFOFFFOFOOO
77480000	OFFFOFFFOFOOF
77500000	OFFFOFFFOFOFO
77580000	OFFFOFFFOFOFF
77600000	OFFFOFFFOFFOO
77680000	OFFFOFFFOFFOF
77700000	OFFFOFFFOFFFO
77780000	OFFFOFFFOFFFF
77800000	OFFFOFFFFOOOO
77880000	OFFFOFFFFOOOF
77900000	OFFFOFFFFOOFO
77980000	OFFFOFFFFOOFF
77A00000	OFFFOFFFFOFOO
77A80000	OFFFOFFFFOFOF
77B00000	OFFFOFFFFOFFO
77B80000	OFFFOFFFFOFFF
77C00000	OFFFOFFFFFOOO
77C80000	OFFFOFFFFFOOF
77D00000	OFFFOFFFFFOFO
77D80000	OFFFOFFFFFOFF
77E00000	OFFFOFFFFFFOO
77E80000	OFFFOFFFFFFOF
77F00000	OFFFOFFFFFFFO
77F80000	OFFFOFFFFFFFF
78000000	OFFFFOOOOOOOO
78080000	OFFFFOOOOOOOF
78100000	OFFFFOOOOOOFO
78180000	OFFFFOOOOOOFF
78200000	OFFFFOOOOOFOO
78280000	OFFFFOOOOOFOF
78300000	OFFFFOOOOOFFO
78380000	OFFFFOOOOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
78400000	OFFFFOOOOFOOO
78480000	OFFFFOOOOFOOF
78500000	OFFFFOOOOFOFO
78580000	OFFFFOOOOFOFF
78600000	OFFFFOOOOFFOO
78680000	OFFFFOOOOFFOF
78700000	OFFFFOOOOFFFO
78780000	OFFFFOOOOFFFF
78800000	OFFFFOOOFOOOO
78880000	OFFFFOOOFOOOF
78900000	OFFFFOOOFOOFO
78980000	OFFFFOOOFOOFF
78A00000	OFFFFOOOFOFOO
78A80000	OFFFFOOOFOFOF
78B00000	OFFFFOOOFOFFO
78B80000	OFFFFOOOFOFFF
78C00000	OFFFFOOOFFOOO
78C80000	OFFFFOOOFFOOF
78D00000	OFFFFOOOFFOFO
78D80000	OFFFFOOOFFOFF
78E00000	OFFFFOOOFFFOO
78E80000	OFFFFOOOFFFOF
78F00000	OFFFFOOOFFFFO
78F80000	OFFFFOOOFFFFF
79000000	OFFFFOOFOOOOO
79080000	OFFFFOOFOOOOF
79100000	OFFFFOOFOOOFO
79180000	OFFFFOOFOOOFF
79200000	OFFFFOOFOOFOO
79280000	OFFFFOOFOOFOF
79300000	OFFFFOOFOOFFO
79380000	OFFFFOOFOOFFF
79400000	OFFFFOOFOFOOO
79480000	OFFFFOOFOFOOF
79500000	OFFFFOOFOFOFO
79580000	OFFFFOOFOFOFF
79600000	OFFFFOOFOFFOO
79680000	OFFFFOOFOFFOF
79700000	OFFFFOOFOFFFO
79780000	OFFFFOOFOFFFF
79800000	OFFFFOOFFOOOO
79880000	OFFFFOOFFOOOF
79900000	OFFFFOOFFOOFO
79980000	OFFFFOOFFOOFF
79A00000	OFFFFOOFFOFOO
79A80000	OFFFFOOFFOFOF
79B00000	OFFFFOOFFOFFO
79B80000	OFFFFOOFFOFFF
79C00000	OFFFFOOFFFOOO
79C80000	OFFFFOOFFFOOF
79D00000	OFFFFOOFFFOFO
79D80000	OFFFFOOFFFOFF
79E00000	OFFFFOOFFFFOO
79E80000	OFFFFOOFFFFOF
79F00000	OFFFFOOFFFFFO
79F80000	OFFFFOOFFFFFF
7A000000	OFFFFOFOOOOOO
7A080000	OFFFFOFOOOOOF
7A100000	OFFFFOFOOOOFO
7A180000	OFFFFOFOOOOFF
7A200000	OFFFFOFOOOFOO
7A280000	OFFFFOFOOOFOF
7A300000	OFFFFOFOOOFFO
7A380000	OFFFFOFOOOFFF
7A400000	OFFFFOFOOFOOO
7A480000	OFFFFOFOOFOOF
7A500000	OFFFFOFOOFOFO
7A580000	OFFFFOFOOFOFF
7A600000	OFFFFOFOOFFOO
7A680000	OFFFFOFOOFFOF
7A700000	OFFFFOFOOFFFO
7A780000	OFFFFOFOOFFFF
7A800000	OFFFFOFOFOOOO
7A880000	OFFFFOFOFOOOF
7A900000	OFFFFOFOFOOFO
7A980000	OFFFFOFOFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
7AA00000	OFFFFOFOFOFOO
7AA80000	OFFFFOFOFOFOF
7AB00000	OFFFFOFOFOFFO
7AB80000	OFFFFOFOFOFFF
7AC00000	OFFFFOFOFFOOO
7AC80000	OFFFFOFOFFOOF
7AD00000	OFFFFOFOFFOFO
7AD80000	OFFFFOFOFFOFF
7AE00000	OFFFFOFOFFFOO
7AE80000	OFFFFOFOFFFOF
7AF00000	OFFFFOFOFFFFO
7AF80000	OFFFFOFOFFFFF
7B000000	OFFFFOFFOOOOO
7B080000	OFFFFOFFOOOOF
7B100000	OFFFFOFFOOOFO
7B180000	OFFFFOFFOOOFF
7B200000	OFFFFOFFOOFOO
7B280000	OFFFFOFFOOFOF
7B300000	OFFFFOFFOOFFO
7B380000	OFFFFOFFOOFFF
7B400000	OFFFFOFFOFOOO
7B480000	OFFFFOFFOFOOF
7B500000	OFFFFOFFOFOFO
7B580000	OFFFFOFFOFOFF
7B600000	OFFFFOFFOFFOO
7B680000	OFFFFOFFOFFOF
7B700000	OFFFFOFFOFFFO
7B780000	OFFFFOFFOFFFF
7B800000	OFFFFOFFFOOOO
7B880000	OFFFFOFFFOOOF
7B900000	OFFFFOFFFOOFO
7B980000	OFFFFOFFFOOFF
7BA00000	OFFFFOFFFOFOO
7BA80000	OFFFFOFFFOFOF
7BB00000	OFFFFOFFFOFFO
7BB80000	OFFFFOFFFOFFF
7BC00000	OFFFFOFFFFOOO
7BC80000	OFFFFOFFFFOOF
7BD00000	OFFFFOFFFFOFO
7BD80000	OFFFFOFFFFOFF
7BE00000	OFFFFOFFFFFOO
7BE80000	OFFFFOFFFFFOF
7BF00000	OFFFFOFFFFFFO
7BF80000	OFFFFOFFFFFFF
7C000000	OFFFFFOOOOOOO
7C080000	OFFFFFOOOOOOF
7C100000	OFFFFFOOOOOFO
7C180000	OFFFFFOOOOOFF
7C200000	OFFFFFOOOOFOO
7C280000	OFFFFFOOOOFOF
7C300000	OFFFFFOOOOFFO
7C380000	OFFFFFOOOOFFF
7C400000	OFFFFFOOOFOOO
7C480000	OFFFFFOOOFOOF
7C500000	OFFFFFOOOFOFO
7C580000	OFFFFFOOOFOFF
7C600000	OFFFFFOOOFFOO
7C680000	OFFFFFOOOFFOF
7C700000	OFFFFFOOOFFFO
7C780000	OFFFFFOOOFFFF
7C800000	OFFFFFOOFOOOO
7C880000	OFFFFFOOFOOOF
7C900000	OFFFFFOOFOOFO
7C980000	OFFFFFOOFOOFF
7CA00000	OFFFFFOOFOFOO
7CA80000	OFFFFFOOFOFOF
7CB00000	OFFFFFOOFOFFO
7CB80000	OFFFFFOOFOFFF
7CC00000	OFFFFFOOFFOOO
7CC80000	OFFFFFOOFFOOF
7CD00000	OFFFFFOOFFOFO
7CD80000	OFFFFFOOFFOFF
7CE00000	OFFFFFOOFFFOO
7CE80000	OFFFFFOOFFFOF
7CF00000	OFFFFFOOFFFFO
7CF80000	OFFFFFOOFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
7D000000	OFFFFFOFOOOOO
7D080000	OFFFFFOFOOOOF
7D100000	OFFFFFOFOOOFO
7D180000	OFFFFFOFOOOFF
7D200000	OFFFFFOFOOFOO
7D280000	OFFFFFOFOOFOF
7D300000	OFFFFFOFOOFFO
7D380000	OFFFFFOFOOFFF
7D400000	OFFFFFOFOFOOO
7D480000	OFFFFFOFOFOOF
7D500000	OFFFFFOFOFOFO
7D580000	OFFFFFOFOFOFF
7D600000	OFFFFFOFOFFOO
7D680000	OFFFFFOFOFFOF
7D700000	OFFFFFOFOFFFO
7D780000	OFFFFFOFOFFFF
7D800000	OFFFFFOFFOOOO
7D880000	OFFFFFOFFOOOF
7D900000	OFFFFFOFFOOFO
7D980000	OFFFFFOFFOOFF
7DA00000	OFFFFFOFFOFOO
7DA80000	OFFFFFOFFOFOF
7DB00000	OFFFFFOFFOFFO
7DB80000	OFFFFFOFFOFFF
7DC00000	OFFFFFOFFFOOO
7DC80000	OFFFFFOFFFOOF
7DD00000	OFFFFFOFFFOFO
7DD80000	OFFFFFOFFFOFF
7DE00000	OFFFFFOFFFFOO
7DE80000	OFFFFFOFFFFOF
7DF00000	OFFFFFOFFFFFO
7DF80000	OFFFFFOFFFFFF
7E000000	OFFFFFFOOOOOO
7E080000	OFFFFFFOOOOOF
7E100000	OFFFFFFOOOOFO
7E180000	OFFFFFFOOOOFF
7E200000	OFFFFFFOOOFOO
7E280000	OFFFFFFOOOFOF
7E300000	OFFFFFFOOOFFO
7E380000	OFFFFFFOOOFFF
7E400000	OFFFFFFOOFOOO
7E480000	OFFFFFFOOFOOF
7E500000	OFFFFFFOOFOFO
7E580000	OFFFFFFOOFOFF
7E600000	OFFFFFFOOFFOO
7E680000	OFFFFFFOOFFOF
7E700000	OFFFFFFOOFFFO
7E780000	OFFFFFFOOFFFF
7E800000	OFFFFFFOFOOOO
7E880000	OFFFFFFOFOOOF
7E900000	OFFFFFFOFOOFO
7E980000	OFFFFFFOFOOFF
7EA00000	OFFFFFFOFOFOO
7EA80000	OFFFFFFOFOFOF
7EB00000	OFFFFFFOFOFFO
7EB80000	OFFFFFFOFOFFF
7EC00000	OFFFFFFOFFOOO
7EC80000	OFFFFFFOFFOOF
7ED00000	OFFFFFFOFFOFO
7ED80000	OFFFFFFOFFOFF
7EE00000	OFFFFFFOFFFOO
7EE80000	OFFFFFFOFFFOF
7EF00000	OFFFFFFOFFFFO
7EF80000	OFFFFFFOFFFFF
7F000000	OFFFFFFFOOOOO
7F080000	OFFFFFFFOOOOF
7F100000	OFFFFFFFOOOFO
7F180000	OFFFFFFFOOOFF
7F200000	OFFFFFFFOOFOO
7F280000	OFFFFFFFOOFOF
7F300000	OFFFFFFFOOFFO
7F380000	OFFFFFFFOOFFF
7F400000	OFFFFFFFOFOOO
7F480000	OFFFFFFFOFOOF
7F500000	OFFFFFFFOFOFO
7F580000	OFFFFFFFOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
7F600000	OFFFFFFFOFFOO
7F680000	OFFFFFFFOFFOF
7F700000	OFFFFFFFOFFFO
7F780000	OFFFFFFFOFFFF
7F800000	OFFFFFFFFOOOO
7F880000	OFFFFFFFFOOOF
7F900000	OFFFFFFFFOOFO
7F980000	OFFFFFFFFOOFF
7FA00000	OFFFFFFFFOFOO
7FA80000	OFFFFFFFFOFOF
7FB00000	OFFFFFFFFOFFO
7FB80000	OFFFFFFFFOFFF
7FC00000	OFFFFFFFFFOOO
7FC80000	OFFFFFFFFFOOF
7FD00000	OFFFFFFFFFOFO
7FD80000	OFFFFFFFFFOFF
7FE00000	OFFFFFFFFFFOO
7FE80000	OFFFFFFFFFFOF
7FF00000	OFFFFFFFFFFFO
7FF80000	OFFFFFFFFFFFF
80000000	FOOOOOOOOOOOO
80080000	FOOOOOOOOOOOF
80100000	FOOOOOOOOOOFO
80180000	FOOOOOOOOOOFF
80200000	FOOOOOOOOOFOO
80280000	FOOOOOOOOOFOF
80300000	FOOOOOOOOOFFO
80380000	FOOOOOOOOOFFF
80400000	FOOOOOOOOFOOO
80480000	FOOOOOOOOFOOF
80500000	FOOOOOOOOFOFO
80580000	FOOOOOOOOFOFF
80600000	FOOOOOOOOFFOO
80680000	FOOOOOOOOFFOF
80700000	FOOOOOOOOFFFO
80780000	FOOOOOOOOFFFF
80800000	FOOOOOOOFOOOO
80880000	FOOOOOOOFOOOF
80900000	FOOOOOOOFOOFO
80980000	FOOOOOOOFOOFF
80A00000	FOOOOOOOFOFOO
80A80000	FOOOOOOOFOFOF
80B00000	FOOOOOOOFOFFO
80B80000	FOOOOOOOFOFFF
80C00000	FOOOOOOOFFOOO
80C80000	FOOOOOOOFFOOF
80D00000	FOOOOOOOFFOFO
80D80000	FOOOOOOOFFOFF
80E00000	FOOOOOOOFFFOO
80E80000	FOOOOOOOFFFOF
80F00000	FOOOOOOOFFFFO
80F80000	FOOOOOOOFFFFF
81000000	FOOOOOFOOOOOO
81080000	FOOOOOFOOOOOF
81100000	FOOOOOFOOOOFO
81180000	FOOOOOFOOOOFF
81200000	FOOOOOFOOFOOO
81280000	FOOOOOFOOFOOF
81300000	FOOOOOFOOFOFO
81380000	FOOOOOFOOFOFF
81400000	FOOOOOFOFOOOO
81480000	FOOOOOFOFOOOF
81500000	FOOOOOFOFOOFO
81580000	FOOOOOFOFOOFF
81600000	FOOOOOFOFFOOO
81680000	FOOOOOFOFFOOF
81700000	FOOOOOFOFFOFO
81780000	FOOOOOFOFFOFF
81800000	FOOOOOFFOOOOO
81880000	FOOOOOFFOOOOF
81900000	FOOOOOFFOOOFO
81980000	FOOOOOFFOOOFF
81A00000	FOOOOOFFOFOOO
81A80000	FOOOOOFFOFOOF
81B00000	FOOOOOFFOFOFO
81B80000	FOOOOOFFOFOFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
81C00000	FOOOOOOFFFOOO
81C80000	FOOOOOOFFFOOF
81D00000	FOOOOOOFFFOFO
81D80000	FOOOOOOFFFOFF
81E00000	FOOOOOOFFFFOO
81E80000	FOOOOOOFFFFOF
81F00000	FOOOOOOFFFFFO
81F80000	FOOOOOOFFFFFF
82000000	FOOOOOFOOOOOO
82080000	FOOOOOFOOOOOF
82100000	FOOOOOFOOOOFO
82180000	FOOOOOFOOOOFF
82200000	FOOOOOFOOOFOO
82280000	FOOOOOFOOOFOF
82300000	FOOOOOFOOOFFO
82380000	FOOOOOFOOOFFF
82400000	FOOOOOFOOFOOO
82480000	FOOOOOFOOFOOF
82500000	FOOOOOFOOFOFO
82580000	FOOOOOFOOFOFF
82600000	FOOOOOFOOFFOO
82680000	FOOOOOFOOFFOF
82700000	FOOOOOFOOFFFO
82780000	FOOOOOFOOFFFF
82800000	FOOOOOFOFOOOO
82880000	FOOOOOFOFOOOF
82900000	FOOOOOFOFOOFO
82980000	FOOOOOFOFOOFF
82A00000	FOOOOOFOFOFOO
82A80000	FOOOOOFOFOFOF
82B00000	FOOOOOFOFOFFO
82B80000	FOOOOOFOFOFFF
82C00000	FOOOOOFOFFOOO
82C80000	FOOOOOFOFFOOF
82D00000	FOOOOOFOFFOFO
82D80000	FOOOOOFOFFOFF
82E00000	FOOOOOFOFFFOO
82E80000	FOOOOOFOFFFOF
82F00000	FOOOOOFOFFFFO
82F80000	FOOOOOFOFFFFF
83000000	FOOOOOFFOOOOO
83080000	FOOOOOFFOOOOF
83100000	FOOOOOFFOOOFO
83180000	FOOOOOFFOOOFF
83200000	FOOOOOFFOOFOO
83280000	FOOOOOFFOOFOF
83300000	FOOOOOFFOOFFO
83380000	FOOOOOFFOOFFF
83400000	FOOOOOFFOFOOO
83480000	FOOOOOFFOFOOF
83500000	FOOOOOFFOFOFO
83580000	FOOOOOFFOFOFF
83600000	FOOOOOFFOFFOO
83680000	FOOOOOFFOFFOF
83700000	FOOOOOFFOFFFO
83780000	FOOOOOFFOFFFF
83800000	FOOOOOFFFOOOO
83880000	FOOOOOFFFOOOF
83900000	FOOOOOFFFOOFO
83980000	FOOOOOFFFOOFF
83A00000	FOOOOOFFFOFOO
83A80000	FOOOOOFFFOFOF
83B00000	FOOOOOFFFOFFO
83B80000	FOOOOOFFFOFFF
83C00000	FOOOOOFFFFOOO
83C80000	FOOOOOFFFFOOF
83D00000	FOOOOOFFFFOFO
83D80000	FOOOOOFFFFOFF
83E00000	FOOOOOFFFFFOO
83E80000	FOOOOOFFFFFOF
83F00000	FOOOOOFFFFFFO
83F80000	FOOOOOFFFFFFF
84000000	FOOOOFOOOOOOO
84080000	FOOOOFOOOOOOF
84100000	FOOOOFOOOOOFO
84180000	FOOOOFOOOOOFF
84200000	FOOOOFOOOOFOO
84280000	FOOOOFOOOOFOF
84300000	FOOOOFOOOOFFO
84380000	FOOOOFOOOOFFF
84400000	FOOOOFOOOFOOO
84480000	FOOOOFOOOFOOF
84500000	FOOOOFOOOFOFO
84580000	FOOOOFOOOFOFF
84600000	FOOOOFOOOFFOO
84680000	FOOOOFOOOFFOF
84700000	FOOOOFOOOFFFO
84780000	FOOOOFOOOFFFF
84800000	FOOOOFOOFOOOO
84880000	FOOOOFOOFOOOF
84900000	FOOOOFOOFOOFO
84980000	FOOOOFOOFOOFF
84A00000	FOOOOFOOFOFOO
84A80000	FOOOOFOOFOFOF
84B00000	FOOOOFOOFOFFO
84B80000	FOOOOFOOFOFFF
84C00000	FOOOOFOOFFOOO
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84D00000	FOOOOFOOFFOFO
84D80000	FOOOOFOOFFOFF
84E00000	FOOOOFOOFFFOO
84E80000	FOOOOFOOFFFOF
84F00000	FOOOOFOOFFFFO
84F80000	FOOOOFOOFFFFF
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85300000	FOOOOFOFOOFFO
85380000	FOOOOFOFOOFFF
85400000	FOOOOFOFOFOOO
85480000	FOOOOFOFOFOOF
85500000	FOOOOFOFOFOFO
85580000	FOOOOFOFOFOFF
85600000	FOOOOFOFOFFOO
85680000	FOOOOFOFOFFOF
85700000	FOOOOFOFOFFFO
85780000	FOOOOFOFOFFFF
85800000	FOOOOFOFFOOOO
85880000	FOOOOFOFFOOOF
85900000	FOOOOFOFFOOFO
85980000	FOOOOFOFFOOFF
85A00000	FOOOOFOFFOFOO
85A80000	FOOOOFOFFOFOF
85B00000	FOOOOFOFFOFFO
85B80000	FOOOOFOFFOFFF
85C00000	FOOOOFOFFFOOO
85C80000	FOOOOFOFFFOOF
85D00000	FOOOOFOFFFOFO
85D80000	FOOOOFOFFFOFF
85E00000	FOOOOFOFFFFOO
85E80000	FOOOOFOFFFFOF
85F00000	FOOOOFOFFFFFO
85F80000	FOOOOFOFFFFFF
86000000	FOOOOFFOOOOOO
86080000	FOOOOFFOOOOOF
86100000	FOOOOFFOOOOFO
86180000	FOOOOFFOOOOFF
86200000	FOOOOFFOOOFOO
86280000	FOOOOFFOOOFOF
86300000	FOOOOFFOOOFFO
86380000	FOOOOFFOOOFFF
86400000	FOOOOFFOOFOOO
86480000	FOOOOFFOOFOOF
86500000	FOOOOFFOOFOFO
86580000	FOOOOFFOOFOFF
86600000	FOOOOFFOOFFOO
86680000	FOOOOFFOOFFOF
86700000	FOOOOFFOOFFFO
86780000	FOOOOFFOOFFFF
86800000	FOOOOFFOFOOOO
86880000	FOOOOFFOFOOOF
86900000	FOOOOFFOFOOFO
86980000	FOOOOFFOFOOFF
86A00000	FOOOOFFOFOFOO
86A80000	FOOOOFFOFOFOF
86B00000	FOOOOFFOFOFFO
86B80000	FOOOOFFOFOFFF
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86D80000	FOOOOFFOFFOFF
86E00000	FOOOOFFOFFFOO
86E80000	FOOOOFFOFFFOF
86F00000	FOOOOFFOFFFFO
86F80000	FOOOOFFOFFFFF
87000000	FOOOOFFFOOOOO
87080000	FOOOOFFFOOOOF
87100000	FOOOOFFFOOOFO
87180000	FOOOOFFFOOOFF
87200000	FOOOOFFFOOFOO
87280000	FOOOOFFFOOFOF
87300000	FOOOOFFFOOFFO
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87400000	FOOOOFFFOFOOO
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87580000	FOOOOFFFOFOFF
87600000	FOOOOFFFOFFOO
87680000	FOOOOFFFOFFOF
87700000	FOOOOFFFOFFFO
87780000	FOOOOFFFOFFFF
87800000	FOOOOFFFFOOOO
87880000	FOOOOFFFFOOOF
87900000	FOOOOFFFFOOFO
87980000	FOOOOFFFFOOFF
87A00000	FOOOOFFFFOFOO
87A80000	FOOOOFFFFOFOF
87B00000	FOOOOFFFFOFFO
87B80000	FOOOOFFFFOFFF
87C00000	FOOOOFFFFFOOO
87C80000	FOOOOFFFFFOOF
87D00000	FOOOOFFFFFOFO
87D80000	FOOOOFFFFFOFF
87E00000	FOOOOFFFFFFOO
87E80000	FOOOOFFFFFFOF
87F00000	FOOOOFFFFFFFO
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88100000	FOOOFOOOOOOFO
88180000	FOOOFOOOOOOFF
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88280000	FOOOFOOOOOFOF
88300000	FOOOFOOOOOFFO
88380000	FOOOFOOOOOFFF
88400000	FOOOFOOOOFOOO
88480000	FOOOFOOOOFOOF
88500000	FOOOFOOOOFOFO
88580000	FOOOFOOOOFOFF
88600000	FOOOFOOOOFFOO
88680000	FOOOFOOOOFFOF
88700000	FOOOFOOOOFFFO
88780000	FOOOFOOOOFFFF
88800000	FOOOFOOOFOOOO
88880000	FOOOFOOOFOOOF
88900000	FOOOFOOOFOOFO
88980000	FOOOFOOOFOOFF
88A00000	FOOOFOOOFOFOO
88A80000	FOOOFOOOFOFOF
88B00000	FOOOFOOOFOFFO
88B80000	FOOOFOOOFOFFF
88C00000	FOOOFOOOFFOOO
88C80000	FOOOFOOOFFOOF
88D00000	FOOOFOOOFFOFO
88D80000	FOOOFOOOFFOFF
88E00000	FOOOFOOOFFFOO
88E80000	FOOOFOOOFFFOF
88F00000	FOOOFOOOFFFFO
88F80000	FOOOFOOOFFFFF
89000000	FOOOFOOFOOOOO
89080000	FOOOFOOFOOOOF
89100000	FOOOFOOFOOOFO
89180000	FOOOFOOFOOOFF
89200000	FOOOFOOFOOFOO
89280000	FOOOFOOFOOFOF
89300000	FOOOFOOFOOFFO
89380000	FOOOFOOFOOFFF
89400000	FOOOFOOFOFOOO
89480000	FOOOFOOFOFOOF
89500000	FOOOFOOFOFOFO
89580000	FOOOFOOFOFOFF
89600000	FOOOFOOFOFFOO
89680000	FOOOFOOFOFFOF
89700000	FOOOFOOFOFFFO
89780000	FOOOFOOFOFFFF
89800000	FOOOFOOFFOOOO
89880000	FOOOFOOFFOOOF
89900000	FOOOFOOFFOOFO
89980000	FOOOFOOFFOOFF
89A00000	FOOOFOOFFOFOO
89A80000	FOOOFOOFFOFOF
89B00000	FOOOFOOFFOFFO
89B80000	FOOOFOOFFOFFF
89C00000	FOOOFOOFFFOOO
89C80000	FOOOFOOFFFOOF
89D00000	FOOOFOOFFFOFO
89D80000	FOOOFOOFFFOFF
89E00000	FOOOFOOFFFFOO
89E80000	FOOOFOOFFFFOF
89F00000	FOOOFOOFFFFFO
89F80000	FOOOFOOFFFFFF
8A000000	FOOOFOFOOOOOO
8A080000	FOOOFOFOOOOOF
8A100000	FOOOFOFOOOOFO
8A180000	FOOOFOFOOOOFF
8A200000	FOOOFOFOOOFOO
8A280000	FOOOFOFOOOFOF
8A300000	FOOOFOFOOOFFO
8A380000	FOOOFOFOOOFFF
8A400000	FOOOFOFOOFOOO
8A480000	FOOOFOFOOFOOF
8A500000	FOOOFOFOOFOFO
8A580000	FOOOFOFOOFOFF
8A600000	FOOOFOFOOFFOO
8A680000	FOOOFOFOOFFOF
8A700000	FOOOFOFOOFFFO
8A780000	FOOOFOFOOFFFF
8A800000	FOOOFOFOFOOOO
8A880000	FOOOFOFOFOOOF
8A900000	FOOOFOFOFOOFO
8A980000	FOOOFOFOFOOFF
8AA00000	FOOOFOFOFOFOO
8AA80000	FOOOFOFOFOFOF
8AB00000	FOOOFOFOFOFFO
8AB80000	FOOOFOFOFOFFF
8AC00000	FOOOFOFOFFOOO
8AC80000	FOOOFOFOFFOOF
8AD00000	FOOOFOFOFFOFO
8AD80000	FOOOFOFOFFOFF
8AE00000	FOOOFOFOFFFOO
8AE80000	FOOOFOFOFFFOF
8AF00000	FOOOFOFOFFFFO
8AF80000	FOOOFOFOFFFFF
8B000000	FOOOFOFFOOOOO
8B080000	FOOOFOFFOOOOF
8B100000	FOOOFOFFOOOFO
8B180000	FOOOFOFFOOOFF
8B200000	FOOOFOFFOOFOO
8B280000	FOOOFOFFOOFOF
8B300000	FOOOFOFFOOFFO
8B380000	FOOOFOFFOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
8B400000	FOOOFOFFOFOOO
8B480000	FOOOFOFFOFOOF
8B500000	FOOOFOFFOFOFO
8B580000	FOOOFOFFOFOFF
8B600000	FOOOFOFFOFFOO
8B680000	FOOOFOFFOFFOF
8B700000	FOOOFOFFOFFFO
8B780000	FOOOFOFFOFFFF
8B800000	FOOOFOFFFOOOO
8B880000	FOOOFOFFFOOOF
8B900000	FOOOFOFFFOOFO
8B980000	FOOOFOFFFOOFF
8BA00000	FOOOFOFFFOFOO
8BA80000	FOOOFOFFFOFOF
8BB00000	FOOOFOFFFOFFO
8BB80000	FOOOFOFFFOFFF
8BC00000	FOOOFOFFFFOOO
8BC80000	FOOOFOFFFFOOF
8BD00000	FOOOFOFFFFOFO
8BD80000	FOOOFOFFFFOFF
8BE00000	FOOOFOFFFFFOO
8BE80000	FOOOFOFFFFFOF
8BF00000	FOOOFOFFFFFFO
8BF80000	FOOOFOFFFFFFF
8C000000	FOOOFFOOOOOOO
8C080000	FOOOFFOOOOOOF
8C100000	FOOOFFOOOOOFO
8C180000	FOOOFFOOOOOFF
8C200000	FOOOFFOOOOFOO
8C280000	FOOOFFOOOOFOF
8C300000	FOOOFFOOOOFFO
8C380000	FOOOFFOOOOFFF
8C400000	FOOOFFOOOFOOO
8C480000	FOOOFFOOOFOOF
8C500000	FOOOFFOOOFOFO
8C580000	FOOOFFOOOFOFF
8C600000	FOOOFFOOOFFOO
8C680000	FOOOFFOOOFFOF
8C700000	FOOOFFOOOFFFO
8C780000	FOOOFFOOOFFFF
8C800000	FOOOFFOOFOOOO
8C880000	FOOOFFOOFOOOF
8C900000	FOOOFFOOFOOFO
8C980000	FOOOFFOOFOOFF
8CA00000	FOOOFFOOFOFOO
8CA80000	FOOOFFOOFOFOF
8CB00000	FOOOFFOOFOFFO
8CB80000	FOOOFFOOFOFFF
8CC00000	FOOOFFOOFFOOO
8CC80000	FOOOFFOOFFOOF
8CD00000	FOOOFFOOFFOFO
8CD80000	FOOOFFOOFFOFF
8CE00000	FOOOFFOOFFFOO
8CE80000	FOOOFFOOFFFOF
8CF00000	FOOOFFOOFFFFO
8CF80000	FOOOFFOOFFFFF
8D000000	FOOOFFOFOOOOO
8D080000	FOOOFFOFOOOOF
8D100000	FOOOFFOFOOOFO
8D180000	FOOOFFOFOOOFF
8D200000	FOOOFFOFOOFOO
8D280000	FOOOFFOFOOFOF
8D300000	FOOOFFOFOOFFO
8D380000	FOOOFFOFOOFFF
8D400000	FOOOFFOFOFOOO
8D480000	FOOOFFOFOFOOF
8D500000	FOOOFFOFOFOFO
8D580000	FOOOFFOFOFOFF
8D600000	FOOOFFOFOFFOO
8D680000	FOOOFFOFOFFOF
8D700000	FOOOFFOFOFFFO
8D780000	FOOOFFOFOFFFF
8D800000	FOOOFFOFFOOOO
8D880000	FOOOFFOFFOOOF
8D900000	FOOOFFOFFOOFO
8D980000	FOOOFFOFFOOFF
8DA00000	FOOOFFOFFOFOO
8DA80000	FOOOFFOFFOFOF
8DB00000	FOOOFFOFFOFFO
8DB80000	FOOOFFOFFOFFF
8DC00000	FOOOFFOFFFOOO
8DC80000	FOOOFFOFFFOOF
8DD00000	FOOOFFOFFFOFO
8DD80000	FOOOFFOFFFOFF
8DE00000	FOOOFFOFFFFOO
8DE80000	FOOOFFOFFFFOF
8DF00000	FOOOFFOFFFFFO
8DF80000	FOOOFFOFFFFFF
8E000000	FOOOFFFOOOOOO
8E080000	FOOOFFFOOOOOF
8E100000	FOOOFFFOOOOFO
8E180000	FOOOFFFOOOOFF
8E200000	FOOOFFFOOOFOO
8E280000	FOOOFFFOOOFOF
8E300000	FOOOFFFOOOFFO
8E380000	FOOOFFFOOOFFF
8E400000	FOOOFFFOOFOOO
8E480000	FOOOFFFOOFOOF
8E500000	FOOOFFFOOFOFO
8E580000	FOOOFFFOOFOFF
8E600000	FOOOFFFOOFFOO
8E680000	FOOOFFFOOFFOF
8E700000	FOOOFFFOOFFFO
8E780000	FOOOFFFOOFFFF
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8EC00000	FOOOFFFOFFOOO
8EC80000	FOOOFFFOFFOOF
8ED00000	FOOOFFFOFFOFO
8ED80000	FOOOFFFOFFOFF
8EE00000	FOOOFFFOFFFOO
8EE80000	FOOOFFFOFFFOF
8EF00000	FOOOFFFOFFFFO
8EF80000	FOOOFFFOFFFFF
8F000000	FOOOFFFFOOOOO
8F080000	FOOOFFFFOOOOF
8F100000	FOOOFFFFOOOFO
8F180000	FOOOFFFFOOOFF
8F200000	FOOOFFFFOOFOO
8F280000	FOOOFFFFOOFOF
8F300000	FOOOFFFFOOFFO
8F380000	FOOOFFFFOOFFF
8F400000	FOOOFFFFOFOOO
8F480000	FOOOFFFFOFOOF
8F500000	FOOOFFFFOFOFO
8F580000	FOOOFFFFOFOFF
8F600000	FOOOFFFFOFFOO
8F680000	FOOOFFFFOFFOF
8F700000	FOOOFFFFOFFFO
8F780000	FOOOFFFFOFFFF
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8F880000	FOOOFFFFFOOOF
8F900000	FOOOFFFFFOOFO
8F980000	FOOOFFFFFOOFF
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8FB00000	FOOOFFFFFOFFO
8FB80000	FOOOFFFFFOFFF
8FC00000	FOOOFFFFFFOOO
8FC80000	FOOOFFFFFFOOF
8FD00000	FOOOFFFFFFOFO
8FD80000	FOOOFFFFFFOFF
8FE00000	FOOOFFFFFFFOO
8FE80000	FOOOFFFFFFFOF
8FF00000	FOOOFFFFFFFFO
8FF80000	FOOOFFFFFFFFF
90000000	FOOFOOOOOOOOO
90080000	FOOFOOOOOOOOF
90100000	FOOFOOOOOOOFO
90180000	FOOFOOOOOOOFF
90200000	FOOFOOOOOOFOO
90280000	FOOFOOOOOOFOF
90300000	FOOFOOOOOOFFO
90380000	FOOFOOOOOOFFF
90400000	FOOFOOOOOFOOO
90480000	FOOFOOOOOFOOF
90500000	FOOFOOOOOFOFO
90580000	FOOFOOOOOFOFF
90600000	FOOFOOOOOFFOO
90680000	FOOFOOOOOFFOF
90700000	FOOFOOOOOFFFO
90780000	FOOFOOOOOFFFF
90800000	FOOFOOOOFOOOO
90880000	FOOFOOOOFOOOF
90900000	FOOFOOOOFOOFO
90980000	FOOFOOOOFOOFF
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91100000	FOOFOOOFOOOFO
91180000	FOOFOOOFOOOFF
91200000	FOOFOOOFOOFOO
91280000	FOOFOOOFOOFOF
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91380000	FOOFOOOFOOFFF
91400000	FOOFOOOFOFOOO
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91500000	FOOFOOOFOFOFO
91580000	FOOFOOOFOFOFF
91600000	FOOFOOOFOFFOO
91680000	FOOFOOOFOFFOF
91700000	FOOFOOOFOFFFO
91780000	FOOFOOOFOFFFF
91800000	FOOFOOOFFOOOO
91880000	FOOFOOOFFOOOF
91900000	FOOFOOOFFOOFO
91980000	FOOFOOOFFOOFF
91A00000	FOOFOOOFFOFOO
91A80000	FOOFOOOFFOFOF
91B00000	FOOFOOOFFOFFO
91B80000	FOOFOOOFFOFFF
91C00000	FOOFOOOFFFOOO
91C80000	FOOFOOOFFFOOF
91D00000	FOOFOOOFFFOFO
91D80000	FOOFOOOFFFOFF
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91F00000	FOOFOOOFFFFFO
91F80000	FOOFOOOFFFFFF
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92080000	FOOFOOFOOOOOF
92100000	FOOFOOFOOOOFO
92180000	FOOFOOFOOOOFF
92200000	FOOFOOFOOOFOO
92280000	FOOFOOFOOOFOF
92300000	FOOFOOFOOOFFO
92380000	FOOFOOFOOOFFF
92400000	FOOFOOFOOFOOO
92480000	FOOFOOFOOFOOF
92500000	FOOFOOFOOFOFO
92580000	FOOFOOFOOFOFF
92600000	FOOFOOFOOFFOO
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92700000	FOOFOOFOOFFFO
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92980000	FOOFOOFOFOOFF
92A00000	FOOFOOFOFOFOO
92A80000	FOOFOOFOFOFOF
92B00000	FOOFOOFOFOFFO
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92E00000	FOOFOOFOFFFOO
92E80000	FOOFOOFOFFFOF
92F00000	FOOFOOFOFFFFO
92F80000	FOOFOOFOFFFFF
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93080000	FOOFOOFFOOOOF
93100000	FOOFOOFFOOOFO
93180000	FOOFOOFFOOOFF
93200000	FOOFOOFFOOFOO
93280000	FOOFOOFFOOFOF
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93380000	FOOFOOFFOOFFF
93400000	FOOFOOFFOFOOO
93480000	FOOFOOFFOFOOF
93500000	FOOFOOFFOFOFO
93580000	FOOFOOFFOFOFF
93600000	FOOFOOFFOFFOO
93680000	FOOFOOFFOFFOF
93700000	FOOFOOFFOFFFO
93780000	FOOFOOFFOFFFF
93800000	FOOFOOFFFOOOO
93880000	FOOFOOFFFOOOF
93900000	FOOFOOFFFOOFO
93980000	FOOFOOFFFOOFF
93A00000	FOOFOOFFFOFOO
93A80000	FOOFOOFFFOFOF
93B00000	FOOFOOFFFOFFO
93B80000	FOOFOOFFFOFFF
93C00000	FOOFOOFFFFOOO
93C80000	FOOFOOFFFFOOF
93D00000	FOOFOOFFFFOFO
93D80000	FOOFOOFFFFOFF
93E00000	FOOFOOFFFFFOO
93E80000	FOOFOOFFFFFOF
93F00000	FOOFOOFFFFFFO
93F80000	FOOFOOFFFFFFF
94000000	FOOFOFOOOOOOO
94080000	FOOFOFOOOOOOF
94100000	FOOFOFOOOOOFO
94180000	FOOFOFOOOOOFF
94200000	FOOFOFOOOOFOO
94280000	FOOFOFOOOOFOF
94300000	FOOFOFOOOOFFO
94380000	FOOFOFOOOOFFF
94400000	FOOFOFOOOFOOO
94480000	FOOFOFOOOFOOF
94500000	FOOFOFOOOFOFO
94580000	FOOFOFOOOFOFF
94600000	FOOFOFOOOFFOO
94680000	FOOFOFOOOFFOF
94700000	FOOFOFOOOFFFO
94780000	FOOFOFOOOFFFF
94800000	FOOFOFOOFOOOO
94880000	FOOFOFOOFOOOF
94900000	FOOFOFOOFOOFO
94980000	FOOFOFOOFOOFF
94A00000	FOOFOFOOFOFOO
94A80000	FOOFOFOOFOFOF
94B00000	FOOFOFOOFOFFO
94B80000	FOOFOFOOFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
94C00000	FOOFOFOOFFOOO
94C80000	FOOFOFOOFFOOF
94D00000	FOOFOFOOFFOFO
94D80000	FOOFOFOOFFOFF
94E00000	FOOFOFOOFFFOO
94E80000	FOOFOFOOFFFOF
94F00000	FOOFOFOOFFFFO
94F80000	FOOFOFOOFFFFF
95000000	FOOFOFOFOOOOO
95080000	FOOFOFOFOOOOF
95100000	FOOFOFOFOOOFO
95180000	FOOFOFOFOOOFF
95200000	FOOFOFOFOOFOO
95280000	FOOFOFOFOOFOF
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95380000	FOOFOFOFOOFFF
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95480000	FOOFOFOFOFOOF
95500000	FOOFOFOFOFOFO
95580000	FOOFOFOFOFOFF
95600000	FOOFOFOFOFFOO
95680000	FOOFOFOFOFFOF
95700000	FOOFOFOFOFFFO
95780000	FOOFOFOFOFFFF
95800000	FOOFOFOFFOOOO
95880000	FOOFOFOFFOOOF
95900000	FOOFOFOFFOOFO
95980000	FOOFOFOFFOOFF
95A00000	FOOFOFOFFOFOO
95A80000	FOOFOFOFFOFOF
95B00000	FOOFOFOFFOFFO
95B80000	FOOFOFOFFOFFF
95C00000	FOOFOFOFFFOOO
95C80000	FOOFOFOFFFOOF
95D00000	FOOFOFOFFFOFO
95D80000	FOOFOFOFFFOFF
95E00000	FOOFOFOFFFFOO
95E80000	FOOFOFOFFFFOF
95F00000	FOOFOFOFFFFFO
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96080000	FOOFOFFOOOOOF
96100000	FOOFOFFOOOOFO
96180000	FOOFOFFOOOOFF
96200000	FOOFOFFOOOFOO
96280000	FOOFOFFOOOFOF
96300000	FOOFOFFOOOFFO
96380000	FOOFOFFOOOFFF
96400000	FOOFOFFOOFOOO
96480000	FOOFOFFOOFOOF
96500000	FOOFOFFOOFOFO
96580000	FOOFOFFOOFOFF
96600000	FOOFOFFOOFFOO
96680000	FOOFOFFOOFFOF
96700000	FOOFOFFOOFFFO
96780000	FOOFOFFOOFFFF
96800000	FOOFOFFOFOOOO
96880000	FOOFOFFOFOOOF
96900000	FOOFOFFOFOOFO
96980000	FOOFOFFOFOOFF
96A00000	FOOFOFFOFOFOO
96A80000	FOOFOFFOFOFOF
96B00000	FOOFOFFOFOFFO
96B80000	FOOFOFFOFOFFF
96C00000	FOOFOFFOFFOOO
96C80000	FOOFOFFOFFOOF
96D00000	FOOFOFFOFFOFO
96D80000	FOOFOFFOFFOFF
96E00000	FOOFOFFOFFFOO
96E80000	FOOFOFFOFFFOF
96F00000	FOOFOFFOFFFFO
96F80000	FOOFOFFOFFFFF
97000000	FOOFOFFFOOOOO
97080000	FOOFOFFFOOOOF
97100000	FOOFOFFFOOOFO
97180000	FOOFOFFFOOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
97200000	FOOFOFFFOOFOO
97280000	FOOFOFFFOOFOF
97300000	FOOFOFFFOOFFO
97380000	FOOFOFFFOOFFF
97400000	FOOFOFFFOFOOO
97480000	FOOFOFFFOFOOF
97500000	FOOFOFFFOFOFO
97580000	FOOFOFFFOFOFF
97600000	FOOFOFFFOFFOO
97680000	FOOFOFFFOFFOF
97700000	FOOFOFFFOFFFO
97780000	FOOFOFFFOFFFF
97800000	FOOFOFFFFOOOO
97880000	FOOFOFFFFOOOF
97900000	FOOFOFFFFOOFO
97980000	FOOFOFFFFOOFF
97A00000	FOOFOFFFFOFOO
97A80000	FOOFOFFFFOFOF
97B00000	FOOFOFFFFOFFO
97B80000	FOOFOFFFFOFFF
97C00000	FOOFOFFFFFOOO
97C80000	FOOFOFFFFFOOF
97D00000	FOOFOFFFFFOFO
97D80000	FOOFOFFFFFOFF
97E00000	FOOFOFFFFFFOO
97E80000	FOOFOFFFFFFOF
97F00000	FOOFOFFFFFFFO
97F80000	FOOFOFFFFFFFF
98000000	FOOFFOOOOOOOO
98080000	FOOFFOOOOOOOF
98100000	FOOFFOOOOOOFO
98180000	FOOFFOOOOOOFF
98200000	FOOFFOOOOOFOO
98280000	FOOFFOOOOOFOF
98300000	FOOFFOOOOOFFO
98380000	FOOFFOOOOOFFF
98400000	FOOFFOOOOFOOO
98480000	FOOFFOOOOFOOF
98500000	FOOFFOOOOFOFO
98580000	FOOFFOOOOFOFF
98600000	FOOFFOOOOFFOO
98680000	FOOFFOOOOFFOF
98700000	FOOFFOOOOFFFO
98780000	FOOFFOOOOFFFF
98800000	FOOFFOOOFOOOO
98880000	FOOFFOOOFOOOF
98900000	FOOFFOOOFOOFO
98980000	FOOFFOOOFOOFF
98A00000	FOOFFOOOFOFOO
98A80000	FOOFFOOOFOFOF
98B00000	FOOFFOOOFOFFO
98B80000	FOOFFOOOFOFFF
98C00000	FOOFFOOOFFOOO
98C80000	FOOFFOOOFFOOF
98D00000	FOOFFOOOFFOFO
98D80000	FOOFFOOOFFOFF
98E00000	FOOFFOOOFFFOO
98E80000	FOOFFOOOFFFOF
98F00000	FOOFFOOOFFFFO
98F80000	FOOFFOOOFFFFF
99000000	FOOFFOOFOOOOO
99080000	FOOFFOOFOOOOF
99100000	FOOFFOOFOOOFO
99180000	FOOFFOOFOOOFF
99200000	FOOFFOOFOOFOO
99280000	FOOFFOOFOOFOF
99300000	FOOFFOOFOOFFO
99380000	FOOFFOOFOOFFF
99400000	FOOFFOOFOFOOO
99480000	FOOFFOOFOFOOF
99500000	FOOFFOOFOFOFO
99580000	FOOFFOOFOFOFF
99600000	FOOFFOOFOFFOO
99680000	FOOFFOOFOFFOF
99700000	FOOFFOOFOFFFO
99780000	FOOFFOOFOFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
99800000	FOOFFOOFFOOOO
99880000	FOOFFOOFFOOOF
99900000	FOOFFOOFFOOFO
99980000	FOOFFOOFFOOFF
99A00000	FOOFFOOFFOFOO
99A80000	FOOFFOOFFOFOF
99B00000	FOOFFOOFFOFFO
99B80000	FOOFFOOFFOFFF
99C00000	FOOFFOOFFFOOO
99C80000	FOOFFOOFFFOOF
99D00000	FOOFFOOFFFOFO
99D80000	FOOFFOOFFFOFF
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99E80000	FOOFFOOFFFFOF
99F00000	FOOFFOOFFFFFO
99F80000	FOOFFOOFFFFFF
9A000000	FOOFFOFOOOOOO
9A080000	FOOFFOFOOOOOF
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9A300000	FOOFFOFOOOFFO
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9A400000	FOOFFOFOOFOOO
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9A580000	FOOFFOFOOFOFF
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9A700000	FOOFFOFOOFFFO
9A780000	FOOFFOFOOFFFF
9A800000	FOOFFOFOFOOOO
9A880000	FOOFFOFOFOOOF
9A900000	FOOFFOFOFOOFO
9A980000	FOOFFOFOFOOFF
9AA00000	FOOFFOFOFOFOO
9AA80000	FOOFFOFOFOFOF
9AB00000	FOOFFOFOFOFFO
9AB80000	FOOFFOFOFOFFF
9AC00000	FOOFFOFOFFOOO
9AC80000	FOOFFOFOFFOOF
9AD00000	FOOFFOFOFFOFO
9AD80000	FOOFFOFOFFOFF
9AE00000	FOOFFOFOFFFOO
9AE80000	FOOFFOFOFFFOF
9AF00000	FOOFFOFOFFFFO
9AF80000	FOOFFOFOFFFFF
9B000000	FOOFFOFFOOOOO
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9B300000	FOOFFOFFOOFFO
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9B500000	FOOFFOFFOFOFO
9B580000	FOOFFOFFOFOFF
9B600000	FOOFFOFFOFFOO
9B680000	FOOFFOFFOFFOF
9B700000	FOOFFOFFOFFFO
9B780000	FOOFFOFFOFFFF
9B800000	FOOFFOFFFOOOO
9B880000	FOOFFOFFFOOOF
9B900000	FOOFFOFFFOOFO
9B980000	FOOFFOFFFOOFF
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9BA80000	FOOFFOFFFOFOF
9BB00000	FOOFFOFFFOFFO
9BB80000	FOOFFOFFFOFFF
9BC00000	FOOFFOFFFFOOO
9BC80000	FOOFFOFFFFOOF
9BD00000	FOOFFOFFFFOFO
9BD80000	FOOFFOFFFFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
9BE00000	FOOFFOFFFFFOO
9BE80000	FOOFFOFFFFFOF
9BF00000	FOOFFOFFFFFFO
9BF80000	FOOFFOFFFFFFF
9C000000	FOOFFFOOOOOOO
9C080000	FOOFFFOOOOOOF
9C100000	FOOFFFOOOOOFO
9C180000	FOOFFFOOOOOFF
9C200000	FOOFFFOOOOFOO
9C280000	FOOFFFOOOOFOF
9C300000	FOOFFFOOOOFFO
9C380000	FOOFFFOOOOFFF
9C400000	FOOFFFOOOFOOO
9C480000	FOOFFFOOOFOOF
9C500000	FOOFFFOOOFOFO
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9C600000	FOOFFFOOOFFOO
9C680000	FOOFFFOOOFFOF
9C700000	FOOFFFOOOFFFO
9C780000	FOOFFFOOOFFFF
9C800000	FOOFFFOOFOOOO
9C880000	FOOFFFOOFOOOF
9C900000	FOOFFFOOFOOFO
9C980000	FOOFFFOOFOOFF
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9CD00000	FOOFFFOOFFOFO
9CD80000	FOOFFFOOFFOFF
9CE00000	FOOFFFOOFFFOO
9CE80000	FOOFFFOOFFFOF
9CF00000	FOOFFFOOFFFFO
9CF80000	FOOFFFOOFFFFF
9D000000	FOOFFFOFOOOOO
9D080000	FOOFFFOFOOOOF
9D100000	FOOFFFOFOOOFO
9D180000	FOOFFFOFOOOFF
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9D980000	FOOFFFOFFOOFF
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9DA80000	FOOFFFOFFOFOF
9DB00000	FOOFFFOFFOFFO
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9DD80000	FOOFFFOFFFOFF
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9E100000	FOOFFFFOOOOFO
9E180000	FOOFFFFOOOOFF
9E200000	FOOFFFFOOOFOO
9E280000	FOOFFFFOOOFOF
9E300000	FOOFFFFOOOFFO
9E380000	FOOFFFFOOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
9E400000	FOOFFFFOOFOOO
9E480000	FOOFFFFOOFOOF
9E500000	FOOFFFFOOFOFO
9E580000	FOOFFFFOOFOFF
9E600000	FOOFFFFOOFFOO
9E680000	FOOFFFFOOFFOF
9E700000	FOOFFFFOOFFFO
9E780000	FOOFFFFOOFFFF
9E800000	FOOFFFFOFOOOO
9E880000	FOOFFFFOFOOOF
9E900000	FOOFFFFOFOOFO
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9EA00000	FOOFFFFOFOFOO
9EA80000	FOOFFFFOFOFOF
9EB00000	FOOFFFFOFOFFO
9EB80000	FOOFFFFOFOFFF
9EC00000	FOOFFFFOFFOOO
9EC80000	FOOFFFFOFFOOF
9ED00000	FOOFFFFOFFOFO
9ED80000	FOOFFFFOFFOFF
9EE00000	FOOFFFFOFFFOO
9EE80000	FOOFFFFOFFFOF
9EF00000	FOOFFFFOFFFFO
9EF80000	FOOFFFFOFFFFF
9F000000	FOOFFFFFOOOOO
9F080000	FOOFFFFFOOOOF
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9F180000	FOOFFFFFOOOFF
9F200000	FOOFFFFFOOFOO
9F280000	FOOFFFFFOOFOF
9F300000	FOOFFFFFOOFFO
9F380000	FOOFFFFFOOFFF
9F400000	FOOFFFFFOFOOO
9F480000	FOOFFFFFOFOOF
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9F580000	FOOFFFFFOFOFF
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9FC80000	FOOFFFFFFFOOF
9FD00000	FOOFFFFFFFOFO
9FD80000	FOOFFFFFFFOFF
9FE00000	FOOFFFFFFFFOO
9FE80000	FOOFFFFFFFFOF
9FF00000	FOOFFFFFFFFFO
9FF80000	FOOFFFFFFFFFF
A0000000	FOFOOOOOOOOOO
A0080000	FOFOOOOOOOOOF
A0100000	FOFOOOOOOOOFO
A0180000	FOFOOOOOOOOFF
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A0300000	FOFOOOOOOOFFO
A0380000	FOFOOOOOOOFFF
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A0480000	FOFOOOOOOFOOF
A0500000	FOFOOOOOOFOFO
A0580000	FOFOOOOOOFOFF
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A0680000	FOFOOOOOOFFOF
A0700000	FOFOOOOOOFFFO
A0780000	FOFOOOOOOFFFF
A0800000	FOFOOOOOFOOOO
A0880000	FOFOOOOOFOOOF
A0900000	FOFOOOOOFOOFO
A0980000	FOFOOOOOFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
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A0A80000	FOFOOOOOFOFOF
A0B00000	FOFOOOOOFOFFO
A0B80000	FOFOOOOOFOFFF
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A1B00000	FOFOOOOFFOFFO
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A1D00000	FOFOOOOFFFOFO
A1D80000	FOFOOOOFFFOFF
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A2B00000	FOFOOOFOFOFFO
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A2E00000	FOFOOOFOFFFOO
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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
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A3180000	FOFOOOFFOOOFF
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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
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A7B00000	FOFOOFFFFOFFO
A7B80000	FOFOOFFFFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
A7C00000	FOFOOFFFFFOOO
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B0180000	FOFFOOOOOOOFF
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B1080000	FOFFOOOFOOOOF
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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
B1400000	FOFFOOOFOFOOO
B1480000	FOFFOOOFOFOOF
B1500000	FOFFOOOFOFOFO
B1580000	FOFFOOOFOFOFF
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B1800000	FOFFOOOFFOOOO
B1880000	FOFFOOOFFOOOF
B1900000	FOFFOOOFFOOFO
B1980000	FOFFOOOFFOOFF
B1A00000	FOFFOOOFFOFOO
B1A80000	FOFFOOOFFOFOF
B1B00000	FOFFOOOFFOFFO
B1B80000	FOFFOOOFFOFFF
B1C00000	FOFFOOOFFFOOO
B1C80000	FOFFOOOFFFOOF
B1D00000	FOFFOOOFFFOFO
B1D80000	FOFFOOOFFFOFF
B1E00000	FOFFOOOFFFFOO
B1E80000	FOFFOOOFFFFOF
B1F00000	FOFFOOOFFFFFO
B1F80000	FOFFOOOFFFFFF
B2000000	FOFFOOFOOOOOO
B2080000	FOFFOOFOOOOOF
B2100000	FOFFOOFOOOOFO
B2180000	FOFFOOFOOOOFF
B2200000	FOFFOOFOOOFOO
B2280000	FOFFOOFOOOFOF
B2300000	FOFFOOFOOOFFO
B2380000	FOFFOOFOOOFFF
B2400000	FOFFOOFOOFOOO
B2480000	FOFFOOFOOFOOF
B2500000	FOFFOOFOOFOFO
B2580000	FOFFOOFOOFOFF
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B2680000	FOFFOOFOOFFOF
B2700000	FOFFOOFOOFFFO
B2780000	FOFFOOFOOFFFF
B2800000	FOFFOOFOFOOOO
B2880000	FOFFOOFOFOOOF
B2900000	FOFFOOFOFOOFO
B2980000	FOFFOOFOFOOFF
B2A00000	FOFFOOFOFOFOO
B2A80000	FOFFOOFOFOFOF
B2B00000	FOFFOOFOFOFFO
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B2C00000	FOFFOOFOFFOOO
B2C80000	FOFFOOFOFFOOF
B2D00000	FOFFOOFOFFOFO
B2D80000	FOFFOOFOFFOFF
B2E00000	FOFFOOFOFFFOO
B2E80000	FOFFOOFOFFFOF
B2F00000	FOFFOOFOFFFFO
B2F80000	FOFFOOFOFFFFF
B3000000	FOFFOOFFOOOOO
B3080000	FOFFOOFFOOOOF
B3100000	FOFFOOFFOOOFO
B3180000	FOFFOOFFOOOFF
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B3280000	FOFFOOFFOOFOF
B3300000	FOFFOOFFOOFFO
B3380000	FOFFOOFFOOFFF
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B3480000	FOFFOOFFOFOOF
B3500000	FOFFOOFFOFOFO
B3580000	FOFFOOFFOFOFF
B3600000	FOFFOOFFOFFOO
B3680000	FOFFOOFFOFFOF
B3700000	FOFFOOFFOFFFO
B3780000	FOFFOOFFOFFFF
B3800000	FOFFOOFFFOOOO
B3880000	FOFFOOFFFOOOF
B3900000	FOFFOOFFFOOFO
B3980000	FOFFOOFFFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
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B3A80000	FOFFOOFFFOFOF
B3B00000	FOFFOOFFFOFFO
B3B80000	FOFFOOFFFOFFF
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B3F00000	FOFFOOFFFFFFO
B3F80000	FOFFOOFFFFFFF
B4000000	FOFFOFOOOOOOO
B4080000	FOFFOFOOOOOOF
B4100000	FOFFOFOOOOOFO
B4180000	FOFFOFOOOOOFF
B4200000	FOFFOFOOOOFOO
B4280000	FOFFOFOOOOFOF
B4300000	FOFFOFOOOOFFO
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B4480000	FOFFOFOOOFOOF
B4500000	FOFFOFOOOFOFO
B4580000	FOFFOFOOOFOFF
B4600000	FOFFOFOOOFFOO
B4680000	FOFFOFOOOFFOF
B4700000	FOFFOFOOOFFFO
B4780000	FOFFOFOOOFFFF
B4800000	FOFFOFOOFOOOO
B4880000	FOFFOFOOFOOOF
B4900000	FOFFOFOOFOOFO
B4980000	FOFFOFOOFOOFF
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B4C00000	FOFFOFOOFFOOO
B4C80000	FOFFOFOOFFOOF
B4D00000	FOFFOFOOFFOFO
B4D80000	FOFFOFOOFFOFF
B4E00000	FOFFOFOOFFFOO
B4E80000	FOFFOFOOFFFOF
B4F00000	FOFFOFOOFFFFO
B4F80000	FOFFOFOOFFFFF
B5000000	FOFFOFOFOOOOO
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B5300000	FOFFOFOFOOFFO
B5380000	FOFFOFOFOOFFF
B5400000	FOFFOFOFOFOOO
B5480000	FOFFOFOFOFOOF
B5500000	FOFFOFOFOFOFO
B5580000	FOFFOFOFOFOFF
B5600000	FOFFOFOFOFFOO
B5680000	FOFFOFOFOFFOF
B5700000	FOFFOFOFOFFFO
B5780000	FOFFOFOFOFFFF
B5800000	FOFFOFOFFOOOO
B5880000	FOFFOFOFFOOOF
B5900000	FOFFOFOFFOOFO
B5980000	FOFFOFOFFOOFF
B5A00000	FOFFOFOFFOFOO
B5A80000	FOFFOFOFFOFOF
B5B00000	FOFFOFOFFOFFO
B5B80000	FOFFOFOFFOFFF
B5C00000	FOFFOFOFFFOOO
B5C80000	FOFFOFOFFFOOF
B5D00000	FOFFOFOFFFOFO
B5D80000	FOFFOFOFFFOFF
B5E00000	FOFFOFOFFFFOO
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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
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B6080000	FOFFOFFOOOOOF
B6100000	FOFFOFFOOOOFO
B6180000	FOFFOFFOOOOFF
B6200000	FOFFOFFOOOFOO
B6280000	FOFFOFFOOOFOF
B6300000	FOFFOFFOOOFFO
B6380000	FOFFOFFOOOFFF
B6400000	FOFFOFFOOFOOO
B6480000	FOFFOFFOOFOOF
B6500000	FOFFOFFOOFOFO
B6580000	FOFFOFFOOFOFF
B6600000	FOFFOFFOOFFOO
B6680000	FOFFOFFOOFFOF
B6700000	FOFFOFFOOFFFO
B6780000	FOFFOFFOOFFFF
B6800000	FOFFOFFOFOOOO
B6880000	FOFFOFFOFOOOF
B6900000	FOFFOFFOFOOFO
B6980000	FOFFOFFOFOOFF
B6A00000	FOFFOFFOFOFOO
B6A80000	FOFFOFFOFOFOF
B6B00000	FOFFOFFOFOFFO
B6B80000	FOFFOFFOFOFFF
B6C00000	FOFFOFFOFFOOO
B6C80000	FOFFOFFOFFOOF
B6D00000	FOFFOFFOFFOFO
B6D80000	FOFFOFFOFFOFF
B6E00000	FOFFOFFOFFFOO
B6E80000	FOFFOFFOFFFOF
B6F00000	FOFFOFFOFFFFO
B6F80000	FOFFOFFOFFFFF
B7000000	FOFFOFFFOOOOO
B7080000	FOFFOFFFOOOOF
B7100000	FOFFOFFFOOOFO
B7180000	FOFFOFFFOOOFF
B7200000	FOFFOFFFOOFOO
B7280000	FOFFOFFFOOFOF
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B7380000	FOFFOFFFOOFFF
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B7480000	FOFFOFFFOFOOF
B7500000	FOFFOFFFOFOFO
B7580000	FOFFOFFFOFOFF
B7600000	FOFFOFFFOFFOO
B7680000	FOFFOFFFOFFOF
B7700000	FOFFOFFFOFFFO
B7780000	FOFFOFFFOFFFF
B7800000	FOFFOFFFFOOOO
B7880000	FOFFOFFFFOOOF
B7900000	FOFFOFFFFOOFO
B7980000	FOFFOFFFFOOFF
B7A00000	FOFFOFFFFOFOO
B7A80000	FOFFOFFFFOFOF
B7B00000	FOFFOFFFFOFFO
B7B80000	FOFFOFFFFOFFF
B7C00000	FOFFOFFFFFOOO
B7C80000	FOFFOFFFFFOOF
B7D00000	FOFFOFFFFFOFO
B7D80000	FOFFOFFFFFOFF
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B7E80000	FOFFOFFFFFFOF
B7F00000	FOFFOFFFFFFFO
B7F80000	FOFFOFFFFFFFF
B8000000	FOFFFOOOOOOOO
B8080000	FOFFFOOOOOOOF
B8100000	FOFFFOOOOOOFO
B8180000	FOFFFOOOOOOFF
B8200000	FOFFFOOOOOFOO
B8280000	FOFFFOOOOOFOF
B8300000	FOFFFOOOOOFFO
B8380000	FOFFFOOOOOFFF
B8400000	FOFFFOOOOFOOO
B8480000	FOFFFOOOOFOOF
B8500000	FOFFFOOOOFOFO
B8580000	FOFFFOOOOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
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B8680000	FOFFFOOOOFFOF
B8700000	FOFFFOOOOFFFO
B8780000	FOFFFOOOOFFFF
B8800000	FOFFFOOOFOOOO
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B8900000	FOFFFOOOFOOFO
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B8A80000	FOFFFOOOFOFOF
B8B00000	FOFFFOOOFOFFO
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B8C00000	FOFFFOOOFFOOO
B8C80000	FOFFFOOOFFOOF
B8D00000	FOFFFOOOFFOFO
B8D80000	FOFFFOOOFFOFF
B8E00000	FOFFFOOOFFFOO
B8E80000	FOFFFOOOFFFOF
B8F00000	FOFFFOOOFFFFO
B8F80000	FOFFFOOOFFFFF
B9000000	FOFFFOOFOOOOO
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B9100000	FOFFFOOFOOOFO
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B9480000	FOFFFOOFOFOOF
B9500000	FOFFFOOFOFOFO
B9580000	FOFFFOOFOFOFF
B9600000	FOFFFOOFOFFOO
B9680000	FOFFFOOFOFFOF
B9700000	FOFFFOOFOFFFO
B9780000	FOFFFOOFOFFFF
B9800000	FOFFFOOFFOOOO
B9880000	FOFFFOOFFOOOF
B9900000	FOFFFOOFFOOFO
B9980000	FOFFFOOFFOOFF
B9A00000	FOFFFOOFFOFOO
B9A80000	FOFFFOOFFOFOF
B9B00000	FOFFFOOFFOFFO
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BA100000	FOFFFOFOOOOFO
BA180000	FOFFFOFOOOOFF
BA200000	FOFFFOFOOOFOO
BA280000	FOFFFOFOOOFOF
BA300000	FOFFFOFOOOFFO
BA380000	FOFFFOFOOOFFF
BA400000	FOFFFOFOOFOOO
BA480000	FOFFFOFOOFOOF
BA500000	FOFFFOFOOFOFO
BA580000	FOFFFOFOOFOFF
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BA780000	FOFFFOFOOFFFF
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BA900000	FOFFFOFOFOOFO
BA980000	FOFFFOFOFOOFF
BAA00000	FOFFFOFOFOFOO
BAA80000	FOFFFOFOFOFOF
BAB00000	FOFFFOFOFOFFO
BAB80000	FOFFFOFOFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
BAC00000	FOFFFOFOFFOOO
BAC80000	FOFFFOFOFFOOF
BAD00000	FOFFFOFOFFOFO
BAD80000	FOFFFOFOFFOFF
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BB480000	FOFFFOFFOFOOF
BB500000	FOFFFOFFOFOFO
BB580000	FOFFFOFFOFOFF
BB600000	FOFFFOFFOFFOO
BB680000	FOFFFOFFOFFOF
BB700000	FOFFFOFFOFFFO
BB780000	FOFFFOFFOFFFF
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BCD80000	FOFFFFOOFFOFF
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BCE80000	FOFFFFOOFFFOF
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BCF80000	FOFFFFOOFFFFF
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BEB80000	FOFFFFFOFOFFF
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BEC80000	FOFFFFFOFFOOF
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BED80000	FOFFFFFOFFOFF
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BFE80000	FOFFFFFFFFFOF
BFF00000	FOFFFFFFFFFFO
BFF80000	FOFFFFFFFFFFF
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C0A80000	FFOOOOOOFOFOF
C0B00000	FFOOOOOOFOFFO
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C2A00000	FFOOOOFOFOFOO
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C2B00000	FFOOOOFOFOFFO
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C2C00000	FFOOOOFOFFOOO
C2C80000	FFOOOOFOFFOOF
C2D00000	FFOOOOFOFFOFO
C2D80000	FFOOOOFOFFOFF
C2E00000	FFOOOOFOFFFOO
C2E80000	FFOOOOFOFFFOF
C2F00000	FFOOOOFOFFFFO
C2F80000	FFOOOOFOFFFFF
C3000000	FFOOOOFFOOOOO
C3080000	FFOOOOFFOOOOF
C3100000	FFOOOOFFOOOFO
C3180000	FFOOOOFFOOOFF
C3200000	FFOOOOFFOOFOO
C3280000	FFOOOOFFOOFOF
C3300000	FFOOOOFFOOFFO
C3380000	FFOOOOFFOOFFF
C3400000	FFOOOOFFOFOOO
C3480000	FFOOOOFFOFOOF
C3500000	FFOOOOFFOFOFO
C3580000	FFOOOOFFOFOFF
C3600000	FFOOOOFFOFFOO
C3680000	FFOOOOFFOFFOF
C3700000	FFOOOOFFOFFFO
C3780000	FFOOOOFFOFFFF
C3800000	FFOOOOFFFOOOO
C3880000	FFOOOOFFFOOOF
C3900000	FFOOOOFFFOOFO
C3980000	FFOOOOFFFOOFF
C3A00000	FFOOOOFFFOFOO
C3A80000	FFOOOOFFFOFOF
C3B00000	FFOOOOFFFOFFO
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C4180000	FFOOOFOOOOOFF
C4200000	FFOOOFOOOOFOO
C4280000	FFOOOFOOOOFOF
C4300000	FFOOOFOOOOFFO
C4380000	FFOOOFOOOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
C4400000	FFOOOFOOOFOOO
C4480000	FFOOOFOOOFOOF
C4500000	FFOOOFOOOFOFO
C4580000	FFOOOFOOOFOFF
C4600000	FFOOOFOOOFFOO
C4680000	FFOOOFOOOFFOF
C4700000	FFOOOFOOOFFFO
C4780000	FFOOOFOOOFFFF
C4800000	FFOOOFOOFOOOO
C4880000	FFOOOFOOFOOOF
C4900000	FFOOOFOOFOOFO
C4980000	FFOOOFOOFOOFF
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C4B00000	FFOOOFOOFOFFO
C4B80000	FFOOOFOOFOFFF
C4C00000	FFOOOFOOFFOOO
C4C80000	FFOOOFOOFFOOF
C4D00000	FFOOOFOOFFOFO
C4D80000	FFOOOFOOFFOFF
C4E00000	FFOOOFOOFFFOO
C4E80000	FFOOOFOOFFFOF
C4F00000	FFOOOFOOFFFFO
C4F80000	FFOOOFOOFFFFF
C5000000	FFOOOFOFOOOOO
C5080000	FFOOOFOFOOOOF
C5100000	FFOOOFOFOOOFO
C5180000	FFOOOFOFOOOFF
C5200000	FFOOOFOFOOFOO
C5280000	FFOOOFOFOOFOF
C5300000	FFOOOFOFOOFFO
C5380000	FFOOOFOFOOFFF
C5400000	FFOOOFOFOFOOO
C5480000	FFOOOFOFOFOOF
C5500000	FFOOOFOFOFOFO
C5580000	FFOOOFOFOFOFF
C5600000	FFOOOFOFOFFOO
C5680000	FFOOOFOFOFFOF
C5700000	FFOOOFOFOFFFO
C5780000	FFOOOFOFOFFFF
C5800000	FFOOOFOFFOOOO
C5880000	FFOOOFOFFOOOF
C5900000	FFOOOFOFFOOFO
C5980000	FFOOOFOFFOOFF
C5A00000	FFOOOFOFFOFOO
C5A80000	FFOOOFOFFOFOF
C5B00000	FFOOOFOFFOFFO
C5B80000	FFOOOFOFFOFFF
C5C00000	FFOOOFOFFFOOO
C5C80000	FFOOOFOFFFOOF
C5D00000	FFOOOFOFFFOFO
C5D80000	FFOOOFOFFFOFF
C5E00000	FFOOOFOFFFFOO
C5E80000	FFOOOFOFFFFOF
C5F00000	FFOOOFOFFFFFO
C5F80000	FFOOOFOFFFFFF
C6000000	FFOOOFFOOOOOO
C6080000	FFOOOFFOOOOOF
C6100000	FFOOOFFOOOOFO
C6180000	FFOOOFFOOOOFF
C6200000	FFOOOFFOOOFOO
C6280000	FFOOOFFOOOFOF
C6300000	FFOOOFFOOOFFO
C6380000	FFOOOFFOOOFFF
C6400000	FFOOOFFOOFOOO
C6480000	FFOOOFFOOFOOF
C6500000	FFOOOFFOOFOFO
C6580000	FFOOOFFOOFOFF
C6600000	FFOOOFFOOFFOO
C6680000	FFOOOFFOOFFOF
C6700000	FFOOOFFOOFFFO
C6780000	FFOOOFFOOFFFF
C6800000	FFOOOFFOFOOOO
C6880000	FFOOOFFOFOOOF
C6900000	FFOOOFFOFOOFO
C6980000	FFOOOFFOFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
C6A00000	FFOOOFFOFOFOO
C6A80000	FFOOOFFOFOFOF
C6B00000	FFOOOFFOFOFFO
C6B80000	FFOOOFFOFOFFF
C6C00000	FFOOOFFOFFOOO
C6C80000	FFOOOFFOFFOOF
C6D00000	FFOOOFFOFFOFO
C6D80000	FFOOOFFOFFOFF
C6E00000	FFOOOFFOFFFOO
C6E80000	FFOOOFFOFFFOF
C6F00000	FFOOOFFOFFFFO
C6F80000	FFOOOFFOFFFFF
C7000000	FFOOOFFFOOOOO
C7080000	FFOOOFFFOOOOF
C7100000	FFOOOFFFOOOFO
C7180000	FFOOOFFFOOOFF
C7200000	FFOOOFFFOOFOO
C7280000	FFOOOFFFOOFOF
C7300000	FFOOOFFFOOFFO
C7380000	FFOOOFFFOOFFF
C7400000	FFOOOFFFOFOOO
C7480000	FFOOOFFFOFOOF
C7500000	FFOOOFFFOFOFO
C7580000	FFOOOFFFOFOFF
C7600000	FFOOOFFFOFFOO
C7680000	FFOOOFFFOFFOF
C7700000	FFOOOFFFOFFFO
C7780000	FFOOOFFFOFFFF
C7800000	FFOOOFFFFOOOO
C7880000	FFOOOFFFFOOOF
C7900000	FFOOOFFFFOOFO
C7980000	FFOOOFFFFOOFF
C7A00000	FFOOOFFFFOFOO
C7A80000	FFOOOFFFFOFOF
C7B00000	FFOOOFFFFOFFO
C7B80000	FFOOOFFFFOFFF
C7C00000	FFOOOFFFFFOOO
C7C80000	FFOOOFFFFFOOF
C7D00000	FFOOOFFFFFOFO
C7D80000	FFOOOFFFFFOFF
C7E00000	FFOOOFFFFFFOO
C7E80000	FFOOOFFFFFFOF
C7F00000	FFOOOFFFFFFFO
C7F80000	FFOOOFFFFFFFF
C8000000	FFOOFOOOOOOOO
C8080000	FFOOFOOOOOOOF
C8100000	FFOOFOOOOOOFO
C8180000	FFOOFOOOOOOFF
C8200000	FFOOFOOOOOFOO
C8280000	FFOOFOOOOOFOF
C8300000	FFOOFOOOOOFFO
C8380000	FFOOFOOOOOFFF
C8400000	FFOOFOOOOFOOO
C8480000	FFOOFOOOOFOOF
C8500000	FFOOFOOOOFOFO
C8580000	FFOOFOOOOFOFF
C8600000	FFOOFOOOOFFOO
C8680000	FFOOFOOOOFFOF
C8700000	FFOOFOOOOFFFO
C8780000	FFOOFOOOOFFFF
C8800000	FFOOFOOOFOOOO
C8880000	FFOOFOOOFOOOF
C8900000	FFOOFOOOFOOFO
C8980000	FFOOFOOOFOOFF
C8A00000	FFOOFOOOFOFOO
C8A80000	FFOOFOOOFOFOF
C8B00000	FFOOFOOOFOFFO
C8B80000	FFOOFOOOFOFFF
C8C00000	FFOOFOOOFFOOO
C8C80000	FFOOFOOOFFOOF
C8D00000	FFOOFOOOFFOFO
C8D80000	FFOOFOOOFFOFF
C8E00000	FFOOFOOOFFFOO
C8E80000	FFOOFOOOFFFOF
C8F00000	FFOOFOOOFFFFO
C8F80000	FFOOFOOOFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
C9000000	FFOOFOOFOOOOO
C9080000	FFOOFOOFOOOOF
C9100000	FFOOFOOFOOOFO
C9180000	FFOOFOOFOOOFF
C9200000	FFOOFOOFOOFOO
C9280000	FFOOFOOFOOFOF
C9300000	FFOOFOOFOOFFO
C9380000	FFOOFOOFOOFFF
C9400000	FFOOFOOFOFOOO
C9480000	FFOOFOOFOFOOF
C9500000	FFOOFOOFOFOFO
C9580000	FFOOFOOFOFOFF
C9600000	FFOOFOOFOFFOO
C9680000	FFOOFOOFOFFOF
C9700000	FFOOFOOFOFFFO
C9780000	FFOOFOOFOFFFF
C9800000	FFOOFOOFFOOOO
C9880000	FFOOFOOFFOOOF
C9900000	FFOOFOOFFOOFO
C9980000	FFOOFOOFFOOFF
C9A00000	FFOOFOOFFOFOO
C9A80000	FFOOFOOFFOFOF
C9B00000	FFOOFOOFFOFFO
C9B80000	FFOOFOOFFOFFF
C9C00000	FFOOFOOFFFOOO
C9C80000	FFOOFOOFFFOOF
C9D00000	FFOOFOOFFFOFO
C9D80000	FFOOFOOFFFOFF
C9E00000	FFOOFOOFFFFOO
C9E80000	FFOOFOOFFFFOF
C9F00000	FFOOFOOFFFFFO
C9F80000	FFOOFOOFFFFFF
CA000000	FFOOFOFOOOOOO
CA080000	FFOOFOFOOOOOF
CA100000	FFOOFOFOOOOFO
CA180000	FFOOFOFOOOOFF
CA200000	FFOOFOFOOOFOO
CA280000	FFOOFOFOOOFOF
CA300000	FFOOFOFOOOFFO
CA380000	FFOOFOFOOOFFF
CA400000	FFOOFOFOOFOOO
CA480000	FFOOFOFOOFOOF
CA500000	FFOOFOFOOFOFO
CA580000	FFOOFOFOOFOFF
CA600000	FFOOFOFOOFFOO
CA680000	FFOOFOFOOFFOF
CA700000	FFOOFOFOOFFFO
CA780000	FFOOFOFOOFFFF
CA800000	FFOOFOFOFOOOO
CA880000	FFOOFOFOFOOOF
CA900000	FFOOFOFOFOOFO
CA980000	FFOOFOFOFOOFF
CAA00000	FFOOFOFOFOFOO
CAA80000	FFOOFOFOFOFOF
CAB00000	FFOOFOFOFOFFO
CAB80000	FFOOFOFOFOFFF
CAC00000	FFOOFOFOFFOOO
CAC80000	FFOOFOFOFFOOF
CAD00000	FFOOFOFOFFOFO
CAD80000	FFOOFOFOFFOFF
CAE00000	FFOOFOFOFFFOO
CAE80000	FFOOFOFOFFFOF
CAF00000	FFOOFOFOFFFFO
CAF80000	FFOOFOFOFFFFF
CB000000	FFOOFOFFOOOOO
CB080000	FFOOFOFFOOOOF
CB100000	FFOOFOFFOOOFO
CB180000	FFOOFOFFOOOFF
CB200000	FFOOFOFFOOFOO
CB280000	FFOOFOFFOOFOF
CB300000	FFOOFOFFOOFFO
CB380000	FFOOFOFFOOFFF
CB400000	FFOOFOFFOFOOO
CB480000	FFOOFOFFOFOOF
CB500000	FFOOFOFFOFOFO
CB580000	FFOOFOFFOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
CB600000	FFOOFOFFOFFOO
CB680000	FFOOFOFFOFFOF
CB700000	FFOOFOFFOFFFO
CB780000	FFOOFOFFOFFFF
CB800000	FFOOFOFFFOOOO
CB880000	FFOOFOFFFOOOF
CB900000	FFOOFOFFFOOFO
CB980000	FFOOFOFFFOOFF
CBA00000	FFOOFOFFFOFOO
CBA80000	FFOOFOFFFOFOF
CBB00000	FFOOFOFFFOFFO
CBB80000	FFOOFOFFFOFFF
CBC00000	FFOOFOFFFFOOO
CBC80000	FFOOFOFFFFOOF
CBD00000	FFOOFOFFFFOFO
CBD80000	FFOOFOFFFFOFF
CBE00000	FFOOFOFFFFFOO
CBE80000	FFOOFOFFFFFOF
CBF00000	FFOOFOFFFFFFO
CBF80000	FFOOFOFFFFFFF
CC000000	FFOOFFOOOOOOO
CC080000	FFOOFFOOOOOOF
CC100000	FFOOFFOOOOOFO
CC180000	FFOOFFOOOOOFF
CC200000	FFOOFFOOOOFOO
CC280000	FFOOFFOOOOFOF
CC300000	FFOOFFOOOOFFO
CC380000	FFOOFFOOOOFFF
CC400000	FFOOFFOOOFOOO
CC480000	FFOOFFOOOFOOF
CC500000	FFOOFFOOOFOFO
CC580000	FFOOFFOOOFOFF
CC600000	FFOOFFOOOFFOO
CC680000	FFOOFFOOOFFOF
CC700000	FFOOFFOOOFFFO
CC780000	FFOOFFOOOFFFF
CC800000	FFOOFFOOFOOOO
CC880000	FFOOFFOOFOOOF
CC900000	FFOOFFOOFOOFO
CC980000	FFOOFFOOFOOFF
CCA00000	FFOOFFOOFOFOO
CCA80000	FFOOFFOOFOFOF
CCB00000	FFOOFFOOFOFFO
CCB80000	FFOOFFOOFOFFF
CCC00000	FFOOFFOOFFOOO
CCC80000	FFOOFFOOFFOOF
CCD00000	FFOOFFOOFFOFO
CCD80000	FFOOFFOOFFOFF
CCE00000	FFOOFFOOFFFOO
CCE80000	FFOOFFOOFFFOF
CCF00000	FFOOFFOOFFFFO
CCF80000	FFOOFFOOFFFFF
CD000000	FFOOFFOFOOOOO
CD080000	FFOOFFOFOOOOF
CD100000	FFOOFFOFOOOFO
CD180000	FFOOFFOFOOOFF
CD200000	FFOOFFOFOOFOO
CD280000	FFOOFFOFOOFOF
CD300000	FFOOFFOFOOFFO
CD380000	FFOOFFOFOOFFF
CD400000	FFOOFFOFOFOOO
CD480000	FFOOFFOFOFOOF
CD500000	FFOOFFOFOFOFO
CD580000	FFOOFFOFOFOFF
CD600000	FFOOFFOFOFFOO
CD680000	FFOOFFOFOFFOF
CD700000	FFOOFFOFOFFFO
CD780000	FFOOFFOFOFFFF
CD800000	FFOOFFOFFOOOO
CD880000	FFOOFFOFFOOOF
CD900000	FFOOFFOFFOOFO
CD980000	FFOOFFOFFOOFF
CDA00000	FFOOFFOFFOFOO
CDA80000	FFOOFFOFFOFOF
CDB00000	FFOOFFOFFOFFO
CDB80000	FFOOFFOFFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
CDC00000	FFOOFFOFFFOOO
CDC80000	FFOOFFOFFFOOF
CDD00000	FFOOFFOFFFOFO
CDD80000	FFOOFFOFFFOFF
CDE00000	FFOOFFOFFFFOO
CDE80000	FFOOFFOFFFFOF
CDF00000	FFOOFFOFFFFFO
CDF80000	FFOOFFOFFFFFF
CE000000	FFOOFFFOOOOOO
CE080000	FFOOFFFOOOOOF
CE100000	FFOOFFFOOOOFO
CE180000	FFOOFFFOOOOFF
CE200000	FFOOFFFOOOFOO
CE280000	FFOOFFFOOOFOF
CE300000	FFOOFFFOOOFFO
CE380000	FFOOFFFOOOFFF
CE400000	FFOOFFFOOFOOO
CE480000	FFOOFFFOOFOOF
CE500000	FFOOFFFOOFOFO
CE580000	FFOOFFFOOFOFF
CE600000	FFOOFFFOOFFOO
CE680000	FFOOFFFOOFFOF
CE700000	FFOOFFFOOFFFO
CE780000	FFOOFFFOOFFFF
CE800000	FFOOFFFOFOOOO
CE880000	FFOOFFFOFOOOF
CE900000	FFOOFFFOFOOFO
CE980000	FFOOFFFOFOOFF
CEA00000	FFOOFFFOFOFOO
CEA80000	FFOOFFFOFOFOF
CEB00000	FFOOFFFOFOFFO
CEB80000	FFOOFFFOFOFFF
CEC00000	FFOOFFFOFFOOO
CEC80000	FFOOFFFOFFOOF
CED00000	FFOOFFFOFFOFO
CED80000	FFOOFFFOFFOFF
CEE00000	FFOOFFFOFFFOO
CEE80000	FFOOFFFOFFFOF
CEF00000	FFOOFFFOFFFFO
CEF80000	FFOOFFFOFFFFF
CF000000	FFOOFFFFOOOOO
CF080000	FFOOFFFFOOOOF
CF100000	FFOOFFFFOOOFO
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CF200000	FFOOFFFFOOFOO
CF280000	FFOOFFFFOOFOF
CF300000	FFOOFFFFOOFFO
CF380000	FFOOFFFFOOFFF
CF400000	FFOOFFFFOFOOO
CF480000	FFOOFFFFOFOOF
CF500000	FFOOFFFFOFOFO
CF580000	FFOOFFFFOFOFF
CF600000	FFOOFFFFOFFOO
CF680000	FFOOFFFFOFFOF
CF700000	FFOOFFFFOFFFO
CF780000	FFOOFFFFOFFFF
CF800000	FFOOFFFFFOOOO
CF880000	FFOOFFFFFOOOF
CF900000	FFOOFFFFFOOFO
CF980000	FFOOFFFFFOOFF
CFA00000	FFOOFFFFFOFOO
CFA80000	FFOOFFFFFOFOF
CFB00000	FFOOFFFFFOFFO
CFB80000	FFOOFFFFFOFFF
CFC00000	FFOOFFFFFFOOO
CFC80000	FFOOFFFFFFOOF
CFD00000	FFOOFFFFFFOFO
CFD80000	FFOOFFFFFFOFF
CFE00000	FFOOFFFFFFFOO
CFE80000	FFOOFFFFFFFOF
CFF00000	FFOOFFFFFFFFO
CFF80000	FFOOFFFFFFFFF
D0000000	FFOFOOOOOOOOO
D0080000	FFOFOOOOOOOOF
D0100000	FFOFOOOOOOOFO
D0180000	FFOFOOOOOOOFF
D0200000	FFOFOOOOOOFOO
D0280000	FFOFOOOOOOFOF
D0300000	FFOFOOOOOOFFO
D0380000	FFOFOOOOOOFFF
D0400000	FFOFOOOOOFOOO
D0480000	FFOFOOOOOFOOF
D0500000	FFOFOOOOOFOFO
D0580000	FFOFOOOOOFOFF
D0600000	FFOFOOOOOFFOO
D0680000	FFOFOOOOOFFOF
D0700000	FFOFOOOOOFFFO
D0780000	FFOFOOOOOFFFF
D0800000	FFOFOOOOFOOOO
D0880000	FFOFOOOOFOOOF
D0900000	FFOFOOOOFOOFO
D0980000	FFOFOOOOFOOFF
D0A00000	FFOFOOOOFOFOO
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D0B00000	FFOFOOOOFOFFO
D0B80000	FFOFOOOOFOFFF
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D0F80000	FFOFOOOOFFFFF
D1000000	FFOFOOOFOOOOO
D1080000	FFOFOOOFOOOOF
D1100000	FFOFOOOFOOOFO
D1180000	FFOFOOOFOOOFF
D1200000	FFOFOOOFOOFOO
D1280000	FFOFOOOFOOFOF
D1300000	FFOFOOOFOOFFO
D1380000	FFOFOOOFOOFFF
D1400000	FFOFOOOFOFOOO
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D1680000	FFOFOOOFOFFOF
D1700000	FFOFOOOFOFFFO
D1780000	FFOFOOOFOFFFF
D1800000	FFOFOOOFFOOOO
D1880000	FFOFOOOFFOOOF
D1900000	FFOFOOOFFOOFO
D1980000	FFOFOOOFFOOFF
D1A00000	FFOFOOOFFOFOO
D1A80000	FFOFOOOFFOFOF
D1B00000	FFOFOOOFFOFFO
D1B80000	FFOFOOOFFOFFF
D1C00000	FFOFOOOFFFOOO
D1C80000	FFOFOOOFFFOOF
D1D00000	FFOFOOOFFFOFO
D1D80000	FFOFOOOFFFOFF
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D2400000	FFOFOOFOOFOOO
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D2580000	FFOFOOFOOFOFF
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D2680000	FFOFOOFOOFFOF
D2700000	FFOFOOFOOFFFO
D2780000	FFOFOOFOOFFFF
D2800000	FFOFOOFOFOOOO
D2880000	FFOFOOFOFOOOF
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D2B00000	FFOFOOFOFOFFO
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D3B00000	FFOFOOFFFOFFO
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D3F80000	FFOFOOFFFFFFF
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D4500000	FFOFOFOOOFOFO
D4580000	FFOFOFOOOFOFF
D4600000	FFOFOFOOOFFOO
D4680000	FFOFOFOOOFFOF
D4700000	FFOFOFOOOFFFO
D4780000	FFOFOFOOOFFFF
D4800000	FFOFOFOOFOOOO
D4880000	FFOFOFOOFOOOF
D4900000	FFOFOFOOFOOFO
D4980000	FFOFOFOOFOOFF
D4A00000	FFOFOFOOFOFOO
D4A80000	FFOFOFOOFOFOF
D4B00000	FFOFOFOOFOFFO
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D4C80000	FFOFOFOOFFOOF
D4D00000	FFOFOFOOFFOFO
D4D80000	FFOFOFOOFFOFF
D4E00000	FFOFOFOOFFFOO
D4E80000	FFOFOFOOFFFOF
D4F00000	FFOFOFOOFFFFO
D4F80000	FFOFOFOOFFFFF
D5000000	FFOFOFOFOOOOO
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D5100000	FFOFOFOFOOOFO
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D5300000	FFOFOFOFOOFFO
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D5400000	FFOFOFOFOFOOO
D5480000	FFOFOFOFOFOOF
D5500000	FFOFOFOFOFOFO
D5580000	FFOFOFOFOFOFF
D5600000	FFOFOFOFOFFOO
D5680000	FFOFOFOFOFFOF
D5700000	FFOFOFOFOFFFO
D5780000	FFOFOFOFOFFFF
D5800000	FFOFOFOFFOOOO
D5880000	FFOFOFOFFOOOF
D5900000	FFOFOFOFFOOFO
D5980000	FFOFOFOFFOOFF
D5A00000	FFOFOFOFFOFOO
D5A80000	FFOFOFOFFOFOF
D5B00000	FFOFOFOFFOFFO
D5B80000	FFOFOFOFFOFFF
D5C00000	FFOFOFOFFFOOO
D5C80000	FFOFOFOFFFOOF
D5D00000	FFOFOFOFFFOFO
D5D80000	FFOFOFOFFFOFF
D5E00000	FFOFOFOFFFFOO
D5E80000	FFOFOFOFFFFOF
D5F00000	FFOFOFOFFFFFO
D5F80000	FFOFOFOFFFFFF
D6000000	FFOFOFFOOOOOO
D6080000	FFOFOFFOOOOOF
D6100000	FFOFOFFOOOOFO
D6180000	FFOFOFFOOOOFF
D6200000	FFOFOFFOOOFOO
D6280000	FFOFOFFOOOFOF
D6300000	FFOFOFFOOOFFO
D6380000	FFOFOFFOOOFFF
D6400000	FFOFOFFOOFOOO
D6480000	FFOFOFFOOFOOF
D6500000	FFOFOFFOOFOFO
D6580000	FFOFOFFOOFOFF
D6600000	FFOFOFFOOFFOO
D6680000	FFOFOFFOOFFOF
D6700000	FFOFOFFOOFFFO
D6780000	FFOFOFFOOFFFF
D6800000	FFOFOFFOFOOOO
D6880000	FFOFOFFOFOOOF
D6900000	FFOFOFFOFOOFO
D6980000	FFOFOFFOFOOFF
D6A00000	FFOFOFFOFOFOO
D6A80000	FFOFOFFOFOFOF
D6B00000	FFOFOFFOFOFFO
D6B80000	FFOFOFFOFOFFF
D6C00000	FFOFOFFOFFOOO
D6C80000	FFOFOFFOFFOOF
D6D00000	FFOFOFFOFFOFO
D6D80000	FFOFOFFOFFOFF
D6E00000	FFOFOFFOFFFOO
D6E80000	FFOFOFFOFFFOF
D6F00000	FFOFOFFOFFFFO
D6F80000	FFOFOFFOFFFFF
D7000000	FFOFOFFFOOOOO
D7080000	FFOFOFFFOOOOF
D7100000	FFOFOFFFOOOFO
D7180000	FFOFOFFFOOOFF
D7200000	FFOFOFFFOOFOO
D7280000	FFOFOFFFOOFOF
D7300000	FFOFOFFFOOFFO
D7380000	FFOFOFFFOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
D7400000	FFOFOFFFOFOOO
D7480000	FFOFOFFFOFOOF
D7500000	FFOFOFFFOFOFO
D7580000	FFOFOFFFOFOFF
D7600000	FFOFOFFFOFFOO
D7680000	FFOFOFFFOFFOF
D7700000	FFOFOFFFOFFFO
D7780000	FFOFOFFFOFFFF
D7800000	FFOFOFFFFOOOO
D7880000	FFOFOFFFFOOOF
D7900000	FFOFOFFFFOOFO
D7980000	FFOFOFFFFOOFF
D7A00000	FFOFOFFFFOFOO
D7A80000	FFOFOFFFFOFOF
D7B00000	FFOFOFFFFOFFO
D7B80000	FFOFOFFFFOFFF
D7C00000	FFOFOFFFFFOOO
D7C80000	FFOFOFFFFFOOF
D7D00000	FFOFOFFFFFOFO
D7D80000	FFOFOFFFFFOFF
D7E00000	FFOFOFFFFFFOO
D7E80000	FFOFOFFFFFFOF
D7F00000	FFOFOFFFFFFFO
D7F80000	FFOFOFFFFFFFF
D8000000	FFOFFOOOOOOOO
D8080000	FFOFFOOOOOOOF
D8100000	FFOFFOOOOOOFO
D8180000	FFOFFOOOOOOFF
D8200000	FFOFFOOOOOFOO
D8280000	FFOFFOOOOOFOF
D8300000	FFOFFOOOOOFFO
D8380000	FFOFFOOOOOFFF
D8400000	FFOFFOOOOFOOO
D8480000	FFOFFOOOOFOOF
D8500000	FFOFFOOOOFOFO
D8580000	FFOFFOOOOFOFF
D8600000	FFOFFOOOOFFOO
D8680000	FFOFFOOOOFFOF
D8700000	FFOFFOOOOFFFO
D8780000	FFOFFOOOOFFFF
D8800000	FFOFFOOOFOOOO
D8880000	FFOFFOOOFOOOF
D8900000	FFOFFOOOFOOFO
D8980000	FFOFFOOOFOOFF
D8A00000	FFOFFOOOFOFOO
D8A80000	FFOFFOOOFOFOF
D8B00000	FFOFFOOOFOFFO
D8B80000	FFOFFOOOFOFFF
D8C00000	FFOFFOOOFFOOO
D8C80000	FFOFFOOOFFOOF
D8D00000	FFOFFOOOFFOFO
D8D80000	FFOFFOOOFFOFF
D8E00000	FFOFFOOOFFFOO
D8E80000	FFOFFOOOFFFOF
D8F00000	FFOFFOOOFFFFO
D8F80000	FFOFFOOOFFFFF
D9000000	FFOFFOOFOOOOO
D9080000	FFOFFOOFOOOOF
D9100000	FFOFFOOFOOOFO
D9180000	FFOFFOOFOOOFF
D9200000	FFOFFOOFOOFOO
D9280000	FFOFFOOFOOFOF
D9300000	FFOFFOOFOOFFO
D9380000	FFOFFOOFOOFFF
D9400000	FFOFFOOFOFOOO
D9480000	FFOFFOOFOFOOF
D9500000	FFOFFOOFOFOFO
D9580000	FFOFFOOFOFOFF
D9600000	FFOFFOOFOFFOO
D9680000	FFOFFOOFOFFOF
D9700000	FFOFFOOFOFFFO
D9780000	FFOFFOOFOFFFF
D9800000	FFOFFOOFFOOOO
D9880000	FFOFFOOFFOOOF
D9900000	FFOFFOOFFOOFO
D9980000	FFOFFOOFFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
D9A00000	FFOFFOOFFOFOO
D9A80000	FFOFFOOFFOFOF
D9B00000	FFOFFOOFFOFFO
D9B80000	FFOFFOOFFOFFF
D9C00000	FFOFFOOFFFOOO
D9C80000	FFOFFOOFFFOOF
D9D00000	FFOFFOOFFFOFO
D9D80000	FFOFFOOFFFOFF
D9E00000	FFOFFOOFFFFOO
D9E80000	FFOFFOOFFFFOF
D9F00000	FFOFFOOFFFFFO
D9F80000	FFOFFOOFFFFFF
DA000000	FFOFFOFOOOOOO
DA080000	FFOFFOFOOOOOF
DA100000	FFOFFOFOOOOFO
DA180000	FFOFFOFOOOOFF
DA200000	FFOFFOFOOOFOO
DA280000	FFOFFOFOOOFOF
DA300000	FFOFFOFOOOFFO
DA380000	FFOFFOFOOOFFF
DA400000	FFOFFOFOOFOOO
DA480000	FFOFFOFOOFOOF
DA500000	FFOFFOFOOFOFO
DA580000	FFOFFOFOOFOFF
DA600000	FFOFFOFOOFFOO
DA680000	FFOFFOFOOFFOF
DA700000	FFOFFOFOOFFFO
DA780000	FFOFFOFOOFFFF
DA800000	FFOFFOFOFOOOO
DA880000	FFOFFOFOFOOOF
DA900000	FFOFFOFOFOOFO
DA980000	FFOFFOFOFOOFF
DAA00000	FFOFFOFOFOFOO
DAA80000	FFOFFOFOFOFOF
DAB00000	FFOFFOFOFOFFO
DAB80000	FFOFFOFOFOFFF
DAC00000	FFOFFOFOFFOOO
DAC80000	FFOFFOFOFFOOF
DAD00000	FFOFFOFOFFOFO
DAD80000	FFOFFOFOFFOFF
DAE00000	FFOFFOFOFFFOO
DAE80000	FFOFFOFOFFFOF
DAF00000	FFOFFOFOFFFFO
DAF80000	FFOFFOFOFFFFF
DB000000	FFOFFOFFOOOOO
DB080000	FFOFFOFFOOOOF
DB100000	FFOFFOFFOOOFO
DB180000	FFOFFOFFOOOFF
DB200000	FFOFFOFFOOFOO
DB280000	FFOFFOFFOOFOF
DB300000	FFOFFOFFOOFFO
DB380000	FFOFFOFFOOFFF
DB400000	FFOFFOFFOFOOO
DB480000	FFOFFOFFOFOOF
DB500000	FFOFFOFFOFOFO
DB580000	FFOFFOFFOFOFF
DB600000	FFOFFOFFOFFOO
DB680000	FFOFFOFFOFFOF
DB700000	FFOFFOFFOFFFO
DB780000	FFOFFOFFOFFFF
DB800000	FFOFFOFFFOOOO
DB880000	FFOFFOFFFOOOF
DB900000	FFOFFOFFFOOFO
DB980000	FFOFFOFFFOOFF
DBA00000	FFOFFOFFFOFOO
DBA80000	FFOFFOFFFOFOF
DBB00000	FFOFFOFFFOFFO
DBB80000	FFOFFOFFFOFFF
DBC00000	FFOFFOFFFFOOO
DBC80000	FFOFFOFFFFOOF
DBD00000	FFOFFOFFFFOFO
DBD80000	FFOFFOFFFFOFF
DBE00000	FFOFFOFFFFFOO
DBE80000	FFOFFOFFFFFOF
DBF00000	FFOFFOFFFFFFO
DBF80000	FFOFFOFFFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
DC000000	FFOFFFOOOOOOO
DC080000	FFOFFFOOOOOOF
DC100000	FFOFFFOOOOOFO
DC180000	FFOFFFOOOOOFF
DC200000	FFOFFFOOOOFOO
DC280000	FFOFFFOOOOFOF
DC300000	FFOFFFOOOOFFO
DC380000	FFOFFFOOOOFFF
DC400000	FFOFFFOOOFOOO
DC480000	FFOFFFOOOFOOF
DC500000	FFOFFFOOOFOFO
DC580000	FFOFFFOOOFOFF
DC600000	FFOFFFOOOFFOO
DC680000	FFOFFFOOOFFOF
DC700000	FFOFFFOOOFFFO
DC780000	FFOFFFOOOFFFF
DC800000	FFOFFFOOFOOOO
DC880000	FFOFFFOOFOOOF
DC900000	FFOFFFOOFOOFO
DC980000	FFOFFFOOFOOFF
DCA00000	FFOFFFOOFOFOO
DCA80000	FFOFFFOOFOFOF
DCB00000	FFOFFFOOFOFFO
DCB80000	FFOFFFOOFOFFF
DCC00000	FFOFFFOOFFOOO
DCC80000	FFOFFFOOFFOOF
DCD00000	FFOFFFOOFFOFO
DCD80000	FFOFFFOOFFOFF
DCE00000	FFOFFFOOFFFOO
DCE80000	FFOFFFOOFFFOF
DCF00000	FFOFFFOOFFFFO
DCF80000	FFOFFFOOFFFFF
DD000000	FFOFFFOFOOOOO
DD080000	FFOFFFOFOOOOF
DD100000	FFOFFFOFOOOFO
DD180000	FFOFFFOFOOOFF
DD200000	FFOFFFOFOOFOO
DD280000	FFOFFFOFOOFOF
DD300000	FFOFFFOFOOFFO
DD380000	FFOFFFOFOOFFF
DD400000	FFOFFFOFOFOOO
DD480000	FFOFFFOFOFOOF
DD500000	FFOFFFOFOFOFO
DD580000	FFOFFFOFOFOFF
DD600000	FFOFFFOFOFFOO
DD680000	FFOFFFOFOFFOF
DD700000	FFOFFFOFOFFFO
DD780000	FFOFFFOFOFFFF
DD800000	FFOFFFOFFOOOO
DD880000	FFOFFFOFFOOOF
DD900000	FFOFFFOFFOOFO
DD980000	FFOFFFOFFOOFF
DDA00000	FFOFFFOFFOFOO
DDA80000	FFOFFFOFFOFOF
DDB00000	FFOFFFOFFOFFO
DDB80000	FFOFFFOFFOFFF
DDC00000	FFOFFFOFFFOOO
DDC80000	FFOFFFOFFFOOF
DDD00000	FFOFFFOFFFOFO
DDD80000	FFOFFFOFFFOFF
DDE00000	FFOFFFOFFFFOO
DDE80000	FFOFFFOFFFFOF
DDF00000	FFOFFFOFFFFFO
DDF80000	FFOFFFOFFFFFF
DE000000	FFOFFFFOOOOOO
DE080000	FFOFFFFOOOOOF
DE100000	FFOFFFFOOOOFO
DE180000	FFOFFFFOOOOFF
DE200000	FFOFFFFOOOFOO
DE280000	FFOFFFFOOOFOF
DE300000	FFOFFFFOOOFFO
DE380000	FFOFFFFOOOFFF
DE400000	FFOFFFFOOFOOO
DE480000	FFOFFFFOOFOOF
DE500000	FFOFFFFOOFOFO
DE580000	FFOFFFFOOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
DE600000	FFOFFFFOOFFOO
DE680000	FFOFFFFOOFFOF
DE700000	FFOFFFFOOFFFO
DE780000	FFOFFFFOOFFFF
DE800000	FFOFFFFOFOOOO
DE880000	FFOFFFFOFOOOF
DE900000	FFOFFFFOFOOFO
DE980000	FFOFFFFOFOOFF
DEA00000	FFOFFFFOFOFOO
DEA80000	FFOFFFFOFOFOF
DEB00000	FFOFFFFOFOFFO
DEB80000	FFOFFFFOFOFFF
DEC00000	FFOFFFFOFFOOO
DEC80000	FFOFFFFOFFOOF
DED00000	FFOFFFFOFFOFO
DED80000	FFOFFFFOFFOFF
DEE00000	FFOFFFFOFFFOO
DEE80000	FFOFFFFOFFFOF
DEF00000	FFOFFFFOFFFFO
DEF80000	FFOFFFFOFFFFF
DF000000	FFOFFFFFOOOOO
DF080000	FFOFFFFFOOOOF
DF100000	FFOFFFFFOOOFO
DF180000	FFOFFFFFOOOFF
DF200000	FFOFFFFFOOFOO
DF280000	FFOFFFFFOOFOF
DF300000	FFOFFFFFOOFFO
DF380000	FFOFFFFFOOFFF
DF400000	FFOFFFFFOFOOO
DF480000	FFOFFFFFOFOOF
DF500000	FFOFFFFFOFOFO
DF580000	FFOFFFFFOFOFF
DF600000	FFOFFFFFOFFOO
DF680000	FFOFFFFFOFFOF
DF700000	FFOFFFFFOFFFO
DF780000	FFOFFFFFOFFFF
DF800000	FFOFFFFFFOOOO
DF880000	FFOFFFFFFOOOF
DF900000	FFOFFFFFFOOFO
DF980000	FFOFFFFFFOOFF
DFA00000	FFOFFFFFFOFOO
DFA80000	FFOFFFFFFOFOF
DFB00000	FFOFFFFFFOFFO
DFB80000	FFOFFFFFFOFFF
DFC00000	FFOFFFFFFFOOO
DFC80000	FFOFFFFFFFOOF
DFD00000	FFOFFFFFFFOFO
DFD80000	FFOFFFFFFFOFF
DFE00000	FFOFFFFFFFFOO
DFE80000	FFOFFFFFFFFOF
DFF00000	FFOFFFFFFFFFO
DFF80000	FFOFFFFFFFFFF
E0000000	FFFOOOOOOOOOO
E0080000	FFFOOOOOOOOOF
E0100000	FFFOOOOOOOOFO
E0180000	FFFOOOOOOOOFF
E0200000	FFFOOOOOOOFOO
E0280000	FFFOOOOOOOFOF
E0300000	FFFOOOOOOOFFO
E0380000	FFFOOOOOOOFFF
E0400000	FFFOOOOOOFOOO
E0480000	FFFOOOOOOFOOF
E0500000	FFFOOOOOOFOFO
E0580000	FFFOOOOOOFOFF
E0600000	FFFOOOOOOFFOO
E0680000	FFFOOOOOOFFOF
E0700000	FFFOOOOOOFFFO
E0780000	FFFOOOOOOFFFF
E0800000	FFFOOOOOFOOOO
E0880000	FFFOOOOOFOOOF
E0900000	FFFOOOOOFOOFO
E0980000	FFFOOOOOFOOFF
E0A00000	FFFOOOOOFOFOO
E0A80000	FFFOOOOOFOFOF
E0B00000	FFFOOOOOFOFFO
E0B80000	FFFOOOOOFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
E0C00000	FFFOOOOOFFOOO
E0C80000	FFFOOOOOFFOOF
E0D00000	FFFOOOOOFFOFO
E0D80000	FFFOOOOOFFOFF
E0E00000	FFFOOOOOFFFOO
E0E80000	FFFOOOOOFFFOF
E0F00000	FFFOOOOOFFFFO
E0F80000	FFFOOOOOFFFFF
E1000000	FFFOOOOFOOOOO
E1080000	FFFOOOOFOOOOF
E1100000	FFFOOOOFOOOFO
E1180000	FFFOOOOFOOOFF
E1200000	FFFOOOOFOOFOO
E1280000	FFFOOOOFOOFOF
E1300000	FFFOOOOFOOFFO
E1380000	FFFOOOOFOOFFF
E1400000	FFFOOOOFOFOOO
E1480000	FFFOOOOFOFOOF
E1500000	FFFOOOOFOFOFO
E1580000	FFFOOOOFOFOFF
E1600000	FFFOOOOFOFFOO
E1680000	FFFOOOOFOFFOF
E1700000	FFFOOOOFOFFFO
E1780000	FFFOOOOFOFFFF
E1800000	FFFOOOOFFOOOO
E1880000	FFFOOOOFFOOOF
E1900000	FFFOOOOFFOOFO
E1980000	FFFOOOOFFOOFF
E1A00000	FFFOOOOFFOFOO
E1A80000	FFFOOOOFFOFOF
E1B00000	FFFOOOOFFOFFO
E1B80000	FFFOOOOFFOFFF
E1C00000	FFFOOOOFFFOOO
E1C80000	FFFOOOOFFFOOF
E1D00000	FFFOOOOFFFOFO
E1D80000	FFFOOOOFFFOFF
E1E00000	FFFOOOOFFFFOO
E1E80000	FFFOOOOFFFFOF
E1F00000	FFFOOOOFFFFFO
E1F80000	FFFOOOOFFFFFF
E2000000	FFFOOOFOOOOOO
E2080000	FFFOOOFOOOOOF
E2100000	FFFOOOFOOOOFO
E2180000	FFFOOOFOOOOFF
E2200000	FFFOOOFOOOFOO
E2280000	FFFOOOFOOOFOF
E2300000	FFFOOOFOOOFFO
E2380000	FFFOOOFOOOFFF
E2400000	FFFOOOFOOFOOO
E2480000	FFFOOOFOOFOOF
E2500000	FFFOOOFOOFOFO
E2580000	FFFOOOFOOFOFF
E2600000	FFFOOOFOOFFOO
E2680000	FFFOOOFOOFFOF
E2700000	FFFOOOFOOFFFO
E2780000	FFFOOOFOOFFFF
E2800000	FFFOOOFOFOOOO
E2880000	FFFOOOFOFOOOF
E2900000	FFFOOOFOFOOFO
E2980000	FFFOOOFOFOOFF
E2A00000	FFFOOOFOFOFOO
E2A80000	FFFOOOFOFOFOF
E2B00000	FFFOOOFOFOFFO
E2B80000	FFFOOOFOFOFFF
E2C00000	FFFOOOFOFFOOO
E2C80000	FFFOOOFOFFOOF
E2D00000	FFFOOOFOFFOFO
E2D80000	FFFOOOFOFFOFF
E2E00000	FFFOOOFOFFFOO
E2E80000	FFFOOOFOFFFOF
E2F00000	FFFOOOFOFFFFO
E2F80000	FFFOOOFOFFFFF
E3000000	FFFOOOFFOOOOO
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E3480000	FFFOOOFFOFOOF
E3500000	FFFOOOFFOFOFO
E3580000	FFFOOOFFOFOFF
E3600000	FFFOOOFFOFFOO
E3680000	FFFOOOFFOFFOF
E3700000	FFFOOOFFOFFFO
E3780000	FFFOOOFFOFFFF
E3800000	FFFOOOFFFOOOO
E3880000	FFFOOOFFFOOOF
E3900000	FFFOOOFFFOOFO
E3980000	FFFOOOFFFOOFF
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E3F00000	FFFOOOFFFFFFO
E3F80000	FFFOOOFFFFFFF
E4000000	FFFOOFOOOOOOO
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E4100000	FFFOOFOOOOOFO
E4180000	FFFOOFOOOOOFF
E4200000	FFFOOFOOOOFOO
E4280000	FFFOOFOOOOFOF
E4300000	FFFOOFOOOOFFO
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E4480000	FFFOOFOOOFOOF
E4500000	FFFOOFOOOFOFO
E4580000	FFFOOFOOOFOFF
E4600000	FFFOOFOOOFFOO
E4680000	FFFOOFOOOFFOF
E4700000	FFFOOFOOOFFFO
E4780000	FFFOOFOOOFFFF
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E4880000	FFFOOFOOFOOOF
E4900000	FFFOOFOOFOOFO
E4980000	FFFOOFOOFOOFF
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E5280000	FFFOOFOFOOFOF
E5300000	FFFOOFOFOOFFO
E5380000	FFFOOFOFOOFFF
E5400000	FFFOOFOFOFOOO
E5480000	FFFOOFOFOFOOF
E5500000	FFFOOFOFOFOFO
E5580000	FFFOOFOFOFOFF
E5600000	FFFOOFOFOFFOO
E5680000	FFFOOFOFOFFOF
E5700000	FFFOOFOFOFFFO
E5780000	FFFOOFOFOFFFF
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E5880000	FFFOOFOFFOOOF
E5900000	FFFOOFOFFOOFO
E5980000	FFFOOFOFFOOFF
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E5A80000	FFFOOFOFFOFOF
E5B00000	FFFOOFOFFOFFO
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E5C00000	FFFOOFOFFFOOO
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E5D00000	FFFOOFOFFFOFO
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E5F00000	FFFOOFOFFFFFO
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E6280000	FFFOOFFOOOFOF
E6300000	FFFOOFFOOOFFO
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E6400000	FFFOOFFOOFOOO
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E6600000	FFFOOFFOOFFOO
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E7480000	FFFOOFFFOFOOF
E7500000	FFFOOFFFOFOFO
E7580000	FFFOOFFFOFOFF
E7600000	FFFOOFFFOFFOO
E7680000	FFFOOFFFOFFOF
E7700000	FFFOOFFFOFFFO
E7780000	FFFOOFFFOFFFF
E7800000	FFFOOFFFFOOOO
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E7900000	FFFOOFFFFOOFO
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E7C00000	FFFOOFFFFFOOO
E7C80000	FFFOOFFFFFOOF
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E7D80000	FFFOOFFFFFOFF
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E7F00000	FFFOOFFFFFFFO
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E8500000	FFFOFOOOOFOFO
E8580000	FFFOFOOOOFOFF
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E8900000	FFFOFOOOFOOFO
E8980000	FFFOFOOOFOOFF
E8A00000	FFFOFOOOFOFOO
E8A80000	FFFOFOOOFOFOF
E8B00000	FFFOFOOOFOFFO
E8B80000	FFFOFOOOFOFFF
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E8C80000	FFFOFOOOFFOOF
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E8E00000	FFFOFOOOFFFOO
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E8F00000	FFFOFOOOFFFFO
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E9180000	FFFOFOOFOOOFF
E9200000	FFFOFOOFOOFOO
E9280000	FFFOFOOFOOFOF
E9300000	FFFOFOOFOOFFO
E9380000	FFFOFOOFOOFFF
E9400000	FFFOFOOFOFOOO
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E9680000	FFFOFOOFOFFOF
E9700000	FFFOFOOFOFFFO
E9780000	FFFOFOOFOFFFF
E9800000	FFFOFOOFFOOOO
E9880000	FFFOFOOFFOOOF
E9900000	FFFOFOOFFOOFO
E9980000	FFFOFOOFFOOFF
E9A00000	FFFOFOOFFOFOO
E9A80000	FFFOFOOFFOFOF
E9B00000	FFFOFOOFFOFFO
E9B80000	FFFOFOOFFOFFF
E9C00000	FFFOFOOFFFOOO
E9C80000	FFFOFOOFFFOOF
E9D00000	FFFOFOOFFFOFO
E9D80000	FFFOFOOFFFOFF
E9E00000	FFFOFOOFFFFOO
E9E80000	FFFOFOOFFFFOF
E9F00000	FFFOFOOFFFFFO
E9F80000	FFFOFOOFFFFFF
EA000000	FFFOFOFOOOOOO
EA080000	FFFOFOFOOOOOF
EA100000	FFFOFOFOOOOFO
EA180000	FFFOFOFOOOOFF
EA200000	FFFOFOFOOOFOO
EA280000	FFFOFOFOOOFOF
EA300000	FFFOFOFOOOFFO
EA380000	FFFOFOFOOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
EA400000	FFFOFOFOOFOOO
EA480000	FFFOFOFOOFOOF
EA500000	FFFOFOFOOFOFO
EA580000	FFFOFOFOOFOFF
EA600000	FFFOFOFOOFFOO
EA680000	FFFOFOFOOFFOF
EA700000	FFFOFOFOOFFFO
EA780000	FFFOFOFOOFFFF
EA800000	FFFOFOFOFOOOO
EA880000	FFFOFOFOFOOOF
EA900000	FFFOFOFOFOOFO
EA980000	FFFOFOFOFOOFF
EAA00000	FFFOFOFOFOFOO
EAA80000	FFFOFOFOFOFOF
EAB00000	FFFOFOFOFOFFO
EAB80000	FFFOFOFOFOFFF
EAC00000	FFFOFOFOFFOOO
EAC80000	FFFOFOFOFFOOF
EAD00000	FFFOFOFOFFOFO
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EAE00000	FFFOFOFOFFFOO
EAE80000	FFFOFOFOFFFOF
EAF00000	FFFOFOFOFFFFO
EAF80000	FFFOFOFOFFFFF
EB000000	FFFOFOFFOOOOO
EB080000	FFFOFOFFOOOOF
EB100000	FFFOFOFFOOOFO
EB180000	FFFOFOFFOOOFF
EB200000	FFFOFOFFOOFOO
EB280000	FFFOFOFFOOFOF
EB300000	FFFOFOFFOOFFO
EB380000	FFFOFOFFOOFFF
EB400000	FFFOFOFFOFOOO
EB480000	FFFOFOFFOFOOF
EB500000	FFFOFOFFOFOFO
EB580000	FFFOFOFFOFOFF
EB600000	FFFOFOFFOFFOO
EB680000	FFFOFOFFOFFOF
EB700000	FFFOFOFFOFFFO
EB780000	FFFOFOFFOFFFF
EB800000	FFFOFOFFFOOOO
EB880000	FFFOFOFFFOOOF
EB900000	FFFOFOFFFOOFO
EB980000	FFFOFOFFFOOFF
EBA00000	FFFOFOFFFOFOO
EBA80000	FFFOFOFFFOFOF
EBB00000	FFFOFOFFFOFFO
EBB80000	FFFOFOFFFOFFF
EBC00000	FFFOFOFFFFOOO
EBC80000	FFFOFOFFFFOOF
EBD00000	FFFOFOFFFFOFO
EBD80000	FFFOFOFFFFOFF
EBE00000	FFFOFOFFFFFOO
EBE80000	FFFOFOFFFFFOF
EBF00000	FFFOFOFFFFFFO
EBF80000	FFFOFOFFFFFFF
EC000000	FFFOFFOOOOOOO
EC080000	FFFOFFOOOOOOF
EC100000	FFFOFFOOOOOFO
EC180000	FFFOFFOOOOOFF
EC200000	FFFOFFOOOOFOO
EC280000	FFFOFFOOOOFOF
EC300000	FFFOFFOOOOFFO
EC380000	FFFOFFOOOOFFF
EC400000	FFFOFFOOOFOOO
EC480000	FFFOFFOOOFOOF
EC500000	FFFOFFOOOFOFO
EC580000	FFFOFFOOOFOFF
EC600000	FFFOFFOOOFFOO
EC680000	FFFOFFOOOFFOF
EC700000	FFFOFFOOOFFFO
EC780000	FFFOFFOOOFFFF
EC800000	FFFOFFOOFOOOO
EC880000	FFFOFFOOFOOOF
EC900000	FFFOFFOOFOOFO
EC980000	FFFOFFOOFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
ECA00000	FFFOFFOOFOFOO
ECA80000	FFFOFFOOFOFOF
ECB00000	FFFOFFOOFOFFO
ECB80000	FFFOFFOOFOFFF
ECC00000	FFFOFFOOFFOOO
ECC80000	FFFOFFOOFFOOF
ECD00000	FFFOFFOOFFOFO
ECD80000	FFFOFFOOFFOFF
ECE00000	FFFOFFOOFFFOO
ECE80000	FFFOFFOOFFFOF
ECF00000	FFFOFFOOFFFFO
ECF80000	FFFOFFOOFFFFF
ED000000	FFFOFFOFOOOOO
ED080000	FFFOFFOFOOOOF
ED100000	FFFOFFOFOOOFO
ED180000	FFFOFFOFOOOFF
ED200000	FFFOFFOFOOFOO
ED280000	FFFOFFOFOOFOF
ED300000	FFFOFFOFOOFFO
ED380000	FFFOFFOFOOFFF
ED400000	FFFOFFOFOFOOO
ED480000	FFFOFFOFOFOOF
ED500000	FFFOFFOFOFOFO
ED580000	FFFOFFOFOFOFF
ED600000	FFFOFFOFOFFOO
ED680000	FFFOFFOFOFFOF
ED700000	FFFOFFOFOFFFO
ED780000	FFFOFFOFOFFFF
ED800000	FFFOFFOFFOOOO
ED880000	FFFOFFOFFOOOF
ED900000	FFFOFFOFFOOFO
ED980000	FFFOFFOFFOOFF
EDA00000	FFFOFFOFFOFOO
EDA80000	FFFOFFOFFOFOF
EDB00000	FFFOFFOFFOFFO
EDB80000	FFFOFFOFFOFFF
EDC00000	FFFOFFOFFFOOO
EDC80000	FFFOFFOFFFOOF
EDD00000	FFFOFFOFFFOFO
EDD80000	FFFOFFOFFFOFF
EDE00000	FFFOFFOFFFFOO
EDE80000	FFFOFFOFFFFOF
EDF00000	FFFOFFOFFFFFO
EDF80000	FFFOFFOFFFFFF
EE000000	FFFOFFFOOOOOO
EE080000	FFFOFFFOOOOOF
EE100000	FFFOFFFOOOOFO
EE180000	FFFOFFFOOOOFF
EE200000	FFFOFFFOOOFOO
EE280000	FFFOFFFOOOFOF
EE300000	FFFOFFFOOOFFO
EE380000	FFFOFFFOOOFFF
EE400000	FFFOFFFOOFOOO
EE480000	FFFOFFFOOFOOF
EE500000	FFFOFFFOOFOFO
EE580000	FFFOFFFOOFOFF
EE600000	FFFOFFFOOFFOO
EE680000	FFFOFFFOOFFOF
EE700000	FFFOFFFOOFFFO
EE780000	FFFOFFFOOFFFF
EE800000	FFFOFFFOFOOOO
EE880000	FFFOFFFOFOOOF
EE900000	FFFOFFFOFOOFO
EE980000	FFFOFFFOFOOFF
EEA00000	FFFOFFFOFOFOO
EEA80000	FFFOFFFOFOFOF
EEB00000	FFFOFFFOFOFFO
EEB80000	FFFOFFFOFOFFF
EEC00000	FFFOFFFOFFOOO
EEC80000	FFFOFFFOFFOOF
EED00000	FFFOFFFOFFOFO
EED80000	FFFOFFFOFFOFF
EEE00000	FFFOFFFOFFFOO
EEE80000	FFFOFFFOFFFOF
EEF00000	FFFOFFFOFFFFO
EEF80000	FFFOFFFOFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
EF000000	FFFOFFFFOOOOO
EF080000	FFFOFFFFOOOOF
EF100000	FFFOFFFFOOOFO
EF180000	FFFOFFFFOOOFF
EF200000	FFFOFFFFOOFOO
EF280000	FFFOFFFFOOFOF
EF300000	FFFOFFFFOOFFO
EF380000	FFFOFFFFOOFFF
EF400000	FFFOFFFFOFOOO
EF480000	FFFOFFFFOFOOF
EF500000	FFFOFFFFOFOFO
EF580000	FFFOFFFFOFOFF
EF600000	FFFOFFFFOFFOO
EF680000	FFFOFFFFOFFOF
EF700000	FFFOFFFFOFFFO
EF780000	FFFOFFFFOFFFF
EF800000	FFFOFFFFFOOOO
EF880000	FFFOFFFFFOOOF
EF900000	FFFOFFFFFOOFO
EF980000	FFFOFFFFFOOFF
EFA00000	FFFOFFFFFOFOO
EFA80000	FFFOFFFFFOFOF
EFB00000	FFFOFFFFFOFFO
EFB80000	FFFOFFFFFOFFF
EFC00000	FFFOFFFFFFOOO
EFC80000	FFFOFFFFFFOOF
EFD00000	FFFOFFFFFFOFO
EFD80000	FFFOFFFFFFOFF
EFE00000	FFFOFFFFFFFOO
EFE80000	FFFOFFFFFFFOF
EFF00000	FFFOFFFFFFFFO
EFF80000	FFFOFFFFFFFFF
F0000000	FFFFOOOOOOOOO
F0080000	FFFFOOOOOOOOF
F0100000	FFFFOOOOOOOFO
F0180000	FFFFOOOOOOOFF
F0200000	FFFFOOOOOOFOO
F0280000	FFFFOOOOOOFOF
F0300000	FFFFOOOOOOFFO
F0380000	FFFFOOOOOOFFF
F0400000	FFFFOOOOOFOOO
F0480000	FFFFOOOOOFOOF
F0500000	FFFFOOOOOFOFO
F0580000	FFFFOOOOOFOFF
F0600000	FFFFOOOOOFFOO
F0680000	FFFFOOOOOFFOF
F0700000	FFFFOOOOOFFFO
F0780000	FFFFOOOOOFFFF
F0800000	FFFFOOOOFOOOO
F0880000	FFFFOOOOFOOOF
F0900000	FFFFOOOOFOOFO
F0980000	FFFFOOOOFOOFF
F0A00000	FFFFOOOOFOFOO
F0A80000	FFFFOOOOFOFOF
F0B00000	FFFFOOOOFOFFO
F0B80000	FFFFOOOOFOFFF
F0C00000	FFFFOOOOFFOOO
F0C80000	FFFFOOOOFFOOF
F0D00000	FFFFOOOOFFOFO
F0D80000	FFFFOOOOFFOFF
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F0E80000	FFFFOOOOFFFOF
F0F00000	FFFFOOOOFFFFO
F0F80000	FFFFOOOOFFFFF
F1000000	FFFFOOOFOOOOO
F1080000	FFFFOOOFOOOOF
F1100000	FFFFOOOFOOOFO
F1180000	FFFFOOOFOOOFF
F1200000	FFFFOOOFOOFOO
F1280000	FFFFOOOFOOFOF
F1300000	FFFFOOOFOOFFO
F1380000	FFFFOOOFOOFFF
F1400000	FFFFOOOFOFOOO
F1480000	FFFFOOOFOFOOF
F1500000	FFFFOOOFOFOFO
F1580000	FFFFOOOFOFOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
F1600000	FFFFOOOFOFFOO
F1680000	FFFFOOOFOFFOF
F1700000	FFFFOOOFOFFFO
F1780000	FFFFOOOFOFFFF
F1800000	FFFFOOOFFOOOO
F1880000	FFFFOOOFFOOOF
F1900000	FFFFOOOFFOOFO
F1980000	FFFFOOOFFOOFF
F1A00000	FFFFOOOFFOFOO
F1A80000	FFFFOOOFFOFOF
F1B00000	FFFFOOOFFOFFO
F1B80000	FFFFOOOFFOFFF
F1C00000	FFFFOOOFFFOOO
F1C80000	FFFFOOOFFFOOF
F1D00000	FFFFOOOFFFOFO
F1D80000	FFFFOOOFFFOFF
F1E00000	FFFFOOOFFFFOO
F1E80000	FFFFOOOFFFFOF
F1F00000	FFFFOOOFFFFFO
F1F80000	FFFFOOOFFFFFF
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F2080000	FFFFOOFOOOOOF
F2100000	FFFFOOFOOOOFO
F2180000	FFFFOOFOOOOFF
F2200000	FFFFOOFOOOFOO
F2280000	FFFFOOFOOOFOF
F2300000	FFFFOOFOOOFFO
F2380000	FFFFOOFOOOFFF
F2400000	FFFFOOFOOFOOO
F2480000	FFFFOOFOOFOOF
F2500000	FFFFOOFOOFOFO
F2580000	FFFFOOFOOFOFF
F2600000	FFFFOOFOOFFOO
F2680000	FFFFOOFOOFFOF
F2700000	FFFFOOFOOFFFO
F2780000	FFFFOOFOOFFFF
F2800000	FFFFOOFOFOOOO
F2880000	FFFFOOFOFOOOF
F2900000	FFFFOOFOFOOFO
F2980000	FFFFOOFOFOOFF
F2A00000	FFFFOOFOFOFOO
F2A80000	FFFFOOFOFOFOF
F2B00000	FFFFOOFOFOFFO
F2B80000	FFFFOOFOFOFFF
F2C00000	FFFFOOFOFFOOO
F2C80000	FFFFOOFOFFOOF
F2D00000	FFFFOOFOFFOFO
F2D80000	FFFFOOFOFFOFF
F2E00000	FFFFOOFOFFFOO
F2E80000	FFFFOOFOFFFOF
F2F00000	FFFFOOFOFFFFO
F2F80000	FFFFOOFOFFFFF
F3000000	FFFFOOFFOOOOO
F3080000	FFFFOOFFOOOOF
F3100000	FFFFOOFFOOOFO
F3180000	FFFFOOFFOOOFF
F3200000	FFFFOOFFOOFOO
F3280000	FFFFOOFFOOFOF
F3300000	FFFFOOFFOOFFO
F3380000	FFFFOOFFOOFFF
F3400000	FFFFOOFFOFOOO
F3480000	FFFFOOFFOFOOF
F3500000	FFFFOOFFOFOFO
F3580000	FFFFOOFFOFOFF
F3600000	FFFFOOFFOFFOO
F3680000	FFFFOOFFOFFOF
F3700000	FFFFOOFFOFFFO
F3780000	FFFFOOFFOFFFF
F3800000	FFFFOOFFFOOOO
F3880000	FFFFOOFFFOOOF
F3900000	FFFFOOFFFOOFO
F3980000	FFFFOOFFFOOFF
F3A00000	FFFFOOFFFOFOO
F3A80000	FFFFOOFFFOFOF
F3B00000	FFFFOOFFFOFFO
F3B80000	FFFFOOFFFOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
F3C00000	FFFFOOFFFFOOO
F3C80000	FFFFOOFFFFOOF
F3D00000	FFFFOOFFFFOFO
F3D80000	FFFFOOFFFFOFF
F3E00000	FFFFOOFFFFFOO
F3E80000	FFFFOOFFFFFOF
F3F00000	FFFFOOFFFFFFO
F3F80000	FFFFOOFFFFFFF
F4000000	FFFFOFOOOOOOO
F4080000	FFFFOFOOOOOOF
F4100000	FFFFOFOOOOOFO
F4180000	FFFFOFOOOOOFF
F4200000	FFFFOFOOOOFOO
F4280000	FFFFOFOOOOFOF
F4300000	FFFFOFOOOOFFO
F4380000	FFFFOFOOOOFFF
F4400000	FFFFOFOOOFOOO
F4480000	FFFFOFOOOFOOF
F4500000	FFFFOFOOOFOFO
F4580000	FFFFOFOOOFOFF
F4600000	FFFFOFOOOFFOO
F4680000	FFFFOFOOOFFOF
F4700000	FFFFOFOOOFFFO
F4780000	FFFFOFOOOFFFF
F4800000	FFFFOFOOFOOOO
F4880000	FFFFOFOOFOOOF
F4900000	FFFFOFOOFOOFO
F4980000	FFFFOFOOFOOFF
F4A00000	FFFFOFOOFOFOO
F4A80000	FFFFOFOOFOFOF
F4B00000	FFFFOFOOFOFFO
F4B80000	FFFFOFOOFOFFF
F4C00000	FFFFOFOOFFOOO
F4C80000	FFFFOFOOFFOOF
F4D00000	FFFFOFOOFFOFO
F4D80000	FFFFOFOOFFOFF
F4E00000	FFFFOFOOFFFOO
F4E80000	FFFFOFOOFFFOF
F4F00000	FFFFOFOOFFFFO
F4F80000	FFFFOFOOFFFFF
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F5680000	FFFFOFOFOFFOF
F5700000	FFFFOFOFOFFFO
F5780000	FFFFOFOFOFFFF
F5800000	FFFFOFOFFOOOO
F5880000	FFFFOFOFFOOOF
F5900000	FFFFOFOFFOOFO
F5980000	FFFFOFOFFOOFF
F5A00000	FFFFOFOFFOFOO
F5A80000	FFFFOFOFFOFOF
F5B00000	FFFFOFOFFOFFO
F5B80000	FFFFOFOFFOFFF
F5C00000	FFFFOFOFFFOOO
F5C80000	FFFFOFOFFFOOF
F5D00000	FFFFOFOFFFOFO
F5D80000	FFFFOFOFFFOFF
F5E00000	FFFFOFOFFFFOO
F5E80000	FFFFOFOFFFFOF
F5F00000	FFFFOFOFFFFFO
F5F80000	FFFFOFOFFFFFF
F6000000	FFFFOFFOOOOOO
F6080000	FFFFOFFOOOOOF
F6100000	FFFFOFFOOOOFO
F6180000	FFFFOFFOOOOFF
F6200000	FFFFOFFOOOFOO
F6280000	FFFFOFFOOOFOF
F6300000	FFFFOFFOOOFFO
F6380000	FFFFOFFOOOFFF
F6400000	FFFFOFFOOFOOO
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F6B80000	FFFFOFFOFOFFF
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F6C80000	FFFFOFFOFFOOF
F6D00000	FFFFOFFOFFOFO
F6D80000	FFFFOFFOFFOFF
F6E00000	FFFFOFFOFFFOO
F6E80000	FFFFOFFOFFFOF
F6F00000	FFFFOFFOFFFFO
F6F80000	FFFFOFFOFFFFF
F7000000	FFFFOFFFOOOOO
F7080000	FFFFOFFFOOOOF
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F7900000	FFFFOFFFFOOFO
F7980000	FFFFOFFFFOOFF
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F7A80000	FFFFOFFFFOFOF
F7B00000	FFFFOFFFFOFFO
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F7C00000	FFFFOFFFFFOOO
F7C80000	FFFFOFFFFFOOF
F7D00000	FFFFOFFFFFOFO
F7D80000	FFFFOFFFFFOFF
F7E00000	FFFFOFFFFFFOO
F7E80000	FFFFOFFFFFFOF
F7F00000	FFFFOFFFFFFFO
F7F80000	FFFFOFFFFFFFF
F8000000	FFFFFOOOOOOOO
F8080000	FFFFFOOOOOOOF
F8100000	FFFFFOOOOOOFO
F8180000	FFFFFOOOOOOFF
F8200000	FFFFFOOOOOFOO
F8280000	FFFFFOOOOOFOF
F8300000	FFFFFOOOOOFFO
F8380000	FFFFFOOOOOFFF
F8400000	FFFFFOOOOFOOO
F8480000	FFFFFOOOOFOOF
F8500000	FFFFFOOOOFOFO
F8580000	FFFFFOOOOFOFF
F8600000	FFFFFOOOOFFOO
F8680000	FFFFFOOOOFFOF
F8700000	FFFFFOOOOFFFO
F8780000	FFFFFOOOOFFFF
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FA500000	FFFFFOFOOFOFO
FA580000	FFFFFOFOOFOFF
FA600000	FFFFFOFOOFFOO
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FB480000	FFFFFOFFOFOOF
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FC480000	FFFFFFOOOFOOF
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FC800000	FFFFFFOOFOOOO
FC880000	FFFFFFOOFOOOF
FC900000	FFFFFFOOFOOFO
FC980000	FFFFFFOOFOOFF
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FCF80000	FFFFFFOOFFFFF
FD000000	FFFFFFOFOOOOO
FD080000	FFFFFFOFOOOOF
FD100000	FFFFFFOFOOOFO
FD180000	FFFFFFOFOOOFF
FD200000	FFFFFFOFOOFOO
FD280000	FFFFFFOFOOFOF
FD300000	FFFFFFOFOOFFO
FD380000	FFFFFFOFOOFFF

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ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
FD400000	FFFFFFOFOFOOO
FD480000	FFFFFFOFOFOOF
FD500000	FFFFFFOFOFOFO
FD580000	FFFFFFOFOFOFF
FD600000	FFFFFFOFOFFOO
FD680000	FFFFFFOFOFFOF
FD700000	FFFFFFOFOFFFO
FD780000	FFFFFFOFOFFFF
FD800000	FFFFFFOFFOOOO
FD880000	FFFFFFOFFOOOF
FD900000	FFFFFFOFFOOFO
FD980000	FFFFFFOFFOOFF
FDA00000	FFFFFFOFFOFOO
FDA80000	FFFFFFOFFOFOF
FDB00000	FFFFFFOFFOFFO
FDB80000	FFFFFFOFFOFFF
FDC00000	FFFFFFOFFFOOO
FDC80000	FFFFFFOFFFOOF
FDD00000	FFFFFFOFFFOFO
FDD80000	FFFFFFOFFFOFF
FDE00000	FFFFFFOFFFFOO
FDE80000	FFFFFFOFFFFOF
FDF00000	FFFFFFOFFFFFO
FDF80000	FFFFFFOFFFFFF
FE000000	FFFFFFFOOOOOO
FE080000	FFFFFFFOOOOOF
FE100000	FFFFFFFOOOOFO
FE180000	FFFFFFFOOOOFF
FE200000	FFFFFFFOOOFOO
FE280000	FFFFFFFOOOFOF
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FE400000	FFFFFFFOOFOOO
FE480000	FFFFFFFOOFOOF
FE500000	FFFFFFFOOFOFO
FE580000	FFFFFFFOOFOFF
FE600000	FFFFFFFOOFFOO
FE680000	FFFFFFFOOFFOF
FE700000	FFFFFFFOOFFFO
FE780000	FFFFFFFOOFFFF
FE800000	FFFFFFFOFOOOO
FE880000	FFFFFFFOFOOOF
FE900000	FFFFFFFOFOOFO
FE980000	FFFFFFFOFOOFF
FEA00000	FFFFFFFOFOFOO
FEA80000	FFFFFFFOFOFOF
FEB00000	FFFFFFFOFOFFO
FEB80000	FFFFFFFOFOFFF
FEC00000	FFFFFFFOFFOOO
FEC80000	FFFFFFFOFFOOF
FED00000	FFFFFFFOFFOFO
FED80000	FFFFFFFOFFOFF
FEE00000	FFFFFFFOFFFOO
FEE80000	FFFFFFFOFFFOF
FEF00000	FFFFFFFOFFFFO
FEF80000	FFFFFFFOFFFFF
FF000000	FFFFFFFFOOOOO
FF080000	FFFFFFFFOOOOF
FF100000	FFFFFFFFOOOFO
FF180000	FFFFFFFFOOOFF
FF200000	FFFFFFFFOOFOO
FF280000	FFFFFFFFOOFOF
FF300000	FFFFFFFFOOFFO
FF380000	FFFFFFFFOOFFF
FF400000	FFFFFFFFOFOOO
FF480000	FFFFFFFFOFOOF
FF500000	FFFFFFFFOFOFO
FF580000	FFFFFFFFOFOFF
FF600000	FFFFFFFFOFFOO
FF680000	FFFFFFFFOFFOF
FF700000	FFFFFFFFOFFFO
FF780000	FFFFFFFFOFFFF
FF800000	FFFFFFFFFOOOO
FF880000	FFFFFFFFFOOOF
FF900000	FFFFFFFFFOOFO
FF980000	FFFFFFFFFOOFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345
FFA00000	FFFFFFFFFOFOO
FFA80000	FFFFFFFFFOFOF
FFB00000	FFFFFFFFFOFFO
FFB80000	FFFFFFFFFOFFF
FFC00000	FFFFFFFFFFOOO
FFC80000	FFFFFFFFFFOOF
FFD00000	FFFFFFFFFFOFO
FFD80000	FFFFFFFFFFOFF
FFE00000	FFFFFFFFFFFOO
FFE80000	FFFFFFFFFFFOF
FFF00000	FFFFFFFFFFFFO
FFF80000	FFFFFFFFFFFFF

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345

ADDRESS	SWCH. SETTING (SW3) (SW2) 1234567812345

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APPENDIX E
COMMUNICATIONS BUFFER BASE ADDRESS JUMPER SETTINGS

The following table shows all possible settings for jumpers JP1-JP13 as discussed in Chapter 2, these jumpers are used to set the base address of the Eagle's 512 Kbyte communications buffer. For each entry in the table below, the first eight jumper settings respectively correspond to jumpers JP1 through JP8. The last five jumper settings respectively correspond to jumpers JP9 through JP13.

Note that "O" stands for a jumper setting of ON (closed), and "F" stands for a setting of OFF (open). An OFF jumper has a value of "1" and an ON switch has a value of "0".

ADDRESS	JUMPER SETTING (JP# 1111 / 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 / 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 / 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 / 1234567890123)
00000000	OOOOOOOOOOOOO	01B00000	OOOOOOOFFOFFO	03600000	OOOOOOFFOFFOO	05100000	OOOOOFOFOOOFO
00080000	OOOOOOOOOOOOF	01B80000	OOOOOOOFFOFFF	03680000	OOOOOOFFOFFOF	05180000	OOOOOFOFOOOFF
00100000	OOOOOOOOOOOFO	01C00000	OOOOOOOFFFOOO	03700000	OOOOOOFFOFFFO	05200000	OOOOOFOFOOFOO
00180000	OOOOOOOOOOOFF	01C80000	OOOOOOOFFFOOF	03780000	OOOOOOFFOFFFF	05280000	OOOOOFOFOOFOF
00200000	OOOOOOOOOOFOO	01D00000	OOOOOOOFFFOFO	03800000	OOOOOOFFFOOOO	05300000	OOOOOFOFOOFFO
00280000	OOOOOOOOOOFOF	01D80000	OOOOOOOFFFOFF	03880000	OOOOOOFFFOOOF	05380000	OOOOOFOFOOFFF
00300000	OOOOOOOOOOFFO	01E00000	OOOOOOOFFFFOO	03900000	OOOOOOFFFOOFO	05400000	OOOOOFOFOFOOO
00380000	OOOOOOOOOOFFF	01E80000	OOOOOOOFFFFOF	03980000	OOOOOOFFFOOFF	05480000	OOOOOFOFOFOOF
00400000	OOOOOOOOOFOOO	01F00000	OOOOOOOFFFFFO	03A00000	OOOOOOFFFOFOO	05500000	OOOOOFOFOFOFO
00480000	OOOOOOOOOFOOF	01F80000	OOOOOOOFFFFFF	03A80000	OOOOOOFFFOFOF	05580000	OOOOOFOFOFOFF
00500000	OOOOOOOOOFOFO	02000000	OOOOOOFOOOOOO	03B00000	OOOOOOFFFOFFO	05600000	OOOOOFOFOFFOO
00580000	OOOOOOOOOFOFF	02080000	OOOOOOFOOOOOF	03B80000	OOOOOOFFFOFFF	05680000	OOOOOFOFOFFOF
00600000	OOOOOOOOOFFOO	02100000	OOOOOOFOOOOFO	03C00000	OOOOOOFFFFOOO	05700000	OOOOOFOFOFFFO
00680000	OOOOOOOOOFFOF	02180000	OOOOOOFOOOOFF	03C80000	OOOOOOFFFFOOF	05780000	OOOOOFOFOFFFF
00700000	OOOOOOOOOFFFO	02200000	OOOOOOFOOOFOO	03D00000	OOOOOOFFFFOFO	05800000	OOOOOFOFFOOOO
00780000	OOOOOOOOOFFFF	02280000	OOOOOOFOOOFOF	03D80000	OOOOOOFFFFOFF	05880000	OOOOOFOFFOOOF
00800000	OOOOOOOOFOOOO	02300000	OOOOOOFOOOFFO	03E00000	OOOOOOFFFFFOO	05900000	OOOOOFOFFOOFO
00880000	OOOOOOOOFOOOF	02380000	OOOOOOFOOOFFF	03E80000	OOOOOOFFFFFOF	05980000	OOOOOFOFFOOFF
00900000	OOOOOOOOFOOFO	02400000	OOOOOOFOOFOOO	03F00000	OOOOOOFFFFFFO	05A00000	OOOOOFOFFOFOO
00980000	OOOOOOOOFOOFF	02480000	OOOOOOFOOFOOF	03F80000	OOOOOOFFFFFFF	05A80000	OOOOOFOFFOFOF
00A00000	OOOOOOOOFOFOO	02500000	OOOOOOFOOFOFO	04000000	OOOOOFOOOOOOO	05B00000	OOOOOFOFFOFFO
00A80000	OOOOOOOOFOFOF	02580000	OOOOOOFOOFOFF	04080000	OOOOOFOOOOOOF	05B80000	OOOOOFOFFOFFF
00B00000	OOOOOOOOFOFFO	02600000	OOOOOOFOOFFOO	04100000	OOOOOFOOOOOFO	05C00000	OOOOOFOFFFOOO
00B80000	OOOOOOOOFOFFF	02680000	OOOOOOFOOFFOF	04180000	OOOOOFOOOOOFF	05C80000	OOOOOFOFFFOOF
00C00000	OOOOOOOOFFOOO	02700000	OOOOOOFOOFFFO	04200000	OOOOOFOOOOFOO	05D00000	OOOOOFOFFFOFO
00C80000	OOOOOOOOFFOOF	02780000	OOOOOOFOOFFFF	04280000	OOOOOFOOOOFOF	05D80000	OOOOOFOFFFOFF
00D00000	OOOOOOOOFFOFO	02800000	OOOOOOFOFOOOO	04300000	OOOOOFOOOOFFO	05E00000	OOOOOFOFFFFOO
00D80000	OOOOOOOOFFOFF	02880000	OOOOOOFOFOOOF	04380000	OOOOOFOOOOFFF	05E80000	OOOOOFOFFFFOF
00E00000	OOOOOOOOFFFOO	02900000	OOOOOOFOFOOFO	04400000	OOOOOFOOOFOOO	05F00000	OOOOOFOFFFFFO
00E80000	OOOOOOOOFFFOF	02980000	OOOOOOFOFOOFF	04480000	OOOOOFOOOFOOF	05F80000	OOOOOFOFFFFFF
00F00000	OOOOOOOOFFFFO	02A00000	OOOOOOFOFOFOO	04500000	OOOOOFOOOFOFO	06000000	OOOOOFFOOOOOO
00F80000	OOOOOOOOFFFFF	02A80000	OOOOOOFOFOFOF	04580000	OOOOOFOOOFOFF	06080000	OOOOOFFOOOOOF
01000000	OOOOOOOFOOOOO	02B00000	OOOOOOFOFOFFO	04600000	OOOOOFOOOFFOO	06100000	OOOOOFFOOOOFO
01080000	OOOOOOOFOOOOF	02B80000	OOOOOOFOFOFFF	04680000	OOOOOFOOOFFOF	06180000	OOOOOFFOOOOFF
01100000	OOOOOOOFOOOFO	02C00000	OOOOOOFOFFOOO	04700000	OOOOOFOOOFFFO	06200000	OOOOOFFOOOFOO
01180000	OOOOOOOFOOOFF	02C80000	OOOOOOFOFFOOF	04780000	OOOOOFOOOFFFF	06280000	OOOOOFFOOOFOF
01200000	OOOOOOOFOOFOO	02D00000	OOOOOOFOFFOFO	04800000	OOOOOFOOFOOOO	06300000	OOOOOFFOOOFFO
01280000	OOOOOOOFOOFOF	02D80000	OOOOOOFOFFOFF	04880000	OOOOOFOOFOOOF	06380000	OOOOOFFOOOFFF
01300000	OOOOOOOFOOFFO	02E00000	OOOOOOFOFFFOO	04900000	OOOOOFOOFOOFO	06400000	OOOOOFFOOFOOO
01380000	OOOOOOOFOOFFF	02E80000	OOOOOOFOFFFOF	04980000	OOOOOFOOFOOFF	06480000	OOOOOFFOOFOOF
01400000	OOOOOOOFOFOOO	02F00000	OOOOOOFOFFFFO	04A00000	OOOOOFOOFOFOO	06500000	OOOOOFFOOFOFO
01480000	OOOOOOOFOFOOF	02F80000	OOOOOOFOFFFFF	04A80000	OOOOOFOOFOFOF	06580000	OOOOOFFOOFOFF
01500000	OOOOOOOFOFOFO	03000000	OOOOOOFFOOOOO	04B00000	OOOOOFOOFOFFO	06600000	OOOOOFFOOFFOO
01580000	OOOOOOOFOFOFF	03080000	OOOOOOFFOOOOF	04B80000	OOOOOFOOFOFFF	06680000	OOOOOFFOOFFOF
01600000	OOOOOOOFOFFOO	03100000	OOOOOOFFOOOFO	04C00000	OOOOOFOOFFOOO	06700000	OOOOOFFOOFFFO
01680000	OOOOOOOFOFFOF	03180000	OOOOOOFFOOOFF	04C80000	OOOOOFOOFFOOF	06780000	OOOOOFFOOFFFF
01700000	OOOOOOOFOFFFO	03200000	OOOOOOFFOOFOO	04D00000	OOOOOFOOFFOFO	06800000	OOOOOFFOFOOOO
01780000	OOOOOOOFOFFFF	03280000	OOOOOOFFOOFOF	04D80000	OOOOOFOOFFOFF	06880000	OOOOOFFOFOOOF
01800000	OOOOOOOFFOOOO	03300000	OOOOOOFFOOFFO	04E00000	OOOOOFOOFFFOO	06900000	OOOOOFFOFOOFO
01880000	OOOOOOOFFOOOF	03380000	OOOOOOFFOOFFF	04E80000	OOOOOFOOFFFOF	06980000	OOOOOFFOFOOFF
01900000	OOOOOOOFFOOFO	03400000	OOOOOOFFOFOOO	04F00000	OOOOOFOOFFFFO	06A00000	OOOOOFFOFOFOO
01980000	OOOOOOOFFOOFF	03480000	OOOOOOFFOFOOF	04F80000	OOOOOFOOFFFFF	06A80000	OOOOOFFOFOFOF
01A00000	OOOOOOOFFOFOO	03500000	OOOOOOFFOFOFO	05000000	OOOOOFOFOOOOO	06B00000	OOOOOFFOFOFFO
01A80000	OOOOOOOFFOFOF	03580000	OOOOOOFFOFOFF	05080000	OOOOOFOFOOOOF	06B80000	OOOOOFFOFOFFF

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06C00000	OOOOOFFOFFOOO	09080000	OOOOFOOFOOOOF	0B500000	OOOOFOFFOFOFO	0D980000	OOOOFFOFFOOFF
06C80000	OOOOOFFOFFOOF	09100000	OOOOFOOFOOOFO	0B580000	OOOOFOFFOFOFF	0DA00000	OOOOFFOFFOFOO
06D00000	OOOOOFFOFFOFO	09180000	OOOOFOOFOOOFF	0B600000	OOOOFOFFOFFOO	0DA80000	OOOOFFOFFOFOF
06D80000	OOOOOFFOFFOFF	09200000	OOOOFOOFOOFOO	0B680000	OOOOFOFFOFFOF	0DB00000	OOOOFFOFFOFFO
06E00000	OOOOOFFOFFFOO	09280000	OOOOFOOFOOFOF	0B700000	OOOOFOFFOFFFO	0DB80000	OOOOFFOFFOFFF
06E80000	OOOOOFFOFFFOF	09300000	OOOOFOOFOOFFO	0B780000	OOOOFOFFOFFFF	0DC00000	OOOOFFOFFFOOO
06F00000	OOOOOFFOFFFFO	09380000	OOOOFOOFOOFFF	0B800000	OOOOFOFFFOOOO	0DC80000	OOOOFFOFFFOOF
06F80000	OOOOOFFOFFFFF	09400000	OOOOFOOFOFOOO	0B880000	OOOOFOFFFOOOF	0DD00000	OOOOFFOFFFOFO
07000000	OOOOOFFFOOOOO	09480000	OOOOFOOFOFOOF	0B900000	OOOOFOFFFOOFO	0DD80000	OOOOFFOFFFOFF
07080000	OOOOOFFFOOOOF	09500000	OOOOFOOFOFOFO	0B980000	OOOOFOFFFOOFF	0DE00000	OOOOFFOFFFFOO
07100000	OOOOOFFFOOOFO	09580000	OOOOFOOFOFOFF	0BA00000	OOOOFOFFFOFOO	0DE80000	OOOOFFOFFFFOF
07180000	OOOOOFFFOOOFF	09600000	OOOOFOOFOFFOO	0BA80000	OOOOFOFFFOFOF	0DF00000	OOOOFFOFFFFFO
07200000	OOOOOFFFOOFOO	09680000	OOOOFOOFOFFOF	0BB00000	OOOOFOFFFOFFO	0DF80000	OOOOFFOFFFFFF
07280000	OOOOOFFFOOFOF	09700000	OOOOFOOFOFFFO	0BB80000	OOOOFOFFFOFFF	0E000000	OOOOFFFOOOOOO
07300000	OOOOOFFFOOFFO	09780000	OOOOFOOFOFFFF	0BC00000	OOOOFOFFFFOOO	0E080000	OOOOFFFOOOOOF
07380000	OOOOOFFFOOFFF	09800000	OOOOFOOFFOOOO	0BC80000	OOOOFOFFFFOOF	0E100000	OOOOFFFOOOOFO
07400000	OOOOOFFFOFOOO	09880000	OOOOFOOFFOOOF	0BD00000	OOOOFOFFFFOFO	0E180000	OOOOFFFOOOOFF
07480000	OOOOOFFFOFOOF	09900000	OOOOFOOFFOOFO	0BD80000	OOOOFOFFFFOFF	0E200000	OOOOFFFOOOFOO
07500000	OOOOOFFFOFOFO	09980000	OOOOFOOFFOOFF	0BE00000	OOOOFOFFFFFOO	0E280000	OOOOFFFOOOFOF
07580000	OOOOOFFFOFOFF	09A00000	OOOOFOOFFOFOO	0BE80000	OOOOFOFFFFFOF	0E300000	OOOOFFFOOOFFO
07600000	OOOOOFFFOFFOO	09A80000	OOOOFOOFFOFOF	0BF00000	OOOOFOFFFFFFO	0E380000	OOOOFFFOOOFFF
07680000	OOOOOFFFOFFOF	09B00000	OOOOFOOFFOFFO	0BF80000	OOOOFOFFFFFFF	0E400000	OOOOFFFOOOFOO
07700000	OOOOOFFFOFFFO	09B80000	OOOOFOOFFOFFF	0C000000	OOOOFFOOOOOOO	0E480000	OOOOFFFOOOFOF
07780000	OOOOOFFFOFFFF	09C00000	OOOOFOOFFFOOO	0C080000	OOOOFFOOOOOOF	0E500000	OOOOFFFOOOFFO
07800000	OOOOOFFFFOOOO	09C80000	OOOOFOOFFFOOF	0C100000	OOOOFFOOOOOFO	0E580000	OOOOFFFOOOFFF
07880000	OOOOOFFFFOOOF	09D00000	OOOOFOOFFFOFO	0C180000	OOOOFFOOOOOFF	0E600000	OOOOFFFOOFFOO
07900000	OOOOOFFFFOOFO	09D80000	OOOOFOOFFFOFF	0C200000	OOOOFFOOOOFOO	0E680000	OOOOFFFOOFFOF
07980000	OOOOOFFFFOOFF	09E00000	OOOOFOOFFFFOO	0C280000	OOOOFFOOOOFOF	0E700000	OOOOFFFOOFFFO
07A00000	OOOOOFFFFOFOO	09E80000	OOOOFOOFFFFOF	0C300000	OOOOFFOOOOFFO	0E780000	OOOOFFFOOFFFF
07A80000	OOOOOFFFFOFOF	09F00000	OOOOFOOFFFFFO	0C380000	OOOOFFOOOOFFF	0E800000	OOOOFFFOOOOOO
07B00000	OOOOOFFFFOFFO	09F80000	OOOOFOOFFFFFF	0C400000	OOOOFFOOOFOOO	0E880000	OOOOFFFOFOOOF
07B80000	OOOOOFFFFOFFF	0A000000	OOOOFOFOOOOOO	0C480000	OOOOFFOOOFOOF	0E900000	OOOOFFFOFOOFO
07C00000	OOOOOFFFFFOOO	0A080000	OOOOFOFOOOOOF	0C500000	OOOOFFOOOFOFO	0E980000	OOOOFFFOFOOFF
07C80000	OOOOOFFFFFOOF	0A100000	OOOOFOFOOOOFO	0C580000	OOOOFFOOOFOFF	0EA00000	OOOOFFFOOFOOO
07D00000	OOOOOFFFFFOFO	0A180000	OOOOFOFOOOOFF	0C600000	OOOOFFOOOFFOO	0EA80000	OOOOFFFOFOFOF
07D80000	OOOOOFFFFFOFF	0A200000	OOOOFOFOOOFOO	0C680000	OOOOFFOOOFFOF	0EB00000	OOOOFFFOFOFFO
07E00000	OOOOOFFFFFFOO	0A280000	OOOOFOFOOOFOF	0C700000	OOOOFFOOOFFFO	0EB80000	OOOOFFFOFOFFF
07E80000	OOOOOFFFFFFOF	0A300000	OOOOFOFOOOFFO	0C780000	OOOOFFOOOFFFF	0EC00000	OOOOFFFOFFOOO
07F00000	OOOOOFFFFFFFO	0A380000	OOOOFOFOOOFFF	0C800000	OOOOFFOOFOOOO	0EC80000	OOOOFFFOFFOOF
07F80000	OOOOOFFFFFFFF	0A400000	OOOOFOFOOFOOO	0C880000	OOOOFFOOFOOOF	0ED00000	OOOOFFFOFFOFO
08000000	OOOOFOOOOOOOO	0A480000	OOOOFOFOOFOOF	0C900000	OOOOFFOOFOOFO	0ED80000	OOOOFFFOFFOFF
08080000	OOOOFOOOOOOOF	0A500000	OOOOFOFOOFOFO	0C980000	OOOOFFOOFOOFF	0EE00000	OOOOFFFOFFFOO
08100000	OOOOFOOOOOOFO	0A580000	OOOOFOFOOFOFF	0CA00000	OOOOFFOOFOFOO	0EE80000	OOOOFFFOFFFOF
08180000	OOOOFOOOOOOFF	0A600000	OOOOFOFOOFFOO	0CA80000	OOOOFFOOFOFOF	0EF00000	OOOOFFFOFFFFO
08200000	OOOOFOOOOOFOO	0A680000	OOOOFOFOOFFOF	0CB00000	OOOOFFOOFOFFO	0EF80000	OOOOFFFOFFFFF
08280000	OOOOFOOOOOFOF	0A700000	OOOOFOFOOFFFO	0CB80000	OOOOFFOOFOFFF	0F000000	OOOOFFFFOOOOO
08300000	OOOOFOOOOOFFO	0A780000	OOOOFOFOOFFFF	0CC00000	OOOOFFOOFFOOO	0F080000	OOOOFFFFOOOOF
08380000	OOOOFOOOOOFFF	0A800000	OOOOFOFOFOOOO	0CC80000	OOOOFFOOFFOOF	0F100000	OOOOFFFFOOOFO
08400000	OOOOFOOOOFOOO	0A880000	OOOOFOFOFOOOF	0CD00000	OOOOFFOOFFOFO	0F180000	OOOOFFFFOOOFF
08480000	OOOOFOOOOFOOF	0A900000	OOOOFOFOFOOFO	0CD80000	OOOOFFOOFFOFF	0F200000	OOOOFFFFOOFOO
08500000	OOOOFOOOOFOFO	0A980000	OOOOFOFOFOOFF	0CE00000	OOOOFFOOFFFOO	0F280000	OOOOFFFFOOFOF
08580000	OOOOFOOOOFOFF	0AA00000	OOOOFOFOFOFOO	0CE80000	OOOOFFOOFFFOF	0F300000	OOOOFFFFOOFFO
08600000	OOOOFOOOOFFOO	0AA80000	OOOOFOFOFOFOF	0CF00000	OOOOFFOOFFFFO	0F380000	OOOOFFFFOOFFF
08680000	OOOOFOOOOFFOF	0AB00000	OOOOFOFOFOFFO	0CF80000	OOOOFFOOFFFFF	0F400000	OOOOFFFFOFOOO
08700000	OOOOFOOOOFFFO	0AB80000	OOOOFOFOFOFFF	0D000000	OOOOFFOFOOOOO	0F480000	OOOOFFFFOFOOF
08780000	OOOOFOOOOFFFF	0AC00000	OOOOFOFOFFOOO	0D080000	OOOOFFOFOOOOF	0F500000	OOOOFFFFOFOFO
08800000	OOOOFOOOFOOOO	0AC80000	OOOOFOFOFFOOF	0D100000	OOOOFFOFOOOFO	0F580000	OOOOFFFFOFOFF
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08900000	OOOOFOOOFOOFO	0AD80000	OOOOFOFOFFOFF	0D200000	OOOOFFOFOOFOO	0F680000	OOOOFFFFOFFOF
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08A00000	OOOOFOOOFOFOO	0AE80000	OOOOFOFOFFFOF	0D300000	OOOOFFOFOOFFO	0F780000	OOOOFFFFOFFFF
08A80000	OOOOFOOOFOFOF	0AF00000	OOOOFOFOFFFFO	0D380000	OOOOFFOFOOFFF	0F800000	OOOOFFFFFOOOO
08B00000	OOOOFOOOFOFFO	0AF80000	OOOOFOFOFFFFF	0D400000	OOOOFFOFOFOOO	0F880000	OOOOFFFFFOOOF
08B80000	OOOOFOOOFOFFF	0B000000	OOOOFOFFOOOOO	0D480000	OOOOFFOFOFOOF	0F900000	OOOOFFFFFOOFO
08C00000	OOOOFOOOFFOOO	0B080000	OOOOFOFFOOOOF	0D500000	OOOOFFOFOFOFO	0F980000	OOOOFFFFFOOFF
08C80000	OOOOFOOOFFOOF	0B100000	OOOOFOFFOOOFO	0D580000	OOOOFFOFOFOFF	0FA00000	OOOOFFFFFOFOO
08D00000	OOOOFOOOFFOFO	0B180000	OOOOFOFFOOOFF	0D600000	OOOOFFOFOFFOO	0FA80000	OOOOFFFFFOFOF
08D80000	OOOOFOOOFFOFF	0B200000	OOOOFOFFOOFOO	0D680000	OOOOFFOFOFFOF	0FB00000	OOOOFFFFFOFFO
08E00000	OOOOFOOOFFFOO	0B280000	OOOOFOFFOOFOF	0D700000	OOOOFFOFOFFFO	0FB80000	OOOOFFFFFOFFF
08E80000	OOOOFOOOFFFOF	0B300000	OOOOFOFFOOFFO	0D780000	OOOOFFOFOFFFF	0FC00000	OOOOFFFFFFOOO
08F00000	OOOOFOOOFFFFO	0B380000	OOOOFOFFOOFFF	0D800000	OOOOFFOFFOOOO	0FC80000	OOOOFFFFFFOOF
08F80000	OOOOFOOOFFFFF	0B400000	OOOOFOFFOFOOO	0D880000	OOOOFFOFFOOOF	0FD00000	OOOOFFFFFFOFO
09000000	OOOOFOOFOOOOO	0B480000	OOOOFOFFOFOOF	0D900000	OOOOFFOFFOOFO	0FD80000	OOOOFFFFFFOFF

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ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)
0FE00000	OOOOFFFFFFFOO
0FE80000	OOOOFFFFFFFOF
0FF00000	OOOOFFFFFFFFO
0FF80000	OOOOFFFFFFFFF
10000000	OOOFOOOOOOOOO
10080000	OOOFOOOOOOOOF
10100000	OOOFOOOOOOOFO
10180000	OOOFOOOOOOOFF
10200000	OOOFOOOOOOFOO
10280000	OOOFOOOOOOFOF
10300000	OOOFOOOOOOFFO
10380000	OOOFOOOOOOFFF
10400000	OOOFOOOOOFOOO
10480000	OOOFOOOOOFOOF
10500000	OOOFOOOOOFOFO
10580000	OOOFOOOOOFOFF
10600000	OOOFOOOOOFFOO
10680000	OOOFOOOOOFFOF
10700000	OOOFOOOOOFFFO
10780000	OOOFOOOOOFFFF
10800000	OOOFOOOOFOOOO
10880000	OOOFOOOOFOOOF
10900000	OOOFOOOOFOOFO
10980000	OOOFOOOOFOOFF
10A00000	OOOFOOOOFOFOO
10A80000	OOOFOOOOFOFOF
10B00000	OOOFOOOOFOFFO
10B80000	OOOFOOOOFOFFF
10C00000	OOOFOOOOFFOOO
10C80000	OOOFOOOOFFOOF
10D00000	OOOFOOOOFFOFO
10D80000	OOOFOOOOFFOFF
10E00000	OOOFOOOOFFFOO
10E80000	OOOFOOOOFFFOF
10F00000	OOOFOOOOFFFFO
10F80000	OOOFOOOOFFFFF
11000000	OOOFOOOFOOOOO
11080000	OOOFOOOFOOOOF
11100000	OOOFOOOFOOOFO
11180000	OOOFOOOFOOOFF
11200000	OOOFOOOFOOFOO
11280000	OOOFOOOFOOFOF
11300000	OOOFOOOFOOFFO
11380000	OOOFOOOFOOFFF
11400000	OOOFOOOFOFOOO
11480000	OOOFOOOFOFOOF
11500000	OOOFOOOFOFOFO
11580000	OOOFOOOFOFOFF
11600000	OOOFOOOFOFFOO
11680000	OOOFOOOFOFFOF
11700000	OOOFOOOFOFFFO
11780000	OOOFOOOFOFFFF
11800000	OOOFOOOFFOOOO
11880000	OOOFOOOFFOOOF
11900000	OOOFOOOFFOOFO
11980000	OOOFOOOFFOOFF
11A00000	OOOFOOOFFOFOO
11A80000	OOOFOOOFFOFOF
11B00000	OOOFOOOFFOFFO
11B80000	OOOFOOOFFOFFF
11C00000	OOOFOOOFFFOOO
11C80000	OOOFOOOFFFOOF
11D00000	OOOFOOOFFFOFO
11D80000	OOOFOOOFFFOFF
11E00000	OOOFOOOFFFFOO
11E80000	OOOFOOOFFFFOF
11F00000	OOOFOOOFFFFFO
11F80000	OOOFOOOFFFFFF
12000000	OOOFOOFOOOOOO
12080000	OOOFOOFOOOOOF
12100000	OOOFOOFOOOOFO
12180000	OOOFOOFOOOOFF
12200000	OOOFOOFOOOFOO
12280000	OOOFOOFOOOFOF
12300000	OOOFOOFOOOFFO
12380000	OOOFOOFOOOFFF
12400000	OOOFOOFOOFOOO
12480000	OOOFOOFOOFOOF
12500000	OOOFOOFOOFOFO
12580000	OOOFOOFOOFOFF
12600000	OOOFOOFOOFFOO
12680000	OOOFOOFOOFFOF
12700000	OOOFOOFOOFFFO
12780000	OOOFOOFOOFFFF
12800000	OOOFOOFOFOOOO
12880000	OOOFOOFOFOOOF
12900000	OOOFOOFOFOOFO
12980000	OOOFOOFOFOOFF
12A00000	OOOFOOFOFOFOO
12A80000	OOOFOOFOFOFOF
12B00000	OOOFOOFOFOFFO
12B80000	OOOFOOFOFOFFF
12C00000	OOOFOOFOFFOOO
12C80000	OOOFOOFOFFOOF
12D00000	OOOFOOFOFFOFO
12D80000	OOOFOOFOFFOFF
12E00000	OOOFOOFOFFFOO
12E80000	OOOFOOFOFFFOF
12F00000	OOOFOOFOFFFFO
12F80000	OOOFOOFOFFFFF
13000000	OOOFOOFFOOOOO
13080000	OOOFOOFFOOOOF
13100000	OOOFOOFFOOOFO
13180000	OOOFOOFFOOOFF
13200000	OOOFOOFFOOFOO
13280000	OOOFOOFFOOFOF
13300000	OOOFOOFFOOFFO
13380000	OOOFOOFFOOFFF
13400000	OOOFOOFFOFOOO
13480000	OOOFOOFFOFOOF
13500000	OOOFOOFFOFOFO
13580000	OOOFOOFFOFOFF
13600000	OOOFOOFFOFFOO
13680000	OOOFOOFFOFFOF
13700000	OOOFOOFFOFFFO
13780000	OOOFOOFFOFFFF
13800000	OOOFOOFFFOOOO
13880000	OOOFOOFFFOOOF
13900000	OOOFOOFFFOOFO
13980000	OOOFOOFFFOOFF
13A00000	OOOFOOFFFOFOO
13A80000	OOOFOOFFFOFOF
13B00000	OOOFOOFFFOFFO
13B80000	OOOFOOFFFOFFF
13C00000	OOOFOOFFFFOOO
13C80000	OOOFOOFFFFOOF
13D00000	OOOFOOFFFFOFO
13D80000	OOOFOOFFFFOFF
13E00000	OOOFOOFFFFFOO
13E80000	OOOFOOFFFFFOF
13F00000	OOOFOOFFFFFFO
13F80000	OOOFOOFFFFFFF
14000000	OOOFOFOOOOOOO
14080000	OOOFOFOOOOOOF
14100000	OOOFOFOOOOOFO
14180000	OOOFOFOOOOOFF
14200000	OOOFOFOOOOFOO
14280000	OOOFOFOOOOFOF
14300000	OOOFOFOOOOFFO
14380000	OOOFOFOOOOFFF
14400000	OOOFOFOOOFOOO
14480000	OOOFOFOOOFOOF
14500000	OOOFOFOOOFOFO
14580000	OOOFOFOOOFOFF
14600000	OOOFOFOOOFFOO
14680000	OOOFOFOOOFFOF
14700000	OOOFOFOOOFFFO
14780000	OOOFOFOOOFFFF
14800000	OOOFOFOOFOOOO
14880000	OOOFOFOOFOOOF
14900000	OOOFOFOOFOOFO
14980000	OOOFOFOOFOOFF
14A00000	OOOFOFOOFOFOO
14A80000	OOOFOFOOFOFOF
14B00000	OOOFOFOOFOFFO
14B80000	OOOFOFOOFOFFF
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14C80000	OOOFOFOOFFOOF
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14E80000	OOOFOFOOFFFOF
14F00000	OOOFOFOOFFFFO
14F80000	OOOFOFOOFFFFF
15000000	OOOFOFOFOOOOO
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15100000	OOOFOFOFOOOFO
15180000	OOOFOFOFOOOFF
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15280000	OOOFOFOFOOFOF
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16780000	OOOFOFFOOFFFF
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17680000	OOOFOFFFOFFOF
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18600000	OOOFFOOOOFFOO
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18C00000	OOOFFOOOFFOOO
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18E00000	OOOFFOOOFFFOO
18E80000	OOOFFOOOFFFOF
18F00000	OOOFFOOOFFFFO
18F80000	OOOFFOOOFFFFF

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JP#	1111 1234567890123	JP#	1111 1234567890123	JP#	1111 1234567890123	JP#	1111 1234567890123
19000000	OOOFFOOFOOOOO	1B480000	OOOFFOFFOFOOF	1D900000	OOOFFFOFFOOFO	1FD80000	OOOFFFFFFFOFF
19080000	OOOFFOOFOOOOF	1B500000	OOOFFOFFOFOFO	1D980000	OOOFFFOFFOOFF	1FE00000	OOOFFFFFFFFOO
19100000	OOOFFOOFOOOFO	1B580000	OOOFFOFFOFOFF	1DA00000	OOOFFFOFFOFOO	1FE80000	OOOFFFFFFFFOF
19180000	OOOFFOOFOOOFF	1B600000	OOOFFOFFOFFOO	1DA80000	OOOFFFOFFOFOF	1FF00000	OOOFFFFFFFFFO
19200000	OOOFFOOFOOFOO	1B680000	OOOFFOFFOFFOF	1DB00000	OOOFFFOFFOFFO	1FF80000	OOOFFFFFFFFFF
19280000	OOOFFOOFOOFOF	1B700000	OOOFFOFFOFFFO	1DB80000	OOOFFFOFFOFFF	20000000	OOFOOOOOOOOOO
19300000	OOOFFOOFOOFFO	1B780000	OOOFFOFFOFFFF	1DC00000	OOOFFFOFFFOOO	20080000	OOFOOOOOOOOOF
19380000	OOOFFOOFOOFFF	1B800000	OOOFFOFFFOOOO	1DC80000	OOOFFFOFFFOOF	20100000	OOFOOOOOOOOFO
19400000	OOOFFOOFOFOOO	1B880000	OOOFFOFFFOOOF	1DD00000	OOOFFFOFFFOFO	20180000	OOFOOOOOOOOFF
19480000	OOOFFOOFOFOOF	1B900000	OOOFFOFFFOOFO	1DD80000	OOOFFFOFFFOFF	20200000	OOFOOOOOOOFOO
19500000	OOOFFOOFOFOFO	1B980000	OOOFFOFFFOOFF	1DE00000	OOOFFFOFFFFOO	20280000	OOFOOOOOOOFOF
19580000	OOOFFOOFOFOFF	1BA00000	OOOFFOFFFOFOO	1DE80000	OOOFFFOFFFFOF	20300000	OOFOOOOOOOFFO
19600000	OOOFFOOFOFFOO	1BA80000	OOOFFOFFFOFOF	1DF00000	OOOFFFOFFFFFO	20380000	OOFOOOOOOOFFF
19680000	OOOFFOOFOFFOF	1BB00000	OOOFFOFFFOFFO	1DF80000	OOOFFFOFFFFFF	20400000	OOFOOOOOOFOOO
19700000	OOOFFOOFOFFFO	1BB80000	OOOFFOFFFOFFF	1E000000	OOOFFFFOOOOOO	20480000	OOFOOOOOOFOOF
19780000	OOOFFOOFOFFFF	1BC00000	OOOFFOFFFFOOO	1E080000	OOOFFFFOOOOOF	20500000	OOFOOOOOOFOFO
19800000	OOOFFOOFFOOOO	1BC80000	OOOFFOFFFFOOF	1E100000	OOOFFFFOOOOFO	20580000	OOFOOOOOOFOFF
19880000	OOOFFOOFFOOOF	1BD00000	OOOFFOFFFFOFO	1E180000	OOOFFFFOOOOFF	20600000	OOFOOOOOOFFOO
19900000	OOOFFOOFFOOFO	1BD80000	OOOFFOFFFFOFF	1E200000	OOOFFFFOOOFOO	20680000	OOFOOOOOOFFOF
19980000	OOOFFOOFFOOFF	1BE00000	OOOFFOFFFFFOO	1E280000	OOOFFFFOOOFOF	20700000	OOFOOOOOOFFFO
19A00000	OOOFFOOFFOFOO	1BE80000	OOOFFOFFFFFOF	1E300000	OOOFFFFOOOFFO	20780000	OOFOOOOOOFFFF
19A80000	OOOFFOOFFOFOF	1BF00000	OOOFFOFFFFFFO	1E380000	OOOFFFFOOOFFF	20800000	OOFOOOOOFOOOO
19B00000	OOOFFOOFFOFFO	1BF80000	OOOFFOFFFFFFF	1E400000	OOOFFFFOOFOOO	20880000	OOFOOOOOFOOOF
19B80000	OOOFFOOFFOFFF	1C000000	OOOFFFOOOOOOO	1E480000	OOOFFFFOOFOOF	20900000	OOFOOOOOFOOFO
19C00000	OOOFFOOFFFOOO	1C080000	OOOFFFOOOOOOF	1E500000	OOOFFFFOOFOFO	20980000	OOFOOOOOFOOFF
19C80000	OOOFFOOFFFOOF	1C100000	OOOFFFOOOOOFO	1E580000	OOOFFFFOOFOFF	20A00000	OOFOOOOOFOFOO
19D00000	OOOFFOOFFFOFO	1C180000	OOOFFFOOOOOFF	1E600000	OOOFFFFOOFFOO	20A80000	OOFOOOOOFOFOF
19D80000	OOOFFOOFFFOFF	1C200000	OOOFFFOOOOFOO	1E680000	OOOFFFFOOFFOF	20B00000	OOFOOOOOFOFFO
19E00000	OOOFFOOFFFFOO	1C280000	OOOFFFOOOOFOF	1E700000	OOOFFFFOOFFFO	20B80000	OOFOOOOOFOFFF
19E80000	OOOFFOOFFFFOF	1C300000	OOOFFFOOOOFFO	1E780000	OOOFFFFOOFFFF	20C00000	OOFOOOOOFFOOO
19F00000	OOOFFOOFFFFFO	1C380000	OOOFFFOOOOFFF	1E800000	OOOFFFFOFOOOO	20C80000	OOFOOOOOFFOOF
19F80000	OOOFFOOFFFFFF	1C400000	OOOFFFOOOFOOO	1E880000	OOOFFFFOFOOOF	20D00000	OOFOOOOOFFOFO
1A000000	OOOFFOFOOOOOO	1C480000	OOOFFFOOOFOOF	1E900000	OOOFFFFOFOOFO	20D80000	OOFOOOOOFFOFF
1A080000	OOOFFOFOOOOOF	1C500000	OOOFFFOOOFOFO	1E980000	OOOFFFFOFOOFF	20E00000	OOFOOOOOFFFOO
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1A180000	OOOFFOFOOOOFF	1C600000	OOOFFFOOOFFOO	1EA80000	OOOFFFFOFOFOF	20F00000	OOFOOOOOFFFFO
1A200000	OOOFFOFOOOFOO	1C680000	OOOFFFOOOFFOF	1EB00000	OOOFFFFOFOFFO	20F80000	OOFOOOOOFFFFF
1A280000	OOOFFOFOOOFOF	1C700000	OOOFFFOOOFFFO	1EB80000	OOOFFFFOFOFFF	21000000	OOFOOOOFOOOOO
1A300000	OOOFFOFOOOFFO	1C780000	OOOFFFOOOFFFF	1EC00000	OOOFFFFOFFOOO	21080000	OOFOOOOFOOOOF
1A380000	OOOFFOFOOOFFF	1C800000	OOOFFFOOFOOOO	1EC80000	OOOFFFFOFFOOF	21100000	OOFOOOOFOOOFO
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1A480000	OOOFFOFOOFOOF	1C900000	OOOFFFOOFOOFO	1ED80000	OOOFFFFOFFOFF	21200000	OOFOOOOFOOFOO
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1A980000	OOOFFOFOFOOFF	1CE00000	OOOFFFOOFFFOO	1F280000	OOOFFFFOFOOFO	21700000	OOFOOOOFOFFFO
1AA00000	OOOFFOFOFOFOO	1CE80000	OOOFFFOOFFFOF	1F300000	OOOFFFFOFOOFF	21780000	OOFOOOOFOFFFF
1AA80000	OOOFFOFOFOFOF	1CF00000	OOOFFFOOFFFFO	1F380000	OOOFFFFOFOOFF	21800000	OOFOOOOFFOOOO
1AB00000	OOOFFOFOFOFFO	1CF80000	OOOFFFOOFFFFF	1F400000	OOOFFFFOFOOOO	21880000	OOFOOOOFFOOOF
1AB80000	OOOFFOFOFOFFF	1D000000	OOOFFFOFOOOOO	1F480000	OOOFFFFOFOOOF	21900000	OOFOOOOFFOOFO
1AC00000	OOOFFOFOFFOOO	1D080000	OOOFFFOFOOOOF	1F500000	OOOFFFFOFOFO	21980000	OOFOOOOFFOOFF
1AC80000	OOOFFOFOFFOOF	1D100000	OOOFFFOFOOOFO	1F580000	OOOFFFFOFOFF	21A00000	OOFOOOOFFOFOO
1AD00000	OOOFFOFOFFOFO	1D180000	OOOFFFOFOOOFF	1F600000	OOOFFFFOFFOO	21A80000	OOFOOOOFFOFOF
1AD80000	OOOFFOFOFFOFF	1D200000	OOOFFFOFOOFOO	1F680000	OOOFFFFOFFOF	21B00000	OOFOOOOFFOFFO
1AE00000	OOOFFOFOFFFOO	1D280000	OOOFFFOFOOFOF	1F700000	OOOFFFFOFFFO	21B80000	OOFOOOOFFOFFF
1AE80000	OOOFFOFOFFFOF	1D300000	OOOFFFOFOOFFO	1F780000	OOOFFFFOFFFF	21C00000	OOFOOOOFFFOOO
1AF00000	OOOFFOFOFFFFO	1D380000	OOOFFFOFOOFFF	1F800000	OOOFFFFFFOOOO	21C80000	OOFOOOOFFFOOF
1AF80000	OOOFFOFOFFFFF	1D400000	OOOFFFOFOFOOO	1F880000	OOOFFFFFFOOOF	21D00000	OOFOOOOFFFOFO
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1B280000	OOOFFOFFOOFOF	1D700000	OOOFFFOFOFFFO	1FB80000	OOOFFFFFFOFFF	22000000	OOFOOOFOOOOOO
1B300000	OOOFFOFFOOFFO	1D780000	OOOFFFOFOFFFF	1FC00000	OOOFFFFFFFOOO	22080000	OOFOOOFOOOOOF
1B380000	OOOFFOFFOOFFF	1D800000	OOOFFFOFFOOOO	1FC80000	OOOFFFFFFFOOF	22100000	OOFOOOFOOOOFO
1B400000	OOOFFOFFOFOOO	1D880000	OOOFFFOFFOOOF	1FD00000	OOOFFFFFFFOFO	22180000	OOFOOOFOOOOFF

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22200000	OOFOOOFOOOFOO	24680000	OOFOOFOOOFFOF	26B00000	OOFOOFFOFOFFO	28F80000	OOFOFOOOFFFFF
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23780000	OOFOOOFFOFFFF	25C00000	OOFOOFOFFFOOO	28080000	OOFOFOOOOOOOF	2A500000	OOFOFOFOOFOFO
23800000	OOFOOOFFFOOOO	25C80000	OOFOOFOFFFOOF	28100000	OOFOFOOOOOOFO	2A580000	OOFOFOFOOFOFF
23880000	OOFOOOFFFOOOF	25D00000	OOFOOFOFFFOFO	28180000	OOFOFOOOOOOFF	2A600000	OOFOFOFOOFFOO
23900000	OOFOOOFFFOOFO	25D80000	OOFOOFOFFFOFF	28200000	OOFOFOOOOOFOO	2A680000	OOFOFOFOOFFOF
23980000	OOFOOOFFFOOFF	25E00000	OOFOOFOFFFFOO	28280000	OOFOFOOOOOFOF	2A700000	OOFOFOFOOFFFO
23A00000	OOFOOOFFFOFOO	25E80000	OOFOOFOFFFFOF	28300000	OOFOFOOOOOFFO	2A780000	OOFOFOFOOFFFF
23A80000	OOFOOOFFFOFOF	25F00000	OOFOOFOFFFFFO	28380000	OOFOFOOOOOFFF	2A800000	OOFOFOFOFOOOO
23B00000	OOFOOOFFFOFFO	25F80000	OOFOOFOFFFFFF	28400000	OOFOFOOOOFOOO	2A880000	OOFOFOFOFOOOF
23B80000	OOFOOOFFFOFFF	26000000	OOFOOFFOOOOOO	28480000	OOFOFOOOOFOOF	2A900000	OOFOFOFOFOOFO
23C00000	OOFOOOFFFFOOO	26080000	OOFOOFFOOOOOF	28500000	OOFOFOOOOFOFO	2A980000	OOFOFOFOFOOFF
23C80000	OOFOOOFFFFOOF	26100000	OOFOOFFOOOOFO	28580000	OOFOFOOOOFOFF	2AA00000	OOFOFOFOFOFOO
23D00000	OOFOOOFFFFOFO	26180000	OOFOOFFOOOOFF	28600000	OOFOFOOOOFFOO	2AA80000	OOFOFOFOFOFOF
23D80000	OOFOOOFFFFOFF	26200000	OOFOOFFOOOFOO	28680000	OOFOFOOOOFFOF	2AB00000	OOFOFOFOFOFFO
23E00000	OOFOOOFFFFFOO	26280000	OOFOOFFOOOFOF	28700000	OOFOFOOOOFFFO	2AB80000	OOFOFOFOFOFFF
23E80000	OOFOOOFFFFFOF	26300000	OOFOOFFOOOFFO	28780000	OOFOFOOOOFFFF	2AC00000	OOFOFOFOFFOOO
23F00000	OOFOOOFFFFFFO	26380000	OOFOOFFOOOFFF	28800000	OOFOFOOOFOOOO	2AC80000	OOFOFOFOFFOOF
23F80000	OOFOOOFFFFFFF	26400000	OOFOOFFOOFOOO	28880000	OOFOFOOOFOOOF	2AD00000	OOFOFOFOFFOFO
24000000	OOFOOFOOOOOOO	26480000	OOFOOFFOOFOOF	28900000	OOFOFOOOFOOFO	2AD80000	OOFOFOFOFFOFF
24080000	OOFOOFOOOOOOF	26500000	OOFOOFFOOFOFO	28980000	OOFOFOOOFOOFF	2AE00000	OOFOFOFOFFFOO
24100000	OOFOOFOOOOOFO	26580000	OOFOOFFOOFOFF	28A00000	OOFOFOOOFOFOO	2AE80000	OOFOFOFOFFFOF
24180000	OOFOOFOOOOOFF	26600000	OOFOOFFOOFFOO	28A80000	OOFOFOOOFOFOF	2AF00000	OOFOFOFOFFFFO
24200000	OOFOOFOOOOFOO	26680000	OOFOOFFOOFFOF	28B00000	OOFOFOOOFOFFO	2AF80000	OOFOFOFOFFFFF
24280000	OOFOOFOOOOFOF	26700000	OOFOOFFOOFFFO	28B80000	OOFOFOOOFOFFF	2B000000	OOFOFOFFOOOOO
24300000	OOFOOFOOOOFFO	26780000	OOFOOFFOOFFFF	28C00000	OOFOFOOOFFOOO	2B080000	OOFOFOFFOOOOF
24380000	OOFOOFOOOOFFF	26800000	OOFOOFFOFOOOO	28C80000	OOFOFOOOFFOOF	2B100000	OOFOFOFFOOOFO
24400000	OOFOOFOOOFOOO	26880000	OOFOOFFOFOOOF	28D00000	OOFOFOOOFFOFO	2B180000	OOFOFOFFOOOFF
24480000	OOFOOFOOOFOOF	26900000	OOFOOFFOFOOFO	28D80000	OOFOFOOOFFOFF	2B200000	OOFOFOFFOOFOO
24500000	OOFOOFOOOFOFO	26980000	OOFOOFFOFOOFF	28E00000	OOFOFOOOFFFOO	2B280000	OOFOFOFFOOFOF
24580000	OOFOOFOOOFOFF	26A00000	OOFOOFFOFOFOO	28E80000	OOFOFOOOFFFOF	2B300000	OOFOFOFFOOFFO
24600000	OOFOOFOOOFFOO	26A80000	OOFOOFFOFOFOF	28F00000	OOFOFOOOFFFFO	2B380000	OOFOFOFFOOFFF

APPENDIX E
ADDRESS JUMPER SETTINGS

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ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)
2B400000	OOFOFOFFOFOOO	2D880000	OOFOFFOFFOOOF	2FD00000	OOFOFFFFFFOFO	32180000	OOFFOOFOOOOFF
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2B500000	OOFOFOFFOFOFO	2D980000	OOFOFFOFFOOFF	2FE00000	OOFOFFFFFFFOO	32280000	OOFFOOFOOOFOF
2B580000	OOFOFOFFOFOFF	2DA00000	OOFOFFOFFOFOO	2FE80000	OOFOFFFFFFFOF	32300000	OOFFOOFOOOFFO
2B600000	OOFOFOFFOFFOO	2DA80000	OOFOFFOFFOFOF	2FF00000	OOFOFFFFFFFFO	32380000	OOFFOOFOOOFFF
2B680000	OOFOFOFFOFFOF	2DB00000	OOFOFFOFFOFFO	2FF80000	OOFOFFFFFFFFF	32400000	OOFFOOFOOFOOO
2B700000	OOFOFOFFOFFFO	2DB80000	OOFOFFOFFOFFF	30000000	OOFFOOOOOOOOO	32480000	OOFFOOFOOFOOF
2B780000	OOFOFOFFOFFFF	2DC00000	OOFOFFOFFFOOO	30080000	OOFFOOOOOOOOF	32500000	OOFFOOFOOFOFO
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2B880000	OOFOFOFFFOOOF	2DD00000	OOFOFFOFFFOFO	30180000	OOFFOOOOOOOFF	32600000	OOFFOOFOOFFOO
2B900000	OOFOFOFFFOOFO	2DD80000	OOFOFFOFFFOFF	30200000	OOFFOOOOOOFOO	32680000	OOFFOOFOOFFOF
2B980000	OOFOFOFFFOOFF	2DE00000	OOFOFFOFFFFOO	30280000	OOFFOOOOOOFOF	32700000	OOFFOOFOOFFFO
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2BE00000	OOFOFOFFFFFOO	2E280000	OOFOFFFOOOFOF	30700000	OOFFOOOOOFFFO	32B80000	OOFFOOFOFOFFF
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2C080000	OOFOFFOOOOOOF	2E500000	OOFOFFFOOFOFO	30980000	OOFFOOOOFOOFF	32E00000	OOFFOOFOFFFOO
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2C380000	OOFOFFOOOOFFF	2E800000	OOFOFFFOFOOOO	30C80000	OOFFOOOOFFOOF	33100000	OOFFOOFFOOOFO
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2D780000	OOFOFFOFOFFFF	2FC00000	OOFOFFFFFFOOO	32080000	OOFFOOFOOOOOF	34500000	OOFFOFOOOFOFO
2D800000	OOFOFFOFFOOOO	2FC80000	OOFOFFFFFFOOF	32100000	OOFFOOFOOOOFO	34580000	OOFFOFOOOFOFF

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ADDRESS	JUMPER 1111 1234567890123	SETTING
34600000	OOFFOFOOOFFOO	
34680000	OOFFOFOOOFFOF	
34700000	OOFFOFOOOFFFO	
34780000	OOFFOFOOOFFFF	
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34880000	OOFFOFOOFOOOF	
34900000	OOFFOFOOFOOFO	
34980000	OOFFOFOOFOOFF	
34A00000	OOFFOFOOFOFOO	
34A80000	OOFFOFOOFOFOF	
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34B80000	OOFFOFOOFOFFF	
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35480000	OOFFOFOFOFOOF	
35500000	OOFFOFOFOFOFO	
35580000	OOFFOFOFOFOFF	
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36A00000	OOFFOFFOFOFOO	

ADDRESS	JUMPER 1111 1234567890123	SETTING
36A80000	OOFFOFFOFOFOF	
36B00000	OOFFOFFOFOFFO	
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ADDRESS	JUMPER 1111 12345678901234	SETTING
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ADDRESS	JUMPER 1111 1234567890123	SETTING
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3BA80000	OOFFFOFFFOFOF	
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3C180000	OOFFFFOOOOOFF	
3C200000	OOFFFFOOOOFOO	
3C280000	OOFFFFOOOOFOF	
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3D700000	OOFFFFOFOFFFO	
3D780000	OOFFFFOFOFFFF	

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3F680000	OOFFFFFFOFFOF	41B00000	OFOOOOOFFOFFO	43F80000	OFOOOOFFFFFFF	46400000	OFOOOFFOOFOOO
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3FA00000	OOFFFFFFFOFOO	41E80000	OFOOOOOFFFFOF	44300000	OFOOOFOOOOFFO	46780000	OFOOOFFOOFFFF
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3FB00000	OOFFFFFFFOFFO	41F80000	OFOOOOOFFFFFF	44400000	OFOOOFOOOFOOO	46880000	OFOOOFFOFOOOF
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3FC00000	OOFFFFFFFFOOO	42080000	OFOOOOFOOOOOF	44500000	OFOOOFOOOFOFO	46980000	OFOOOFFOFOOFF

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46A00000	OFOOOFFOFOFOO	48E80000	OFOOFOOOFFFOF	4B300000	OFOOFOFFOOFFO	4D780000	OFOOFFOFOFFFF
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JP#	1111 1234567890123	JP#	1111 1234567890123	JP#	1111 1234567890123	JP#	1111 1234567890123
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62580000	OFFOOOFOOFOFF	64A00000	OFFOOFOOFOFOO	66E80000	OFFOOFFOFFFOF	69300000	OFFOFOOFOOFFO
62600000	OFFOOOFOOFFOO	64A80000	OFFOOFOOFOFOF	66F00000	OFFOOFFOFFFFO	69380000	OFFOFOOFOOFFF
62680000	OFFOOOFOOFFOF	64B00000	OFFOOFOOFOFFO	66F80000	OFFOOFFOFFFFF	69400000	OFFOFOOFOFOOO
62700000	OFFOOOFOOFFFO	64B80000	OFFOOFOOFOFFF	67000000	OFFOOFFFOOOOO	69480000	OFFOFOOFOFOOF
62780000	OFFOOOFOOFFFF	64C00000	OFFOOFOOFFOOO	67080000	OFFOOFFFOOOOF	69500000	OFFOFOOFOFOFO
62800000	OFFOOOFOFOOOO	64C80000	OFFOOFOOFFOOF	67100000	OFFOOFFFOOOFO	69580000	OFFOFOOFOFOFF
62880000	OFFOOOFOFOOOF	64D00000	OFFOOFOOFFOFO	67180000	OFFOOFFFOOOFF	69600000	OFFOFOOFOFFOO
62900000	OFFOOOFOFOOFO	64D80000	OFFOOFOOFFOFF	67200000	OFFOOFFFOOFOO	69680000	OFFOFOOFOFFOF
62980000	OFFOOOFOFOOFF	64E00000	OFFOOFOOFFFOO	67280000	OFFOOFFFOOFOF	69700000	OFFOFOOFOFFFO
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62B00000	OFFOOOFOFOFFO	64F80000	OFFOOFOOFFFFF	67400000	OFFOOFFFOFOOO	69880000	OFFOFOOFFOOOF
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62F00000	OFFOOOFOFFFFO	65380000	OFFOOFOFOOFFF	67800000	OFFOOFFFFOOOO	69C80000	OFFOFOOFFFOOF
62F80000	OFFOOOFOFFFFF	65400000	OFFOOFOFOFOOO	67880000	OFFOOFFFFOOOF	69D00000	OFFOFOOFFFOFO
63000000	OFFOOOFFOOOOO	65480000	OFFOOFOFOFOOF	67900000	OFFOOFFFFOOFO	69D80000	OFFOFOOFFFOFF
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63100000	OFFOOOFFOOOFO	65580000	OFFOOFOFOFOFF	67A00000	OFFOOFFFFOFOO	69E80000	OFFOFOOFFFFOF
63180000	OFFOOOFFOOOFF	65600000	OFFOOFOFOFFOO	67A80000	OFFOOFFFFOFOF	69F00000	OFFOFOOFFFFFO
63200000	OFFOOOFFOOFOO	65680000	OFFOOFOFOFFOF	67B00000	OFFOOFFFFOFFO	69F80000	OFFOFOOFFFFFF
63280000	OFFOOOFFOOFOF	65700000	OFFOOFOFOFFFO	67B80000	OFFOOFFFFOFFF	6A000000	OFFOFOFOOOOOO
63300000	OFFOOOFFOOFFO	65780000	OFFOOFOFOFFFF	67C00000	OFFOOFFFFFOOO	6A080000	OFFOFOFOOOOOF
63380000	OFFOOOFFOOFFF	65800000	OFFOOFOFFOOOO	67C80000	OFFOOFFFFFOOF	6A100000	OFFOFOFOOOOFO
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63680000	OFFOOOFFOFFOF	65B00000	OFFOOFOFFOFFO	67F80000	OFFOOFFFFFFFF	6A400000	OFFOFOFOOFOOO
63700000	OFFOOOFFOFFFO	65B80000	OFFOOFOFFOFFF	68000000	OFFOFOOOOOOOO	6A480000	OFFOFOFOOFOOF
63780000	OFFOOOFFOFFFF	65C00000	OFFOOFOFFFOOO	68080000	OFFOFOOOOOOOF	6A500000	OFFOFOFOOFOFO
63800000	OFFOOOFFFOOOO	65C80000	OFFOOFOFFFOOF	68100000	OFFOFOOOOOOFO	6A580000	OFFOFOFOOFOFF
63880000	OFFOOOFFFOOOF	65D00000	OFFOOFOFFFOFO	68180000	OFFOFOOOOOOFF	6A600000	OFFOFOFOOFFOO
63900000	OFFOOOFFFOOFO	65D80000	OFFOOFOFFFOFF	68200000	OFFOFOOOOOFOO	6A680000	OFFOFOFOOFFOF
63980000	OFFOOOFFFOOFF	65E00000	OFFOOFOFFFFOO	68280000	OFFOFOOOOOFOF	6A700000	OFFOFOFOOFFFO
63A00000	OFFOOOFFFOFOO	65E80000	OFFOOFOFFFFOF	68300000	OFFOFOOOOOFFO	6A780000	OFFOFOFOOFFFF
63A80000	OFFOOOFFFOFOF	65F00000	OFFOOFOFFFFFO	68380000	OFFOFOOOOOFFF	6A800000	OFFOFOFOFOOOO
63B00000	OFFOOOFFFOFFO	65F80000	OFFOOFOFFFFFF	68400000	OFFOFOOOOFOOO	6A880000	OFFOFOFOFOOOF
63B80000	OFFOOOFFFOFFF	66000000	OFFOOFFOOOOOO	68480000	OFFOFOOOOFOOF	6A900000	OFFOFOFOFOOFO
63C00000	OFFOOOFFFFOOO	66080000	OFFOOFFOOOOOF	68500000	OFFOFOOOOFOFO	6A980000	OFFOFOFOFOOFF
63C80000	OFFOOOFFFFOOF	66100000	OFFOOFFOOOOFO	68580000	OFFOFOOOOFOFF	6AA00000	OFFOFOFOFOFOO
63D00000	OFFOOOFFFFOFO	66180000	OFFOOFFOOOOFF	68600000	OFFOFOOOOFFOO	6AA80000	OFFOFOFOFOFOF
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63E80000	OFFOOOFFFFFOF	66300000	OFFOOFFOOOFFO	68780000	OFFOFOOOOFFFF	6AC00000	OFFOFOFOFFOOO
63F00000	OFFOOOFFFFFFO	66380000	OFFOOFFOOOFFF	68800000	OFFOFOOOFOOOO	6AC80000	OFFOFOFOFFOOF
63F80000	OFFOOOFFFFFFF	66400000	OFFOOFFOOFOOO	68880000	OFFOFOOOFOOOF	6AD00000	OFFOFOFOFFOFO
64000000	OFFOOFOOOOOOO	66480000	OFFOOFFOOFOOF	68900000	OFFOFOOOFOOFO	6AD80000	OFFOFOFOFFOFF
64080000	OFFOOFOOOOOOF	66500000	OFFOOFFOOFOFO	68980000	OFFOFOOOFOOFF	6AE00000	OFFOFOFOFFFOO
64100000	OFFOOFOOOOOFO	66580000	OFFOOFFOOFOFF	68A00000	OFFOFOOOFOFOO	6AE80000	OFFOFOFOFFFOF
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64300000	OFFOOFOOOOFFO	66780000	OFFOOFFOOFFFF	68C00000	OFFOFOOOFFOOO	6B080000	OFFOFOFFOOOOF
64380000	OFFOOFOOOOFFF	66800000	OFFOOFFOFOOOO	68C80000	OFFOFOOOFFOOF	6B100000	OFFOFOFFOOOFO
64400000	OFFOOFOOOFOOO	66880000	OFFOOFFOFOOOF	68D00000	OFFOFOOOFFOFO	6B180000	OFFOFOFFOOOFF

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6B200000	OFFOFOFFOOFOO	6D680000	OFFOFFOFOFFOF	6FB00000	OFFOFFFFFOFFO	71F80000	OFFFOOOFFFFFF
6B280000	OFFOFOFFOOFOF	6D700000	OFFOFFOFOFFFO	6FB80000	OFFOFFFFFOFFF	72000000	OFFFOOFOOOOOO
6B300000	OFFOFOFFOOFFO	6D780000	OFFOFFOFOFFFF	6FC00000	OFFOFFFFFFOOO	72080000	OFFFOOFOOOOOF
6B380000	OFFOFOFFOOFFF	6D800000	OFFOFFOFFOOOO	6FC80000	OFFOFFFFFFOOF	72100000	OFFFOOFOOOOFO
6B400000	OFFOFOFFOFOOO	6D880000	OFFOFFOFFOOOF	6FD00000	OFFOFFFFFFOFO	72180000	OFFFOOFOOOOFF
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6B600000	OFFOFOFFOFFOO	6DA80000	OFFOFFOFFOFOF	6FF00000	OFFOFFFFFFFFO	72380000	OFFFOOFOOOFFF
6B680000	OFFOFOFFOFFOF	6DB00000	OFFOFFOFFOFFO	6FF80000	OFFOFFFFFFFFF	72400000	OFFFOOFOOFOOO
6B700000	OFFOFOFFOFFFO	6DB80000	OFFOFFOFFOFFF	70000000	OFFFOOOOOOOOO	72480000	OFFFOOFOOFOOF
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6D300000	OFFOFFOFOOFFO	6F780000	OFFOFFFFOFFFF	71C00000	OFFFOOOFFFOOO	74080000	OFFFOFOOOOOOF
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74500000	OFFFOFOOOFOFO	76980000	OFFFOFFOFOOFF	78E00000	OFFFFOOOFFFOO	7B280000	OFFFFOFFOOFOF
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74600000	OFFFOFOOOFFOO	76A80000	OFFFOFFOFOFOF	78F00000	OFFFFOOOFFFFO	7B380000	OFFFFOFFOOFFF
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74700000	OFFFOFOOOFFFO	76B80000	OFFFOFFOFOFFF	79000000	OFFFFOOFOOOOO	7B480000	OFFFFOFFOFOOF
74780000	OFFFOFOOOFFFF	76C00000	OFFFOFFOFFOOO	79080000	OFFFFOOFOOOOF	7B500000	OFFFFOFFOFOFO
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74980000	OFFFOFOOFOOFF	76E00000	OFFFOFFOFFFOO	79280000	OFFFFOOFOOFOF	7B700000	OFFFFOFFOFFFO
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74B80000	OFFFOFOOFOFFF	77000000	OFFFOFFFOOOOO	79480000	OFFFFOOFOFOOF	7B900000	OFFFFOFFFOOFO
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74D80000	OFFFOFOOFFOFF	77200000	OFFFOFFFOOFOO	79680000	OFFFFOOFOFFOF	7BB00000	OFFFFOFFFOFFO
74E00000	OFFFOFOOFFFOO	77280000	OFFFOFFFOOFOF	79700000	OFFFFOOFOFFFO	7BB80000	OFFFFOFFFOFFF
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74F00000	OFFFOFOOFFFFO	77380000	OFFFOFFFOOFFF	79800000	OFFFFOOFFOOOO	7BC80000	OFFFFOFFFFOOF
74F80000	OFFFOFOOFFFFF	77400000	OFFFOFFFOFOOO	79880000	OFFFFOOFFOOOF	7BD00000	OFFFFOFFFFOFO
75000000	OFFFOFOFOOOOO	77480000	OFFFOFFFOFOOF	79900000	OFFFFOOFFOOFO	7BD80000	OFFFFOFFFFOFF
75080000	OFFFOFOFOOOOF	77500000	OFFFOFFFOFOFO	79980000	OFFFFOOFFOOFF	7BE00000	OFFFFOFFFFFOO
75100000	OFFFOFOFOOOFO	77580000	OFFFOFFFOFOFF	79A00000	OFFFFOOFFOFOO	7BE80000	OFFFFOFFFFFOF
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75500000	OFFFOFOFOFOFO	77980000	OFFFOFFFFOOFF	79E00000	OFFFFOOFFFFOO	7C280000	OFFFFFOOOOFOF
75580000	OFFFOFOFOFOFF	77A00000	OFFFOFFFFOFOO	79E80000	OFFFFOOFFFFOF	7C300000	OFFFFFOOOOFFO
75600000	OFFFOFOFOFFOO	77A80000	OFFFOFFFFOFOF	79F00000	OFFFFOOFFFFFO	7C380000	OFFFFFOOOOFFF
75680000	OFFFOFOFOFFOF	77B00000	OFFFOFFFFOFFO	79F80000	OFFFFOOFFFFFF	7C400000	OFFFFFOOOFOOO
75700000	OFFFOFOFOFFFO	77B80000	OFFFOFFFFOFFF	7A000000	OFFFFOFOOOOOO	7C480000	OFFFFFOOOFOOF
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75B00000	OFFFOFOFFOFFO	77F80000	OFFFOFFFFFFFF	7A400000	OFFFFOFOOFOOO	7C880000	OFFFFFOOFOOOF
75B80000	OFFFOFOFFOFFF	78000000	OFFFFOOOOOOOO	7A480000	OFFFFOFOOFOOF	7C900000	OFFFFFOOFOOFO
75C00000	OFFFOFOFFFOOO	78080000	OFFFFOOOOOOOF	7A500000	OFFFFOFOOFOFO	7C980000	OFFFFFOOFOOFF
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75E00000	OFFFOFOFFFFOO	78280000	OFFFFOOOOOFOF	7A700000	OFFFFOFOOFFFO	7CB80000	OFFFFFOOFOFFF
75E80000	OFFFOFOFFFFOF	78300000	OFFFFOOOOOFFO	7A780000	OFFFFOFOOFFFF	7CC00000	OFFFFFOOFFOOO
75F00000	OFFFOFOFFFFFO	78380000	OFFFFOOOOOFFF	7A800000	OFFFFOFOFOOOO	7CC80000	OFFFFFOOFFOOF
75F80000	OFFFOFOFFFFFF	78400000	OFFFFOOOOFOOO	7A880000	OFFFFOFOFOOOF	7CD00000	OFFFFFOOFFOFO
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76080000	OFFFOFFOOOOOF	78500000	OFFFFOOOOFOFO	7A980000	OFFFFOFOFOOFF	7CE00000	OFFFFFOOFFFOO
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76700000	OFFFOFFOOFFFO	78B80000	OFFFFOOOFOFFF	7B000000	OFFFFOFFOOOOO	7D480000	OFFFFFOFOFOOF
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76800000	OFFFOFFOFOOOO	78C80000	OFFFFOOOFFOOF	7B100000	OFFFFOFFOOOFO	7D580000	OFFFFFOFOFOFF

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ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)	ADDRESS	JUMPER SETTING (JP# 1111 1234567890123)	ADDRESS	JUMPER SETTING (JP# 12345678901234)
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7DA00000	OFFFFFOFFOFOO	7FE80000	OFFFFFFFFFFOF	82300000	FOOOOOFOOOFFO	84780000	FOOOOFOOOFFFF
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	JP#	1111 1234567890123		JP#	1111 1234567890123		JP#	1111 1234567890123		JP#	1111 1234567890123
86800000	FOOOOFFOFOOOO		88C80000	FOOOFOOOFFOOF		8B100000	FOOOFOFFOOOFO		8D580000	FOOOFFOFOFOFF	
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87F80000	FOOOOFFFFFFFF		8A400000	FOOOFOFOOFOOO		8C880000	FOOOFFOOFOOOF		8ED00000	FOOOFFFOFFOFO	
88000000	FOOOFOOOOOOOO		8A480000	FOOOFOFOOFOOF		8C900000	FOOOFFOOFOOFO		8ED80000	FOOOFFFOFFOFF	
88080000	FOOOFOOOOOOOF		8A500000	FOOOFOFOOFOFO		8C980000	FOOOFFOOFOOFF		8EE00000	FOOOFFFOFFFOO	
88100000	FOOOFOOOOOOFO		8A580000	FOOOFOFOOFOFF		8CA00000	FOOOFFOOFOFOO		8EE80000	FOOOFFFOFFFOF	
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88200000	FOOOFOOOOOFOO		8A680000	FOOOFOFOOFFOF		8CB00000	FOOOFFOOFOFFO		8EF80000	FOOOFFFOFFFFF	
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88380000	FOOOFOOOOOFFF		8A800000	FOOOFOFOFOOOO		8CC80000	FOOOFFOOFFOOF		8F100000	FOOOFFFFOOOFO	
88400000	FOOOFOOOOFOOO		8A880000	FOOOFOFOFOOOF		8CD00000	FOOOFFOOFFOFO		8F180000	FOOOFFFFOOOFF	
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88700000	FOOOFOOOOFFFO		8AB80000	FOOOFOFOFOFFF		8D000000	FOOOFFOFOOOOO		8F480000	FOOOFFFFOFOOF	
88780000	FOOOFOOOOFFFF		8AC00000	FOOOFOFOFFOOO		8D080000	FOOOFFOFOOOOF		8F500000	FOOOFFFFOFOFO	
88800000	FOOOFOOOFOOOO		8AC80000	FOOOFOFOFFOOF		8D100000	FOOOFFOFOOOFO		8F580000	FOOOFFFFOFOFF	
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88A00000	FOOOFOOOFOFOO		8AE80000	FOOOFOFOFFFOF		8D300000	FOOOFFOFOOFFO		8F780000	FOOOFFFFOFFFF	
88A80000	FOOOFOOOFOFOF		8AF00000	FOOOFOFOFFFFO		8D380000	FOOOFFOFOOFFF		8F800000	FOOOFFFFFOOOO	
88B00000	FOOOFOOOFOFFO		8AF80000	FOOOFOFOFFFFF		8D400000	FOOOFFOFOFOOO		8F880000	FOOOFFFFFOOOF	
88B80000	FOOOFOOOFOFFF		8B000000	FOOOFOFFOOOOO		8D480000	FOOOFFOFOFOOF		8F900000	FOOOFFFFFOOFO	
88C00000	FOOOFOOOFFOOO		8B080000	FOOOFOFFOOOOF		8D500000	FOOOFFOFOFOFO		8F980000	FOOOFFFFFOOFF	

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9A980000 FOOFFOFOFOOFF	9CE00000 FOOFFFOOFFFOO	9F280000 FOOFFFFFOOFOF	A1700000 FOFOOOOFOFFFO
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9AA80000 FOOFFOFOFOFOF	9CF00000 FOOFFFOOFFFFO	9F380000 FOOFFFFFOOFFF	A1800000 FOFOOOOFFOOOO
9AB00000 FOOFFOFOFOFFO	9CF80000 FOOFFFOOFFFFF	9F400000 FOOFFFFFOFOOO	A1880000 FOFOOOOFFOOOF
9AB80000 FOOFFOFOFOFFF	9D000000 FOOFFFOFOOOOO	9F480000 FOOFFFFFOFOOF	A1900000 FOFOOOOFFOOFO
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9AC80000 FOOFFOFOFFOOF	9D100000 FOOFFFOFOOOFO	9F580000 FOOFFFFFOFOFF	A1A00000 FOFOOOOFFOFOO
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9B000000 FOOFFOFFOOOOO	9D480000 FOOFFFOFOFOOF	9F900000 FOOFFFFFFOOFO	A1D80000 FOFOOOOFFFOFF

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A4200000	FOFOOFOOOOFOO	A6680000	FOFOOFFOOFFOF	A8B00000	FOFOFOOOFOFFO	AAF80000	FOFOFOFOFFFFF

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AB000000	FOFOFOFFOOOOO	AD480000	FOFOFFOFOFOOF	AF900000	FOFOFFFFFOOFO	B1D80000	FOFFOOOFFFOFF
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ADDRESS	JUMPER 1111 1234567890123	SETTING
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INDEX

A
architecture 1-4, 1-7

C
cabling
  for Ethernet 802.3 transceiver 2-11, 2-21, 2-30, 6-2
  for routing signals off P2 connector 2-1, 2-11, 2-21, 2-30
  installing 2-12, 2-22, 2-31
  verifying operation 5-18
Command Queue 3-3, 3-4, 4-5
commands
  issued through Command Queue 3-4
  issued through Master Command Entry 3-3
  priority of 3-3
communications buffer 3-5
Controller Initialization Block 5-31
conventions 1-8
  used in manual 1-8

D
data transmissions 3-6
diagnostics 3-9
  extended 5-2
  extended (error codes) 7-4, 7-5
  overview of 3-9
  power up 3-9, 4-2, 4-3
  power up error codes 7-2, 7-3, 7-4

E
Electrostatic Discharge (ESD) 2-1
errors
  controller 7-1
  EPROM 7-3
  ethernet 7-2
  general 7-1
  LANCE 7-4
  miscellaneous 7-2
  multiple completions 7-1
  processor 7-2
  queue 7-1
  RAM address 7-3
  RAM size 7-3
  VMEbus 7-1

G
Gather operations 3-6

H
Humidity 6-1

I
Initialize Controller 5-27
Installation
  Overview 2-1
  PB04207-000,REVD motherboard 2-14
  PB04207-001,REVA Motherboard 2-23
  PB0760-A08-010 Motherboard 2-4
interrupts 3-9

J
jumpers 2-5, 2-15, 2-24
  daughtercard 2-10, 2-20, 2-29

L
LED 6-1
loopback test 5-19

M
MACSI data structures 3-1, 4-1
Master Command Entry 3-3, 4-4
Master Control Register 4-3
Master Control/Status Block 4-1
Master Status Register 4-2
MULTIPLE 7-1

P
P2 connector
  routing signals from 2-2, 2-12, 2-22, 2-31
P2/DB15 Adapter 2-12, 2-21, 2-30
Power 6-1
priority
  of commands 3-3

R
receiving data
  1-4, 1-6, 3-1, 3-2, 3-4, 3-5, 3-6, 4-5, 4-15
  disabling 5-32
  DMA Command 5-62
  error messages 7-2
  over VMEbus C-1
  receive buffer pool offset 4-13

S
short I/O 4-1
specifications 1-7
switches 2-7

T
Temperature 6-1
transmitting data
  1-4, 1-6, 3-1, 3-2, 3-4, 3-5, 4-5

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buffer pool offset 4-13
disabling CRC 5-32
DMA Command 5-62
error messages 7-2
statistics 4-14

V

VMEbus 1-7
specifications (ref.) 1-2
timing C-1

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