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4207 User's Guide Errata (UG04207-000,REVD 10/07/94)

- We've recently discovered some errors in the User's Guide:

  This product is **End-of-Life**.
  There are no plans for a User's Guide update, or ReadMeFirst.
  There are no locations for 4207 Errata on the Web or FTP sites.

  1. UG04207-000,REVE pp 2-23 page-title indicates:
  SECTION III: PB04207-001,REVA EAGLE MOTHERBOARD INSTALLATION
  All current shipments of 4207 are actually PB04207-001,REVC. For the purposes of this section, there are no significant differences.

  2. UG04207-000,REVE pp 2-23 Figure 2-9 drawing title indicates:
  The figure below shows the location of jumpers on the Eagle PB04207-001,REVB Motherboard.
  All current shipments of 4207 are actually PB04207-001,REVC. For the purposes of this section, there are no significant differences.

  3. UG04207-000,REVE does not have a drawing of the current production artwork PB04207-001,REVC.
  For the purposes of this section, there are no significant differences between REVB and REVC. Nor, for that matter, between REVA and REVC.

  4. UG04207-000,REVE pp 2-24 STEP 2 incorrectly states the status of JA-4 (on-board clock de-select).

  A. Jumper JA-4 header-pins are not installed on any current configurations.

  B. The accompanying illustration and text incorrectly states the condition of this jumper location.

  Incorrect:
  IN = FACTORY DEFAULT Do not alter
  OUT = Deselects factory-to-CPU clock circuitry

  Correct:
  This illustration and text should be removed.

  Some details:
  The on-board oscillator's output is routed to the input of a "D-type" flip-flop to condition the signal for 50-50 duty-cycle.
  In the past, this was used for margin-testing, and then only when the oscillator and flip-flop were both de-installed (for injecting an external signal).
  JA-4, when installed on a fully-assembled board, would connect the output of the on-board oscillator to the output of this flip-flop.
  The reason this may appear to work (header-pins installed and a jumper on JA-4) is because the output of the flip-flop is connected to the rest of the circuitry through a series resistor that prevents the signal from being fatally corrupt.

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