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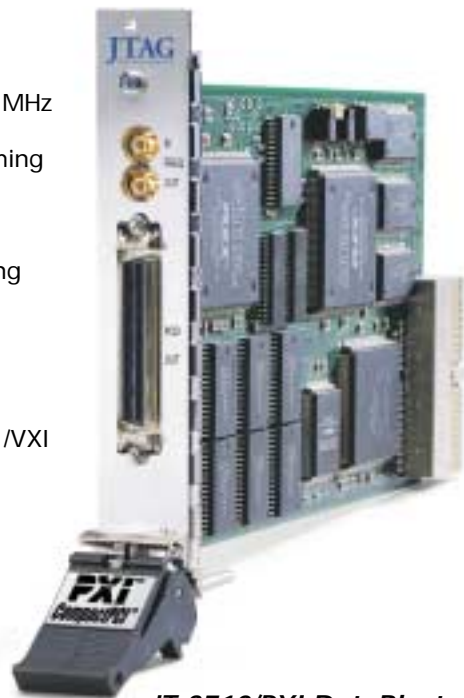
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JT 3710/PXI DataBlaster™

CompactPCI/PXI Boundary-Scan Controller for Testing, Flash Memory and PLD On-Board Programming

Features

- CompactPCI/PXI based controller for boundary-scan testing and flash and PLD on-board programming
- Plug and Play operation
- Sustained data rate of 25 Mbps per channel
- Autonomous controller operation
- Programmable TCK in steps of 100 kHz from 100 kHz to 25 MHz
- AutoWrite™ for flash programming acceleration
- Dedicated hardware for high speed in-system programming of flash memories
- High signal quality at target by actively terminated POD (max cable length of 4 meters)
- Controls up to 4 scan chains, allowing multi-chain testing and gang programmable
- Supports optional programmable specific signals: VPP-enable, Ready/Busy, User1, User0
- Compatible with 3.3 V and 5 V target voltage levels
- Windows 95/98, Windows NT/2000
- Application compatible with JT 3710/ISA, /PCI, /USB and /VXI



JT 3710/PXI DataBlaster™

General Description

The CompactPCI/PXI version of the JT 3710 DataBlaster is fully compatible with the ISA, PCI, VXI, and USB versions, allowing applications developed on one of the models to be executed on any other version. The JT 3710/PXI includes a POD that provides four parallel TAP output channels, allowing a single JT 3710/PXI controller to drive and sense up to four boundary-scan chains simultaneously.

Functional Description

The TAPs driven by the JT 3710/PXI support test frequencies from 100 kHz up to 25 MHz, in 100 kHz increments. Dedicated hardware within the JT 3710/PXI delivers test and programming files to the target in a continuous fashion, rather than in a burst mode, for maximum performance.

The DataBlaster performs data compression and de-compression on the fly and is capable of autonomous operation, allowing it to control the boundary-scan operation without intervention from the host computer. Control returns to the host upon completion of the operation.

The JT 3710/PXI supports JTAG Technologies' AutoWrite™ (patent pending) feature that reduces flash programming time by a factor of 2 to 3. This is accomplished by real-time hardware control of the flash write-enable line instead of via the boundary-scan chain. The JT 3710/PXI also supports other flash-specific control signals, including Vpp Enable (to control optional programming voltages on the target), User0/User1 (watchdog- or software-controlled) and Ready (for optional use to verify completion of an operation).

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High-Performance Boundary-Scan Controllers

The POD provides active line terminations, maintaining the integrity of critical signals at the point of test. The POD may be separate from the controller module and is connected to it via a SCSI-2 type cable up to 4 meters in length. Four connectors on the POD allow easy interconnection via short cables to the target board(s). Three types of PODs are available:

the PF 2137/10 for bench top use, and the PF 2137/12 and PF 2137/13 for industrial applications where rugged construction, integration within a test fixture, and easy interchangeability between fixtures are required.

The same POD may be used for all of the JT 3710 controller types. In flash programming applications, the JT 3710 POD allows gang programming of up to four flash memories.

LabWindows and LabVIEW Integration Packages

Besides JTAG Technologies' standard production packages for stand-alone operation and for DDE-based networked operations, integration packages are available for the JT 3710/PXI. These packages run 32-bit DLLs under National Instruments' LabWindows /CVI and LabVIEW operating systems.

The LabWindows instrument drivers and the associated function panels allow boundary-scan test

and programming applications to be easily integrated along with other tests in a PXI station using LabWindows/CVI. The user interface at the PXI tester is thereby extended to the boundary-scan processes, bringing the benefits of commonality to the user and reducing the amount of board handling.

The LabVIEW production integration package supports the execution of boundary-scan 32-bit test and programming DLLs under National Instruments' LabVIEW operating system. Test engineers are able to quickly develop test and programming applications using the graphical LabVIEW environment at their PXI based tester. Refer to the box for available instrument drivers.

LabVIEW Instrument Drivers

LV-ICL10	for testing/flash programming using the Instrument Control Library
LV-ACE	for testing/flash programming using interpretation of APL formats
LV-PAT	for test execution using PAT formats
LV-JPF	for JEDEC/SVF programming of PLDs
LV-JAM	for JAM programming of PLDs

Ordering Information

Product Number	Description
JT 3710/PXI	CompactPCI/PXI version of DataBlaster, highest-performance boundary-scan controller for testing, on-board flash memory programming, and PLD programming
PIP 2176/LW	Production Integration Package, Runtime DLLs plus LabWindows/CVI Drivers
PIP 2176/LV	Production Integration Package, Runtime DLLs plus LabVIEW Drivers

Note: Controller includes one of the versions of the actively-terminated PF 2137 TAP POD with four channels for multi-chain testing and parallel flash memory programming, SCSI cable to connect the controller to the POD, and a cable to connect the POD to the target.

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