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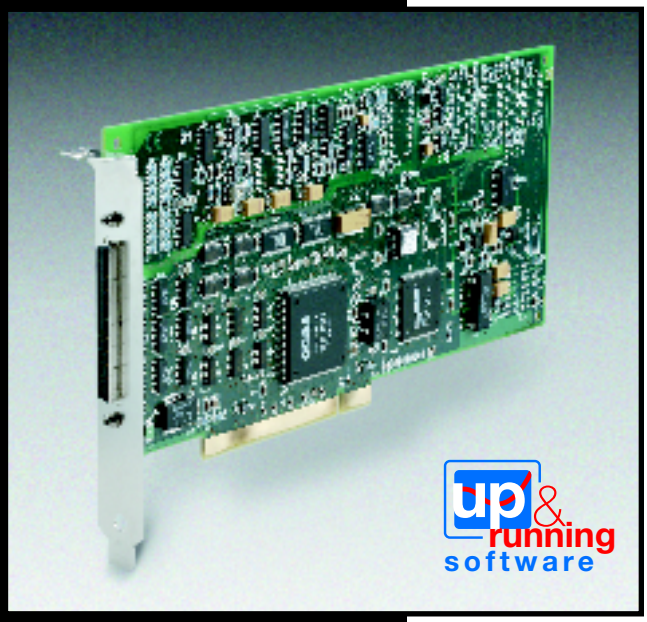
Find the *Keithley KPCI-3102* at our website: ***Click HERE***

Strong performance at an extremely affordable price

PCI/ISA/PCMCIA

KPCI-3101
KPCI-3102
KPCI-3103
KPCI-3104

225/400kHz, 12-Bit, Low Gain Analog I/O Boards



- 12-bit resolution
- Throughput of up to 225kS/s or 400kS/s
- Digital I/O scanning speeds of up to 3MHz
- 16 single-ended or 8 differential analog inputs
- 23 digital I/O lines
- 2 analog outputs (KPCI-3102 and -3104 only)
- 4 counter/timers
- Low gain (1, 2, 4, 8)
- Pre-, post-, and about-triggering
- 1024-location channel-gain queue
- 32-bit DriverLINX drivers plus a suite of bundled software including ExceLINX, VisualSCOPE, TestPoint, and LabVIEW drivers

Our KPCI-3101/3102/3103/3104 multifunction boards provide strong performance at an extremely affordable price. Fully loaded, they are an attractive one-stop solution, providing everything you need on a single low-cost board.

Functional Description

This family of PCI-bus data acquisition boards features low gain, 12-bit resolution and a choice of throughput speeds. The KPCI-3101/3102 boards provide a throughput of 225kS/s, while the KPCI-3103/3104 are designed for applications requiring a faster input speed of 400kS/s. In addition, these multifunction boards include 32-bit DriverLINX software drivers, TestPoint drivers, and LabVIEW VIs. Keithley's new start-up software is also included at no charge.

Analog Inputs

The analog inputs are software configurable for single-ended or differential inputs and bipolar or unipolar input ranges. An Amp Low connection allows single-ended inputs to be referenced to a common point other than ground to provide 16 pseudo-differential inputs. For added flexibility, a 1024-location channel-gain queue allows you to sample non-sequential channels and channels with different gains.

The Calibration utility allows both manual and automatic software calibrations.

Analog Input Acquisition Modes

These boards can acquire a single value from any channel or a number of samples from multiple channels. To acquire data from multiple channels, the boards provide 2 scan modes: continuously paced and triggered. Both scan modes can be paced using an internal or an external clock.

The boards provide several triggering modes, including pre-trigger, post-trigger, and about-trigger modes.

- Pre-trigger mode allows acquisition to occur until an external trigger occurs.
- Post-trigger is the standard acquisition mode; acquisition begins after an internal or external trigger event and continues until an end condition occurs or the specified number of samples are collected.
- About-trigger mode allows acquisition to occur both before and after an external trigger.

Analog Outputs

The KPCI-3102 and -3014 boards feature 2 serial, multiplying, analog output channels. The output range is ±10V at 16-bit resolution. The analog outputs are set to 0V at power-up, and they supply single value updates suitable for DC control signals.

Digital I/O

All the boards feature 23 digital I/O lines. These lines are divided into two 8-bit ports and one 7-bit port. With software, ports A and B can be combined to create a virtual port consisting of 16 digital I/O lines. Each port can be programmed for input or output, and the status of the A and B ports can be read at rates as high as 3MS/s by including the digital input lines of ports A and B as a channel in the analog channel-gain queue. The digital outputs have sufficient current capability to drive external solid-state relay modules (12mA sink and 15mA source).

Port C can ground 7 digital I/O lines and can be programmed for general-purpose (non-clocked) input or output. The digital outputs can drive external solid-state relay modules with its 12mA sink and 15mA source.

Counter/Timers

These boards provide four 16-bit counter/timers. Uses include counting events, creating a one-shot or frequency output, and measuring frequency input. They can also be used to set the duty cycle, frequency, and output polarity of the output pulse.

These counter/timers can be cascaded. Cascade 2 counter/timers internally through software. Cascade 3 or 4 counter/timers externally on a screw terminal accessory.

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A GREATER MEASURE OF CONFIDENCE

KPCI-3101
KPCI-3102
KPCI-3103
KPCI-3104

Ordering Information

KPCI-3101 Low-gain, multifunction, 225kS/s, PCI-bus board

KPCI-3102 Low-gain, multifunction, 225kS/s, PCI-bus board with Analog outputs

KPCI-3103 Low-gain, multifunction, 400kS/s, PCI-bus board

KPCI-3104 Low-gain, multifunction, 400kS/s, PCI-bus board with Analog outputs

ACCESSORIES AVAILABLE

CAB-305	KPCI-3101/3102/3103/3104 to STA-300, 68-pin, 2-meter, Shielded Cable
STA-300	Screw Terminal Accessory
STP-68	Screw Terminal Panel (not CE approved)
MB-01*	16-Channel Direct-Connection Module Mounting Rack
MB-05*	8-Channel Direct-Connection Module Mounting Rack
TESTPOINT	TestPoint Software Package
MS-KPCI-3104	Additional Hardware and Software Manuals and Standard Software for the KPCI-3101/3102/3103/3104

*Signal conditioning modules for the MB-01, MB-02, and MB-05 can be found in the Signal Conditioning and Accessories section.

Physical and Environmental Specifications

PHYSICAL:
Dimensions: 8.5 inches (length) by 4.2 inches (width).
I/O Connector: 68 pin Amp (#749621-7).

CERTIFICATION AND COMPLIANCE: FCC Class A verified; will not compromise FCC compliance of host computer CE.

COMPLIANCE: Conforms to European Union directive 89/336/EEC (EMC directive), EN55022, and EN50082-1. (Product is CE marked.)

ENVIRONMENTAL:
Operating Temperature Range: 0°C to 70°C.
Storage Temperature Range: –25°C to 85°C.
Relative Humidity: To 95%, noncondensing.

225/400kHz, 12-Bit, Low Gain Analog I/O Boards

Features Summary

ANALOG INPUTS

Board	Channels	Resolution	Input Ranges
KPCI-3101/3102	16 SE/8 Diff	12 bits	±1.25, 2.5, 5, 10 V 0–1.25, 2.5, 5, 10 V
KPCI-3103/3104	16 SE/8 Diff	12 bits	±1.25, 2.5, 5, 10 V 0–1.25, 2.5, 5, 10 V

ANALOG OUTPUTS

Board	Channels	Resolution	Output Ranges	Counter/Timer
KPCI-3101/3103	0	N/A	N/A	4
KPCI-3102/3104	2	16 bits	±10 V	4

Analog Inputs

	KPCI-3101/3102	KPCI-3103/3104
Number of analog input channels		
Single-ended/pseudo-differential	16	16
Differential	8	8
Resolution	12 bits	12 bits
Channel-gain list	1024 locations	1024 locations
Input FIFO size	1024 locations	1024 locations
Input gains	1, 2, 4, 8	1, 2, 4, 8
Input range		
Bipolar	±10, ±5, ±2.5, ±1.25 V	±10, ±5, ±2.5, ±1.25 V
Unipolar	0–10, 5, 2.5, 1.25 V	0–10, 5, 2.5, 1.25 V
Drift		
Zero	±30µV + (+20µV*Gain)/°C	±30µV + (+20µV*Gain)/°C
Gain	±30 ppm,/°C	±30 ppm,/°C
Input impedance	100 MΩ, 10 pF, Off 100 MΩ, 100 pF, On	100 MΩ, 10 pF, Off 100 MΩ, 100 pF, On
Input bias current	±20 nA	±20 nA
Common mode voltage	±11 V maximum (operational)	±11 V maximum operational
Maximum input voltage	±35 V maximum (protection)	±35 V maximum (protection)
Channel acquisition time	3 µs	1 µs
A/D conversion time	4.44 µs	2.5 µs
Accuracy		
Nonlinearity (integral)	±1.0 LSB	±1.0 LSB
Differential nonlinearity	±0.5 LSB (no missing codes)	±0.5 LSB (no missing codes)
System noise	0.3 LSB rms	0.3 LSB rms
Channel-to-channel offset	±40.0 µV	±40.0 µV
Clocking and trigger input		
Maximum A/D pacer clock		
Single analog input throughput	225 kS/s @ 0.03% accuracy	400 kS/s @ 0.03% accuracy
Multiple analog input throughput	160 kS/s @ 0.03% accuracy	300 kS/s @ 0.03% accuracy
Multiple analog input throughput	225 kS/s @ 0.05% accuracy	400 kS/s @ 0.05% accuracy
Single digital input channel	3 MS/s	3 MS/s
Minimum A/D pacer clock throughput	1.2 S/s	1.2 S/s
External A/D sample clock		
Minimum pulse width	100 ns (high); 100 ns (low)	100 ns (high); 100 ns (low)
Maximum frequency (analog inputs)	225 kHz	400 kHz
Maximum frequency (digital inputs only)	3 MHz	3 MHz
External digital (TTL) trigger		
High-level input voltage	2.0 V minimum	2.0 V minimum
Low-level input voltage	0.8 V maximum	0.8 V maximum
Minimum pulse width	100 ns (high); 100 ns (low)	100 ns (high); 100 ns (low)

One-stop solution at a low cost

PCI/ISA/PCMCIA

KPCI-3101
KPCI-3102
KPCI-3103
KPCI-3104

225/400kHz, 12-Bit, Low Gain Analog I/O Boards

Analog Outputs

	KPCI-3102	KPCI-3104
Number of analog output channels	2 (voltage output)	2 (voltage output)
Resolution	16 bits	16 bits
Output range	±10V	±10V
Error: Gain Zero	±2 LSB + Reference Software adjustable to 0	±2 LSB + Reference Software adjustable to 0
Current output	±5mA minimum	±5mA minimum
Output impedance	0.3Ω typical	0.3Ω typical
Capacitive drive capability	0.001μF (no oscillations)	0.001μF (no oscillations)
Nonlinearity (integral)	±1.0 LSB ±1.0 LSB	
Differential linearity	±0.5 LSB (monotonic)	±0.5 LSB (monotonic)
Protection	Short circuit to Analog Common	Short circuit to Analog Common
Power-on voltage	0V ±10mV	0V ±10mV
Settling time to 0.01% of FSR	50μs, 20V step; 10.0μs, 100mV step	50μs, 20V step 10.0μs, 100mV step
Slew rate	2V/μs	2V/μs

Digital I/O

	Port A	Port B	Port C
Number of lines	8 bidirectional	8 bidirectional	7 bidirectional
Inputs			
High-level input voltage	2.0V minimum	2.0V minimum	2.0V minimum
Low-level input voltage	0.8V maximum	0.8V maximum	0.8V maximum
High-level input current	3μA	3μA	100μA
Low-level input current	–3μA	–3μA	–100μA
Maximum internal pacer clock rate (single digital channel)	3MHz	3MHz	3MHz
Outputs			
Output driver high voltage	2.4V minimum (I_{OH} = –15mA)	2.4V minimum (I_{OH} = –15mA)	2.4V minimum (I_{OH} = 4mA)
Output driver low voltage	0.5V maximum (I_{OL} = 12mA)	0.5V maximum (I_{OL} = 12mA)	0.8V maximum (I_{OL} = 4mA)

Counter/Timer

NUMBER OF COUNTER/TIMER CHANNELS: 4.

CLOCK INPUTS:

High-Level Input Voltage: 2.0V minimum.

Low-Level Input Voltage: 0.8V maximum.

Minimum Pulse Width: 100ns (high); 100ns (low).

Maximum Frequency: 5.0MHz.

GATE INPUTS:

High-Level Input Voltage: 2.0V minimum.

Low-Level Input Voltage: 0.8V maximum.

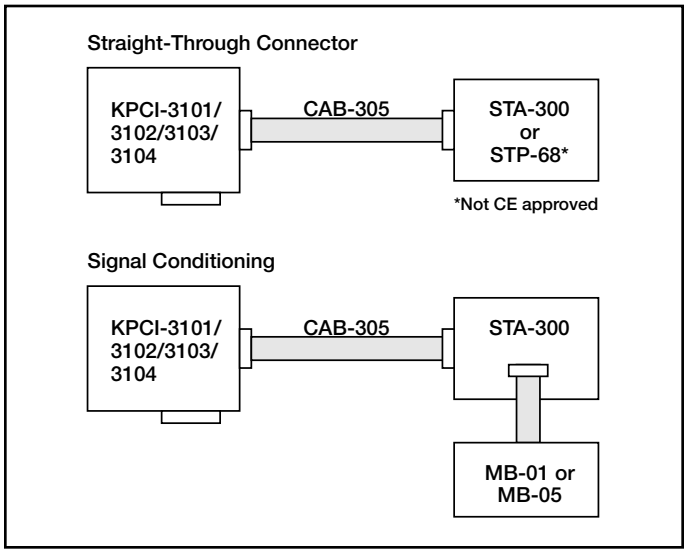
Minimum Pulse Width: 100ns (high); 100ns (low).

COUNTER OUTPUTS:

Output Driver High Voltage: 2.0V minimum (I_{OH} = –15mA); 2.4V minimum (I_{OH} = –3mA).

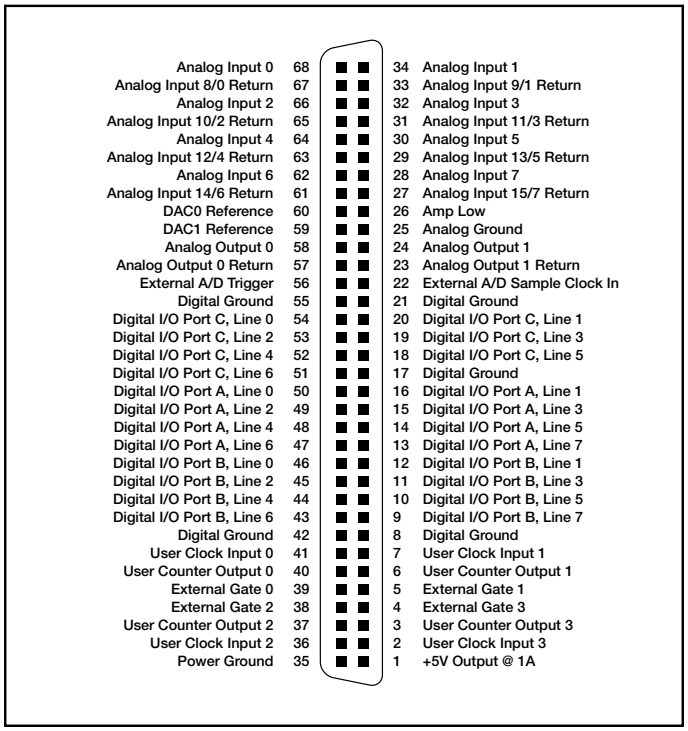
Output Driver Low Voltage: 0.5V maximum (I_{OL} = –24mA); 0.4V maximum (I_{OL} = –12mA).

Configuration Guide



Connector Pin Assignments

The analog input, analog output, digital input, and digital output connections are made with a 68-pin, subminiature D connector at the rear of the computer.



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