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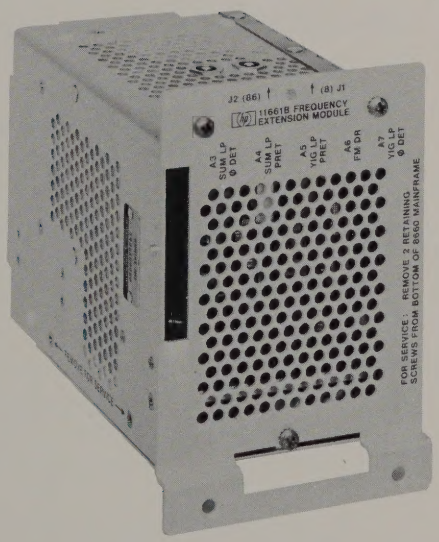
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OPERATING AND SERVICE MANUAL

11661B
FREQUENCY EXTENSION MODULE



HEWLETT PACKARD

CERTIFICATION

Hewlett-Packard Company certifies that this product met its published specifications at the time of shipment from the factory. Hewlett-Packard further certifies that its calibration measurements are traceable to the United States National Bureau of Standards, to the extent allowed by the Bureau's calibration facility, and to the calibration facilities of other International Standards Organization members.

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MANUAL CHANGES

MANUAL IDENTIFICATION
Model Number: 11661B
Date Printed: Aug. 1981
Part Number: 11661-90025

FREQUENCY EXTENSION MODULE

This supplement contains important information for correcting manual errors and for adapting the manual to instruments containing improvements made after printing the manual.

To use this supplement, first, make all ERRATA corrections and then all appropriate serial number related changes indicated in the tables below.

SERIAL PREFIX OR NUMBER	MAKE MANUAL CHANGES
2446A	1
2551A	1-2
>> 2607A	1-3

SERIAL PREFIX OR NUMBER	MAKE MANUAL CHANGES

>> NEW ITEM

NOTE:
Manual change supplements are revised as often as necessary to keep manuals as current and accurate as possible. Hewlett-Packard recommends that you periodically request the latest edition of this supplement. Free copies are available from all HP offices. When requesting copies, quote the manual identification information from your supplement or the model number and print date from the title page of the manual.

Printed in U.S.A.

04 April 1986
5 Pages Text
2 Pages Illustrations
1 Foldout

HEWLETT PACKARD

ERRATA

Page 1-2, paragraph 1-18:
Add the following note after paragraph 1-18:

NOTE

If this instrument is not shipped from the factory as part of a complete signal generator system (mainframe, modulation section, and RF section), it may be necessary to perform the adjustments in Section V before all performance specifications will be met.

Page 5-2, Table 5-1:
For Reference Designator A4C8*, change maximum Normal Value from 330 to 510 pF.
Add the following to Table 5-1:

A6R19	To obtain a symmetrical search waveform about 0 Vdc.	1.3k to 2.0k ohms	5

Page 5-16, paragraph 5-33:
Under PROCEDURE, add the following to step 3:
(approximately 0 dBm)
Page 5-17, paragraph 5-33:
Under PROCEDURE, add the following:
Step 7A. Insert the Modulation Plug-in removed in step 5.
Page 5-20, Paragraph 5-35. SUM LOOP BANDWIDTH ADJUSTMENT:
In Step 6 line two, change 560 to 510
Add the following table after Step 18.

A4C8*(pF)	Part Number
200	0140-0198
220	0160-0134
240	0140-0199
330	0160-2208
360	0160-2209
390	0140-0200
430	0160-0939
510	0160-3534

ERRATA (cont'd)

Page 6-4, Table 6-2:
Change A1R1 to 0698-7202 CD7 0.050W.
Under A1 MISCELLANEOUS:
Add 11661-00021 QTY1 COVER, SUM YIG LOOP.
Add 11661-20046 QTY1 SHIELD, POLYIRON (P/OA1A1).
Add 11661-20048 QTY1 SHIELD, POLYIRON (P/OA1A1).
Delete 11661-00004 and 11661-00009.

Page 6-6, Table 6-2:
Change A3U1 and U3 to 1820-0681 IC GATE TTL S NAND QUAD 2-INP.

Page 6-7, Table 6-2:
Change A4U1 to 1820-0214 CD9 IC DCDR TTL BCD-TO-DEC 4-TO-10-LINE SN7442AN.

Page 6-8, Table 6-2:
Change A5R21 Part Number and Description to:
2100-3123 CD0 RESISTOR-TRMR 500 10% C SIDE-ADJ 17-TRN.

Page 6-9, Table 6-2:
Add an asterisk (*) to A6R19
Change A6R19* Part Number and Description to:
0698-7242 CD5 RESISTOR 1.78K 1% .05W.

Page 6-10, Table 6-2:
Change A7T1 to 11661-60087 CD7 TRNSFRMR ASSY
Change A7U3 TO 1826-0547 OP AMP GP DUAL DIP.
Change A8 part number to 11661-60084.

Page 6-11, Table 6-2:
Under CHASSIS PARTS, change part number for P4 as follows:
11661-60055 CD3.

Page 6-12, Table 6-2:
Under CHASSIS MISCELLANEOUS, change Part Number on WIRE GASKET to 11661-20051.

Page 7-1, Paragraph 7-2:
Change serial prefix number in line four to 1920A.

Page 7-13, Figure 7-8:
On right side of schematic, replace topview of A7U5 with the Figure from this supplement.

Page 8-9, Figure 8-3 (SS1):
In upper left corner, cable from A8J3 to A2J1 should be 3 .
At bottom right, cable from A2XA6-11 should be 90 and should connect to A13P1-5. Cable from A2XA6-10 should be 9 and should connect to A13P1-2. A10J1-3 should connect to 95 and A10J1-4 should connect to 96
Delete A1A2R3 VCO BIAS ADJ partial schematic above A1U2 and connect line from A14 OSCILLATOR REGULATOR ASSY to A1U2 through A1C6.

>> Page 8-11, Service Sheet 2 (schematic):
Connect CR2 so that the cathode of CR2 connects to "YIG COIL (+)" and the anode of CR2 connects to the anode of VR4. Connect the cathode of VR4 to "YIG COIL (-)".

ERRATA (cont'd)

Page 8-11, Figure 8-5 (SS2):
On right side of page, change YIG COIL(+) and (-) as follows:
From XA5-9 95 K 5.
From XA5-10 96 L 5.
Add C1 to unlabelled pin on A5Q6 Topview.

Page 8-12, Figure 8-15 (SS7):
On right side of schematic at A2XA4 BOTTOM VIEW, delete last section pins P and 13.

Page 8-13, Figure 8-6:
Just below A1U2, re-label A1R2 as A1R3 and A1R3 as A1R2.

Page 8-13, Figure 8-7 (SS3):
On left side at the bottom of schematic, input to P4-C should be in 10MHz steps.
On A1A1 Assembly at Q3 source insert Test Point 1.
At J3 change A12W1 to 83 .

Page 8-15, Figure 8-8:
Replace Figure 8-8 with new figure from this supplement.

Page 8-15, Figure 8-9 (SS4):
Change A7U3A and 3B part number to 1826-0547.
Replace A7U3 Topview with the Figure from this supplement.

Page 8-17, Figure 8-11 (SS5):
On right side of schematic, change A13P1 pin 4 to pin 3 and A13P1 pin 3 to pin 4.
Change cable color code 96 to 95, and change 95 to 96.
Add an asterisk (*) to A6R19 located near A6Q6 (drain).
Change value of A6R19* to 1780 ohms.

Page 8-19, Figure 8-12:
In the middle of the A3 Assembly just above R12 and between C14 and C12, change C5 to C4.

Page 8-19, Figure 8-13 (SS6):
On right side of page, add C1 to unlabelled pin next to tab and change pin labelled C1 to E1.

Page 8-23, Figure 8-17:
Replace A1 ASSEMBLY with new figure from this supplement.
At EXTENSION MODULE (INTERNAL VIEW), change A12W1 to GRAY-ORANGE.

CHANGE 1

Page 6-6, Table 6-2:
Change A4Q1-12 to 1853-0360 CD5 TRANSITOR PNP 2N3799A SI TO-18 PD=360MW.
Delete A4R3, 9, 14, 18, 21, 24, 27, 30, 33, and 36.
Add VR1-10 1902-0943 CD5 DIODE-ZNR 2.4V 5% DO-35 PD=.4W TC=-.044%.
Change A4R4, 10, 15, 19, 22, 25, 28, 31, 34, and 37 to 0698-7264 CD1 RESISTOR 14.7K 1% .05W F TC=0+-100.

Page 8-21, Service Sheet 7:
Replace Service Sheet 7 with the attached foldout "**Figure 8-14. A4 Sum Loop Pretune Assembly Component Locations**" and "**Figure 8-15. Sum Loop Pretune Section Schematic Diagram**".
In Figure 8-15 Sum loop Pretune Section Schematic Diagram on the foldout in this supplement, change the value of R4, 10, 15, 19, 22, 25, 28, 31, 34, and 37 14.7k

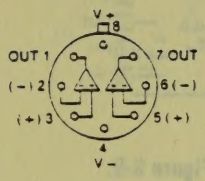
CHANGE 2

Page 6-4, Table 6-2:
Change A1A1Q2 to 1854-0809 CD9 TRANSISTOR NPN 2N2369A SI TO-18 PD=360 MW 28480.

>> CHANGE 3

Page 6-10, Table 6-2:
Change A7U4 to 0955-0292 CD7 DC MIXER 600 MHZ.

A7U5 TOPVIEW



P/O Figure 7-8.

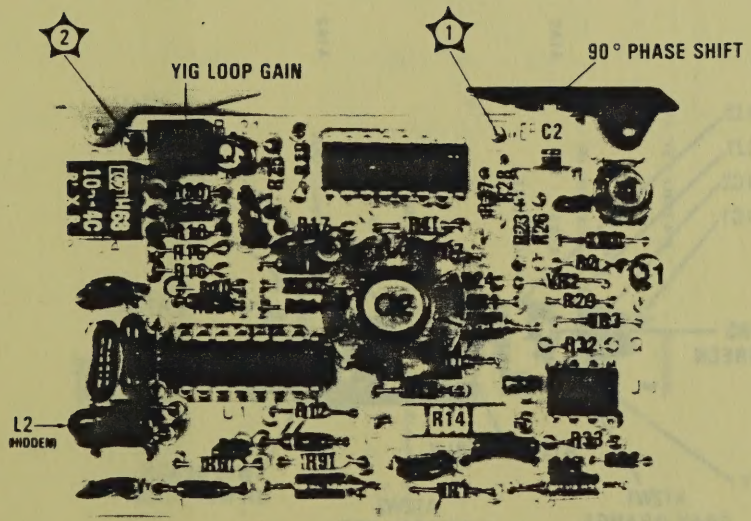
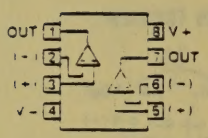


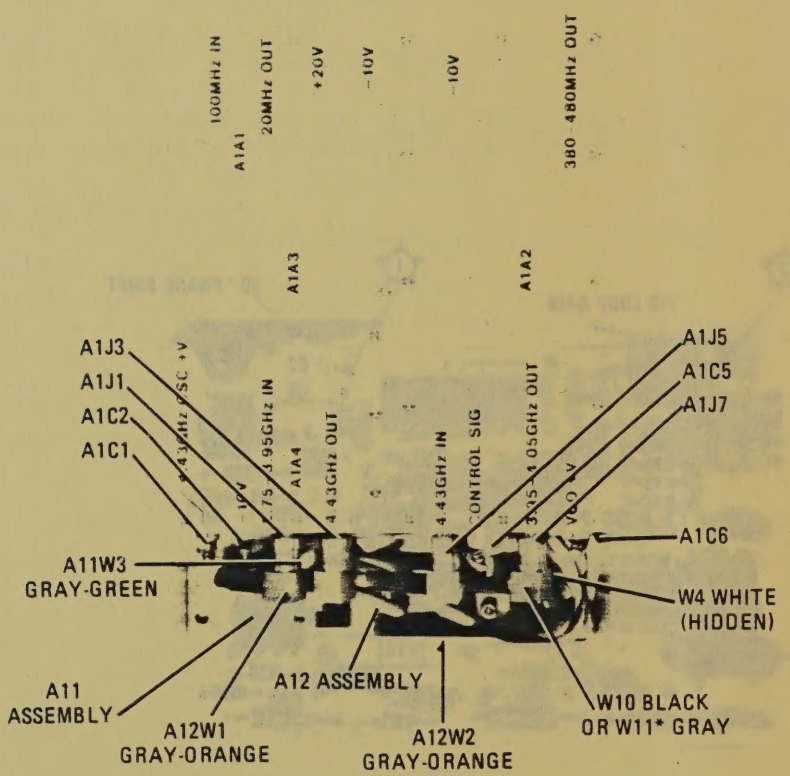
Figure 8-8. A7 Yig Loop Phase Detector Assembly

A7U3 TOPVIEW

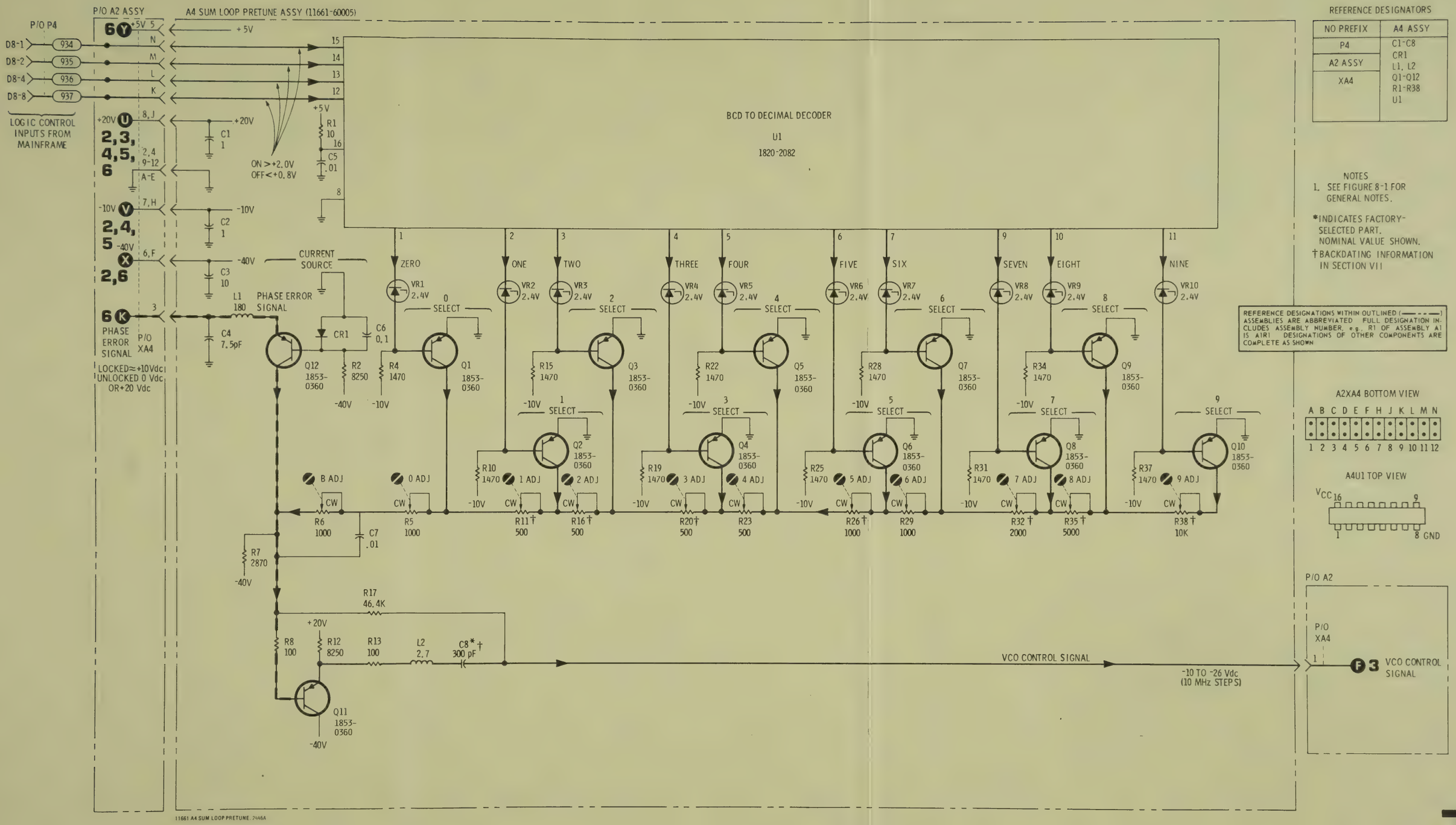


P/O Figure 8-9.

A1 ASSEMBLY



P/O Figure 8-17. Oscillator/Mixer Housing Assembly



7 A4

Figure 8-15. Sum Loop Pretune Section Schematic Diagram

OPERATING AND SERVICE MANUAL

11661B
FREQUENCY EXTENSION MODULE

SERIAL NUMBERS

This manual applies directly to instruments with serial numbers prefixed 1920A.

With changes described in Section VII, this manual also applies to instruments with serial numbers prefixed 1439A, 1511A, 1515A, 1533A, 1538A, 1543A, 1544A, 1545A, 1604A, 1619A, 1729A, 1734A, 1801A, and 1805A.

For additional important information about serial numbers, see INSTRUMENTS COVERED BY MANUAL in Section I.

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WARNINGS

SAFETY

Although this instrument has been designed in accordance with international safety standards, this manual contains information, cautions, and warnings which must be followed to retain the instrument in safe condition. Be sure to read and follow the safety information in Sections II, V, and VIII.

BEFORE CONNECTING THIS SYSTEM TO LINE (MAINS) VOLTAGE, the safety and installation instructions found in Sections II and III of the mainframe manual should be followed.

HIGH VOLTAGE

To avoid contact with the line voltage, remove the line (main) power cable from the power outlet before removing or connecting the Frequency Extension Module.

Capacitors inside the instrument may still be charged even if the system has been disconnected from its source of supply.

Adjustments and troubleshooting are often performed with power supplied to the instrument while protective covers are removed. Energy available at many points may, if contacted, result in personal injury.

The multi-pin plug connector (on mainframe), which provides interconnection to the Extension Module will expose power supply voltages which may remain on the pins after the Extension Module is removed and after the (Mains) power cable is disconnected from the mainframe. Be careful to avoid contact with the pins during interconnection with the Extension Module.

CAUTIONS

PERFORMANCE TESTING

To avoid the possibility of damage to the instrument or test equipment, read completely through each test before starting it. Then make any preliminary control settings necessary before continuing with the procedure.

PLUG-IN REMOVAL

Before removing the RF Section plug-in from the mainframe, remove the line (Mains) voltage by disconnecting the power cable from the power outlet.

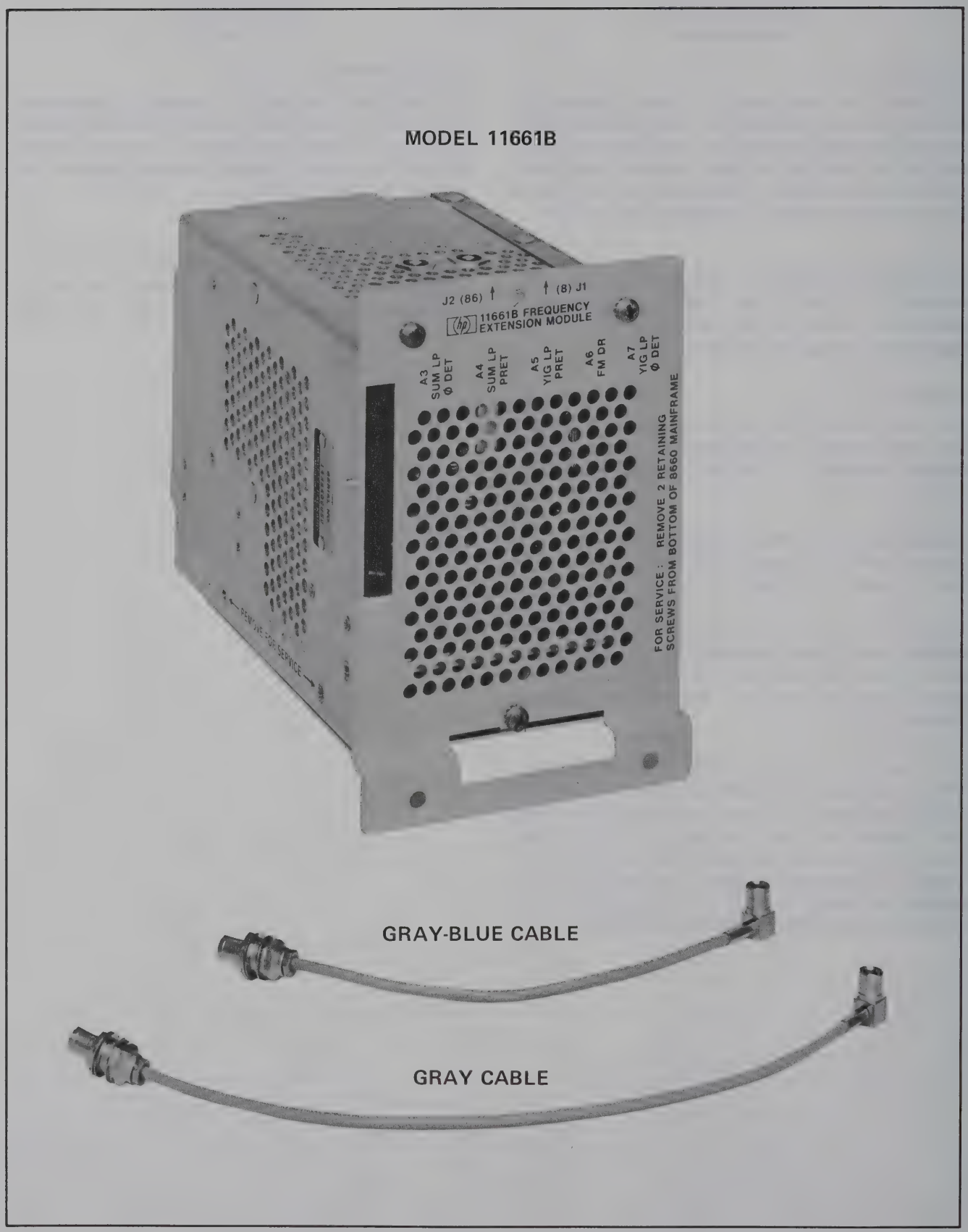


Figure 1-1. HP Model 11661B and Accessories Supplied

SECTION I
GENERAL INFORMATION

1-1. INTRODUCTION

1-2. This manual contains all information required to install, operate, test, adjust and service the Hewlett-Packard 11661B Frequency Extension Module plug-in, sometimes referred to as the Extension Module. For more information on related instruments such as the Model 8660-series mainframes, 86600-series RF Section plug-ins, or 86630-series Modulation Section plug-ins, refer to the appropriate manual.

1-3. This manual is divided into eight sections which provide information as follows:

a. SECTION I, GENERAL INFORMATION, contains the instrument description as well as the accessory and recommended test equipment test.

b. SECTION II, INSTALLATION, contains information relative to receiving inspection, preparation for use, mounting, packing, and shipping.

c. SECTION III, OPERATION, provides information relative to instrument operation.

d. SECTION IV, PERFORMANCE TESTS, provides information required to ascertain that the instrument is performing in accordance with published specifications.

e. SECTION V, ADJUSTMENTS, contains information required to properly adjust and align the instrument after repair.

f. SECTION VI, REPLACEABLE PARTS, contains information required to order all parts and assemblies or effect exchange of assemblies.

g. SECTION VII, MANUAL CHANGES, provides manual change information necessary to document all prefixes listed on the title page. It also contains recommended modifications for the earlier instrument configuration.

h. SECTION VIII, SERVICE, contains descriptions of the circuits, schematic diagrams, parts location diagrams, and troubleshooting procedures to aid the user in maintaining the instrument.

1-4. Figure 1-1 shows the Extension Module with included accessories.

1-5. On the title page of this manual, below the manual part number, is a "Microfiche" part number. This number may be used to order 4x6-inch microfilm transparencies of the manual. Each microfiche contains up to 60 photo-duplicates of the manual pages. The microfiche package also includes the latest Manual Changes supplement as well as all pertinent Service Notes.

1-6. SPECIFICATIONS

1-7. Specifications for the Extension Module and RF Section plug-ins are combined. Refer to the RF Section manual for the combined specifications.

1-8. INSTRUMENTS COVERED BY MANUAL

1-9. This instrument has a two-part serial number. The first four digits and the letter comprise the serial number prefix. The last five digits form the sequential suffix that is unique to each instrument. The contents of this manual apply directly to instruments having the same serial number prefix(es) as listed under SERIAL NUMBERS on the title page.

1-10. An instrument manufactured after the printing of this manual may have a serial prefix that is not listed on the title page. This unlisted serial prefix indicates that the instrument is different from those documented in this manual. The manual for this instrument is supplied with a yellow Manual Changes supplement that contains "change information" that documents the differences.

1-11. In addition to change information, the supplement may contain information for correcting errors in the manual. To keep this manual as current and accurate as possible, Hewlett-Packard recommends that you periodically request the latest Manual Changes supplement. The supplement for this manual is keyed to this manual's print date and part number, both of which appear on the title page. Complimentary copies of the supplement are available from Hewlett-Packard.

1-12. For information concerning a serial number prefix not listed on the title page or in the Manual Changes supplement, contact your nearest Hewlett-Packard office.

1-13. DESCRIPTION

1-14. The HP Model 11661B Frequency Extension Module plug-in extends the output frequency range of the mainframe to meet the input requirements of high-frequency RF Section plug-ins (>160 MHz). The Extension Module contains two high-frequency phase-locked loops which receive digital tuning signals, variable synthesized signals, and fixed synthesized signals from the mainframe. The phase-locked loops use the mainframe signals in conjunction with a 4.43 GHz oscillator output that is common to both loops to produce two high-frequency output signals. One output signal is generated by a phase-locked summing loop using a Voltage Controlled Oscillator (VCO) that is tuneable in 1 Hz steps (100 Hz steps for option 004 mainframe) over the 3.95 to 4.05 GHz range. The other output signal is generated by a phase-locked loop using a Yittrium-Iron -Garnet (YIG) oscillator that is tunable in 100 MHz steps over the 2.75 to 3.95 GHz range. Since both phase-locked loops use the same 4.43 GHz oscillator, variations in the oscillator frequency do not affect the frequency difference between the summing loop and YIG loop outputs. The two output signals from the Extension Module are coupled to the RF Section plug-ins for mixing, amplification of the converted signal, and final output power level control.

1-15. Frequency modulation (FM) of the YIG loop output can be effected by supplying a frequency modulated reference signal instead of a fixed reference signal, to a phase detector in the phase-locked YIG loop. Thus, as the frequency modulated reference signal varies, the YIG loop output frequency varies accordingly.

1-16. ACCESSORIES SUPPLIED

1-17. Two coaxial cables, HP Part Numbers 11661-60026 (Gray-blue) and 11661-60028 (Gray), are supplied with the Extension Module. The cables are used to interconnect the YIG and SUM loop outputs to the RF Section inputs. The accessories are shown with the Extension Module in Figure 1-1.

1-18. EQUIPMENT REQUIRED BUT NOT SUPPLIED

1-19. Each Frequency Extension Module is installed in a plug-in cavity within an 8660-series mainframe. Logic control inputs, RF inputs, and power supply inputs are connected directly from the mainframe or through a compatible 86600-series RF Section plug-in to the Extension Module. The Extension Module outputs are connected to the RF Section.

1-20. The Synthesized Signal Generator System requires installation of an Auxiliary or Modulation Section. The only direct interaction between a Modulation Section and the Extension Module occurs when a frequency modulated RF output is selected. A 86630-series plug-in with FM capability couples a frequency modulated RF signal to the Extension Module. The FM portion of this signal is superimposed on an RF output to the RF Section.

1-21. EQUIPMENT AVAILABLE

1-22. An extender cable, HP Part Number 11672-60002, is required to extend the Extension Module for maintenance purposes. The extender cable is part of the HP 11672A Service Kit but may be ordered separately.

1-23. Extender cards used in servicing the Extension Module are contained in the HP Kit, Part Number 08660-60070, which is supplied with the mainframe.

1-24. RECOMMENDED TEST EQUIPMENT

1-25. Table 1-1 lists the test equipment and accessories recommended for use in testing, adjusting, and servicing the Extension Module. If any of the recommended test equipment is unavailable, instruments with equivalent specifications may be used.

1-26. SAFETY CONSIDERATIONS

1-27. This instrument has been designed in accordance with international safety standards and has been supplied in safe condition.

1-28. Although this instrument has been designed in accordance with international safety standards, this manual contains information, cautions, and warnings which must be followed to retain the instrument in safe condition. Be sure to read and follow the safety information in Sections II, V, and VIII.

Table 1-1. Recommended Test Equipment

Item	Critical Specifications	Suggested Model	Use*
Digital Voltmeter	Accuracy: ±0.2% Range: 0.00 to 60 Volts	HP 34740 with HP 34702A	A,T
Oscilloscope	Vertical: Bandwidth 50 MHz with sensitivity of 5 mV/division minimum Horizontal: Sweep time 10 ns to 1s Delayed sweep External triggering to 100 MHz	HP 180A with HP 1801A and HP 1821A plug-ins	A.T
10:1 divider probe	10:1 divider 10 Megohm 10 pF	HP 10004	A,T
Spectrum Analyzer	Absolute Accuracy ±1.6 dB from 10 MHz to 1.3 GHz Measurement Accuracy ±2.6 dB from 10 MHz to 1.3 GHz	HP 8555A with HP 8552B and HP 140T	A,T
Test Oscillator	1 kHz to 20 kHz 0.2 to 2.0 Vrms into 50Ω	HP 651B	A
Microwave Frequency Counter	Range: 0.2—4430 MHz Resolution: 1 Hz	HP 5340A	A,T
Frequency Synthesizer	20 to 30 MHz settable in 1 Hz increments Phase Modulation ±3 radians deviation	HP 5105A/5110B	A
VHF Oscillator	10 to 30 MHz Leveled Output	HP 8654A	A
Extender Board	24 Contact (2 x 12 pins) Supplied with mainframe rack mounting kit.	HP 5060-0258	A,T
Step Attenuator (10 dB)	0 to 120 dB in 10 dB steps Range: 10 to 550 MHz Accuracy: ±1.5 dB to 90 dB	HP 355D	A
Service Kit	Interconnect cables, adaptors coaxial cables compatible to 8660-series plugs and jacks	HP 11672A (see Operating Note or mainframe manual for parts list)	A,T

*A = Adjustments; T = Troubleshooting

SECTION II
INSTALLATION

2-1. INTRODUCTION

2-2. This section contains information related to the initial inspection, preparation for use, and storage and shipping instructions for the Frequency Extension Module.

2-3. INITIAL INSPECTION

NOTE

If the Extension Module has been received as part of a signal generator system (8660-series Option 100), for mechanical inspection purposes the module should be considered part of the mainframe. Refer to the RF Section manual for information related to electrical inspection.

2-4. Inspect the shipping container for damage. If the shipping container or cushioning material is damaged, it should be kept until the contents of the shipment have been checked for completeness and the instrument has been checked mechanically and electrically. The contents of the shipment should be as shown in Figure 1-1, and procedures for checking electrical performance are given in the RF Section manual. If the contents are incomplete, if there is mechanical damage or defect, or if the instrument does not pass the electrical performance test, notify the nearest Hewlett-Packard office. If the shipping container is damaged, or the cushioning material shows signs of stress, notify the carrier as well as the Hewlett-Packard office. Keep the shipping materials for carrier's inspection. The HP office will arrange for repair or replacement without waiting for claim settlement.

2-5. PREPARATION FOR USE

2-6. Power Requirements

2-7. The power consumed by the Frequency Extension Module during normal operation is 35 V·A maximum.

2-8. Interconnections

2-9. Installing the Extension Module into the mainframe plug-in cavity ensures all necessary connections are made to the mainframe and the Modulation Section plug-in. Two coaxial cable accessories also must be installed to complete necessary connections to the RF Section plug-in.

2-10. Operating Environment

2-11. The Extension Module is designed to operate a mainframe which is operating within the following environment conditions:

Temperature	0 to 55°C (+32 to +131°F)
Humidity	less than 95% relative
Altitude	less than 4500 m (15 000 feet)

2-12. Installation Instructions

2-13. Safety Considerations. During installation of the Extension Module, the top and bottom protective covers of the mainframe are removed. Energy available at many points may, if contacted, result in personal injury.

WARNINGS

1. Disconnect line (Mains) power cable from mainframe to remove available energy.

2. Capacitors inside the instrument may still be charged even if the system has been disconnected from its source of supply.

3. The multi-pin connector (mounted on mainframe) which provides interconnection to the Extension Module exposes power supply voltages which may remain after the power cable is disconnected from mainframe.

2-14. Order of Installation Procedures. If the Extension Module is being installed in the mainframe for the first time, perform the following

procedures in the order listed. To reinstall the Extension Module, perform only the Extension Module Installation.

a. Accessory Cable Installation, Figure 2-1.

b. Extension Module Installation, Figure 2-2.

c. Abbreviated Adjustment procedure in Section V.

2-15. STORAGE AND SHIPMENT

2-16. Environment

2-17. The storage and shipping environment of the Extension Module should not exceed the following limits:

Temperature	−40° to +75° C (−40° to +167° F)
Humidity	less than 95%, relative
Altitude	less than 7600 m (25 000 feet)

2-18. Packaging

2-19. Original Type Packaging. Containers and materials identical to those used in factory packaging are available through Hewlett-Packard offices. If the instrument is being returned to Hewlett-Packard for servicing, attach a tag indicating the type of service required, return address, model number, and full serial number. Also, mark the container FRAGILE to assure careful handling. In any correspondence, refer to the instrument by model number and full serial number.

2-20. Other Packaging. The following general instructions should be used for re-packaging with commercially available materials:

a. Wrap the instrument in heavy paper or plastic. (If shipping to a Hewlett-Packard office or service center, attach a tag indicating the type of service required, return address, model number, and full serial number.)

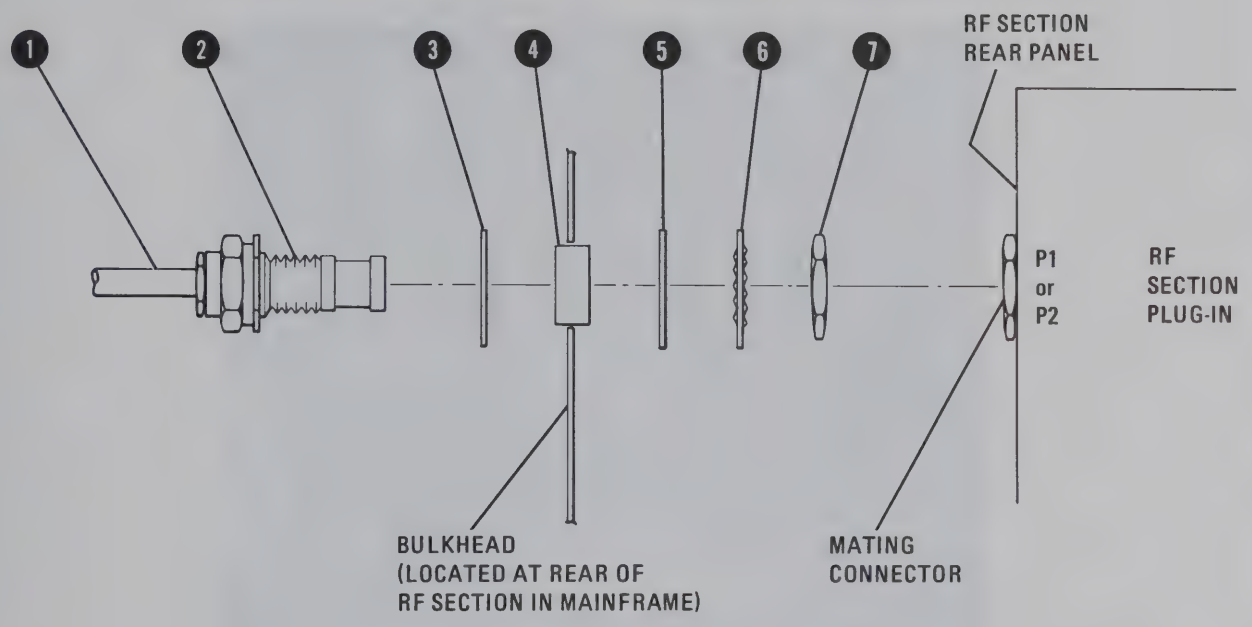
b. Use a strong shipping container. A double-wall carton made of 2.4 MPa (350 psi) test material is adequate.

c. Use enough shock-absorbing material (75—100 mm) around all sides of the instrument to provide firm cushion and prevent movement inside the container.

d. Seal the shipping container securely.

e. Mark the shipping container FRAGILE to assure careful handling.

BULKHEAD MOUNTING OF ACCESSORY CABLES



WARNING

Before installing the cables, read the paragraphs under the heading "Installation Instructions" in this section.

a. Take the grey Coaxial Cable (1) and place the Flat Washer (3) on the Sealectro Bulkhead Connector (2).

b. Insert the connector through the Spacer (4) which is mounted in a hole on the Bulkhead. The grey cable is mounted below the multi-pin connector (refer to Figure 2-2).

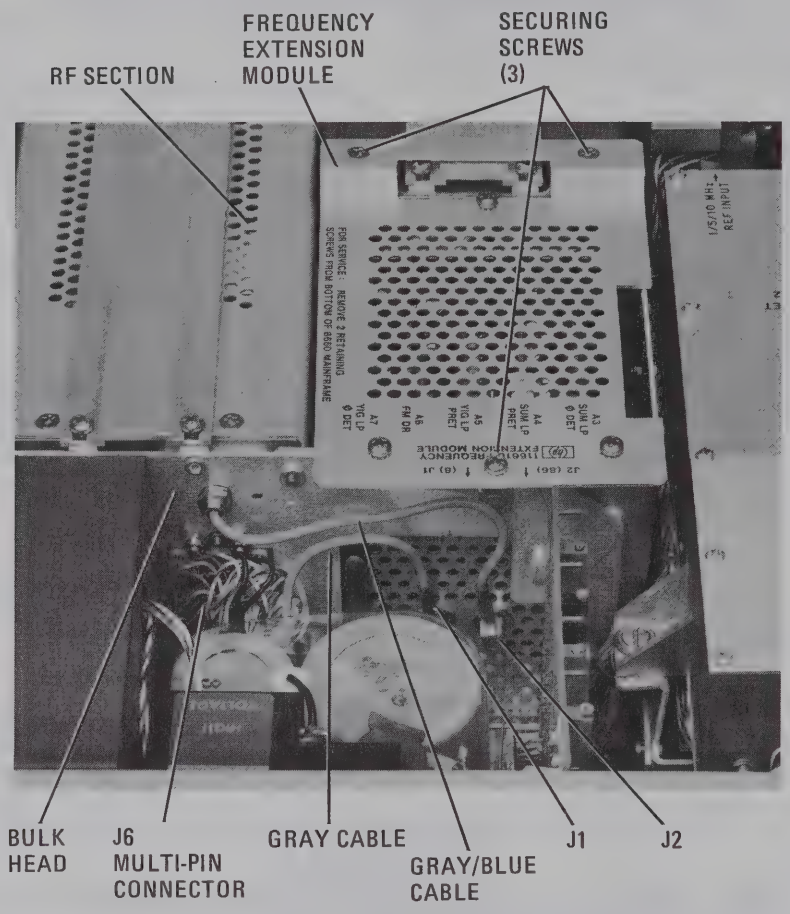
c. Place the Flat Washer (5) and the Lock Washer (6) over the protruding end of the Bulkhead Connector.

d. Secure the connector by threading and tightening the Hex Nut (7) onto the connector. The connector should have approximately 1/32-inch play in all directions.

e. Follow steps a. through d. in mounting the grey-blue cable in the Bulkhead above the multi-pin connector.

Figure 2-1. Accessory Cable Installation

EXTENSION MODULE INSTALLED IN MAINFRAME



WARNING

Before installing the Extension Module read the paragraphs under the heading "Installation Instructions" in this Section.

a. Position the Frequency Extension Module above the plug-in cavity with the multi-pin connector of the Extension Module below and on the right (as viewed from rear of mainframe). J1 and J2 should face the rear of the mainframe (refer to figure).

b. Lower the Extension Module into place in the mainframe.

c. Make sure the multi-pin connector mates properly with the mainframe connector and press the Extension Module into place.

d. Secure the Module in place with 5 Pozi-driv screws, 3 from the top as shown in the figure and 2 from the bottom of the mainframe.

e. Press the free end of the grey accessory cable into J1 and the grey-blue cable into J2 as shown in the figure.

Figure 2-2. Extension Module Installation

SECTION III
OPERATION

3-1. INTRODUCTION

3-2. The operation of the Frequency Extension Module is dependent on the Model 8660-series mainframe (frequency control) and the Model 86630-series Modulation Section plug-in. Refer to Section III of the appropriate manual for operating information.

SECTION IV
PERFORMANCE TESTS

4-1. INTRODUCTION

4-2. The performance of RF Sections which have a high frequency limit greater than 160 MHz is dependent on the performance of the Frequency Extension Module. Refer to Section IV of the appropriate RF Section Operating and Service Manual for combined performance tests.

SECTION V
ADJUSTMENTS

5-1. INTRODUCTION

5-2. This section contains adjustment procedures which will return the Frequency Extension Module to peak operating condition. An abbreviated procedure is included to adjust an Extension Module the first time it is used with a mainframe so they will operate with each other in the system.

5-3. The Extension Module should be adjusted after any repair or if the unit, in conjunction with the RF Section, fails to meet the performance tests of Section IV in the RF Section manual. Prior to making any adjustment, let the complete system warm up for 15 minutes.

5-4. The order in which the adjustments are made is critical. Perform the adjustments in sequence and under the conditions presented in this section. DO NOT attempt to make random adjustments to the instrument. The Abbreviated Adjustments are independent and are to be performed only under special conditions. Prior to making any adjustments to the Frequency Extension Module, refer to the paragraph entitled Related Adjustments.

5-5. EQUIPMENT REQUIRED

5-6. Each adjustment procedure in this section contains a list of test equipment and accessories required to perform the adjustment. The test equipment is also identified by callouts in the test setup diagrams included with each procedure.

5-7. If substitutions must be made for the specified test equipment, refer to Table 1-1 for the minimum specifications of the test equipment to be used in the adjustment procedures. Since the Synthesized Signal Generator System is extremely accurate, it is particularly important that the test equipment used in the adjustment procedures meets the critical specifications listed in Table 1-1.

5-8. The HP 11672A Service Kit is an accessory item available from Hewlett-Packard for use in maintaining the Frequency Extension Module. A detailed listing of the items contained in the service kit is provided in the HP 11672A Operating Note and the mainframe manual. Each item may be ordered separately.

5-9. Extender cards used in servicing the Extension Module are contained in the HP Kit, Part Number 08660-60070, which is supplied with the mainframe.

5-10. SAFETY CONSIDERATIONS

5-11. Although this instrument has been designed in accordance with international safety standards, this manual and the system mainframe manual contain information, cautions, and warnings which must be followed to ensure safe operation and to retain the complete system in safe condition. Service adjustments should be performed only by qualified service personnel.

NOTE

Refer to the mainframe manual for safety information relating to ac line (Mains) voltage, fuses, protective earth grounding, etc.

5-12. Any adjustment, maintenance, and repair of the opened instrument under voltage should be avoided as much as possible and, when inevitable, should be carried out only by a skilled person who is aware of the hazard involved.

5-13. Capacitors inside the instrument may still be charged even if the instrument has been disconnected from its source of supply.

WARNINGS

1. Adjustments described herein are performed with power supplied to the instrument while protective covers are removed. Energy available at many points may, if contacted, result in personal injury.

2. The multi-pin plug connector (on mainframe), which provides interconnection to the Extension Module will expose power supply voltages which may remain on the pins after the Extension Module is removed and after the (Mains) power cable is disconnected from the mainframe. Be careful to avoid contact with the pins during interconnection with the Extension Module.

5-14. FACTORY-SELECTED COMPONENTS

5-15. Factory-selected components are identified on the schematics and parts list by an asterisk which follows the reference designator. The nominal value of the components are normally shown. The manual change sheets will provide updated information pertaining to the selected components. Table 5-1 lists the reference designator, the criterion used for selecting a particular value, the normal value range, and the service sheet where the component part is shown.

5-16. RELATED ADJUSTMENTS

5-17. The adjustment procedures found in this section are normally performed in sequence. The Abbreviated Adjustment procedure is independent and is performed only when an Extension Module is being used with a mainframe for the first time.

5-18. If the 4.43 GHz Oscillator is adjusted, the procedures which follow must all be performed.

5-19. If the 20 MHz IF Amplifier Adjustment is performed, the YIG Pretune Driver Adjustment, the YIG Loop Phase Detector Adjustment, and the YIG Loop Gain and Bandwidth Adjustment must be performed in sequence.

5-20. If the 3.95 to 4.05 GHz VCO Bias Adjustment is performed, the Sum Loop Pretune Adjustment and the Sum Loop Bandwidth Adjustment must be performed in sequence.

5-21. Only the Abbreviated Adjustment, the YIG Loop Gain Bandwidth Adjustment, and the Sum Loop Bandwidth Adjustment are independent of other procedures. The final checks of the Abbreviated Adjustment procedure indicate if the other procedures need to be performed.

5-22. ADJUSTMENT LOCATIONS

5-23. The last foldout in this manual contains a table which cross-references all pictorial and schematic locations of the adjustable controls. The figure accompanying the table shows the locations of assemblies, chassis mounted parts, adjustable components, and test points.

5-24. ADJUSTMENTS

5-25. Prior to performing the adjustments on the Extension Module, remove the mainframe and Extension Module top covers. Refer to the disassembly procedures found on the lefthand foldout page which preceeds the last foldout in this manual.

5-26. Prior to performing the COMPLETE adjustment procedures remove the five circuit board assemblies (A3 through A7).

Table 5-1. Factory Selected Components

Reference Designator	Selected For	Normal Value	Service Sheet
A4C8*	Sum Loop Bandwidth (3 dB down) of 500 ± 150 kHz with center frequency set to 1.095 GHz. Increasing capacitance increases bandwidth.	200 to 330 pF	7
A3L2	Phase lock with an increase in center frequency (10 MHz steps). Monitor A3TP1 with an oscilloscope. A dc level is observed if Sum Loop is phase locked as opposed to an ac signal when unlocked. DC level should be observed in each of the following cases: set system center frequency to 99.9 MHz; then to 109.9 MHz. Set to 299.9 MHz; then 309.9 MHz. Set to 399.9 MHz; then 409.9 MHz. Set to 799.9 MHz; then 809.9 MHz.	5.6 to 12.0 μH	6
A6C7	Selected for YIG loop bandwidth of ±150 kHz (increased capacitance increases bandwidth).	100 to 1000 pF (100 pF nom.)	5
A1A2R10	Selected to keep power level out of IF Amplifier in the −8 to −3 dBm range, this controls the "J" and "F" spurs.	No resistor or if selected in the range of 51.1 to 196Ω.	3

*Selection of A4C8 is part of the Sum Loop Bandwidth Adjustment Procedure.

ADJUSTMENTS

5-27. ABBREVIATED ADJUSTMENT OF FREQUENCY EXTENSION MODULE

REFERENCE:

Service Sheets 5 and 7.

DESCRIPTION:

Each time a Frequency Extension Module is inserted into a different mainframe, minor adjustments must be made to the Extension Module to ensure proper operation of the entire signal generator system. Mainframe power supplies are checked and adjustments are made if necessary. An adjustment of the 4.43 GHz oscillator is done. The Sum Loop Pretune Assembly Outputs are adjusted and rechecked along with the 4.43 GHz oscillator. The dc voltage levels at A6TP1 of the YIG Loop Pretune Assembly are measured at specific preset frequencies.

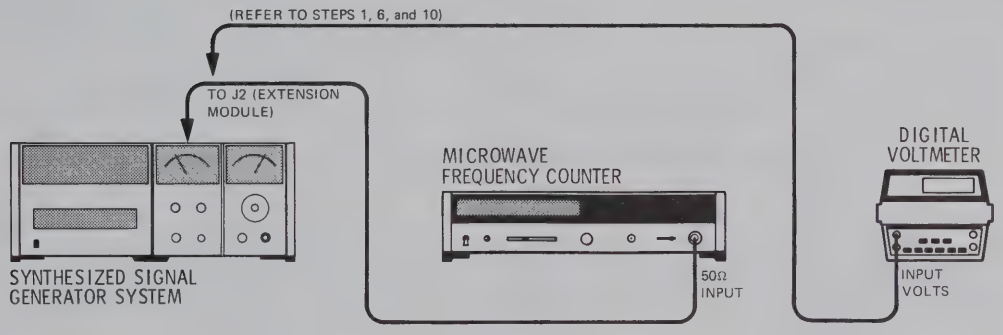


Figure 5-1. Abbreviated Adjustment Test Setup

EQUIPMENT:

Microwave Frequency Counter	HP 5340A
Digital Voltmeter	HP 37470A/34702A

PROCEDURE:

1. Check the regulated power supply voltages in the mainframe (refer to Section VIII of the mainframe manual for the figure entitled Assembly Locations).

NOTE

DO NOT adjust the voltages if they are within tolerance.

Mainframe Test Point	Voltage and Tolerance (Vdc)
A5TP1	−40.00 ± 0.02
A5TP2	−10.00 ± 0.01
A5TP3	+20.000 ± 0.005
A5TP4	+ 5.25 ± 0.02

ADJUSTMENTS

5-27. ABBREVIATED ADJUSTMENT OF FREQUENCY EXTENSION MODULE (Cont'd)

2. Connect the RF SIGNAL OUTPUT from J2 of the Extension Module to the high frequency input of the microwave frequency counter.

3. Set the mainframe center frequency to 1200 MHz.

4. Set the A14R2 control for an output from J2 (monitored by the microwave frequency counter) of 2750.000 ±0.100 MHz. (This indirectly sets the frequency of the 4.43 GHz Oscillator.)

5. Set the mainframe LINE switch control to STNDBY and place the A4 Assembly on an extender board.

6. Return the LINE switch to ON and Monitor the dc voltage on A3TP1 with a digital voltmeter.

7. As shown by the table, set the center frequency and adjust the appropriate control for a reading of 11.0±0.5 Vdc on the digital voltmeter.

Center Frequency	Adjustable Control	
	Name	Reference Designator
5 MHz	B Adj	A4R6
15 MHz	1 Adj	A4R10
25 MHz	2 Adj	A4R16
35 MHz	3 Adj	A4R20
45 MHz	4 Adj	A4R23
55 MHz	5 Adj	A4R26
65 MHz	6 Adj	A4R29
75 MHz	7 Adj	A4R32
85 MHz	8 Adj	A4R35
95 MHz	9 Adj	A4R38

8. Recheck the voltage readings at each center frequency setting (step 7).

9. Recheck the 4.43 GHz Oscillator frequency (see steps 3 and 4). If necessary, repeat steps 3 through 9.

10. Monitor the dc voltage on A6TP1 with the DVM while programming in 100 MHz steps from 0 (zero) to 1200 MHz (i.e., 0 MHz, 100 MHz, 200 MHz . . . 1200 MHz). The dc voltage should be 0.0 ± 0.3 Vdc for each frequency setting.

NOTE

If the voltage at any frequency setting is >± 0.5 Vdc, perform the rest of the adjustment procedures in this section.

ADJUSTMENTS

5-28. 4.43 GHz OSCILLATOR ADJUSTMENT

REFERENCE:

Service Sheet 3

DESCRIPTION:

The 4.43 GHz Oscillator output is monitored by a frequency counter while the frequency is adjusted.

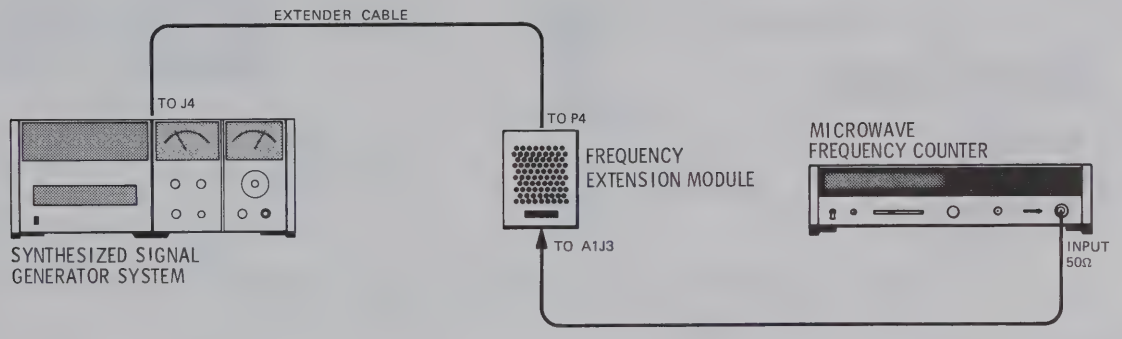


Figure 5-2. 4.43 GHz Oscillator Adjustment Test Setup

EQUIPMENT:

Microwave Frequency Counter	HP 5340A
Extender Cable	HP 11672-60002

PROCEDURE:

1. Interconnect equipment as illustrated in Figure 5-2.
2. Connect microwave frequency counter input to the 4.43 GHz OUT connector A1J3.
3. Adjust potentiometer A14R2 for a 4.4300 ± 0.0005 GHz as indicated by the microwave frequency counter.

5-29. 20 MHz IF AMPLIFIER ADJUSTMENT

REFERENCE:

Service Sheet 3

DESCRIPTION:

A 20 MHz signal from the mainframe is attenuated and injected at the input of the 20 MHz IF amplifier. The output is monitored with an oscilloscope and the 20 MHz ADJ control is set for the peak signal output.

ADJUSTMENTS

5-29. 20 MHz IF AMPLIFIER ADJUSTMENT (Cont'd)

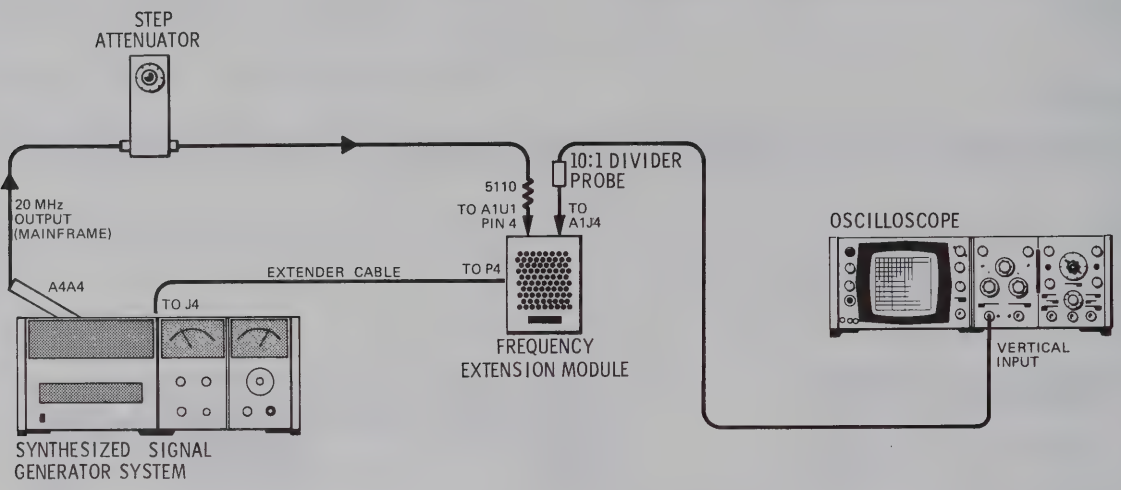


Figure 5-3. 20 MHz IF Amplifier Adjustment Test Setup

EQUIPMENT:

Oscilloscope	HP 180A/1801A/1821A
10:1 Divider Probe	HP 10004
Step Attenuator (10 dB)	HP 355D
Extender Cable	HP 11672-60002
5110 Ohm 1/4 Watt Resistor	HP 0757-0438

PROCEDURE:

1. Remove the A1A1, A1A3, and A1A4 Assemblies' cover. Refer to the disassembly procedures on the lefthand foldout page which preceeds the last foldout.

2. Disconnect W4 from A1J4; W3 from A1J2.

3. Set the step attenuator controls for 20 dB attenuation.

4. Connect the equipment together as shown in Figure 5-3.

5. Set the oscilloscope controls to monitor the 20 MHz signal (amplitude normally about 1 Vp-p).

6. Peak the 20 MHz output as seen on the oscilloscope display by adjusting the A1A1C1 control.

7. Disconnect the equipment, connect W4 to A1J4, connect W3 to A1J2, and replace the A1A1, A1A3, and A1A4 Assemblies' cover. Reconnect the correct cable to the 20 MHz OUTPUT on the mainframe A4A4 Assembly.

ADJUSTMENTS

5-30. 3.95 to 4.05 GHz VCO BIAS ADJUSTMENT

REFERENCE:

Service Sheet 3

DESCRIPTION:

The VCO Bias Adj control sets the bias voltage of the 3.95 to 4.05 GHz oscillator.

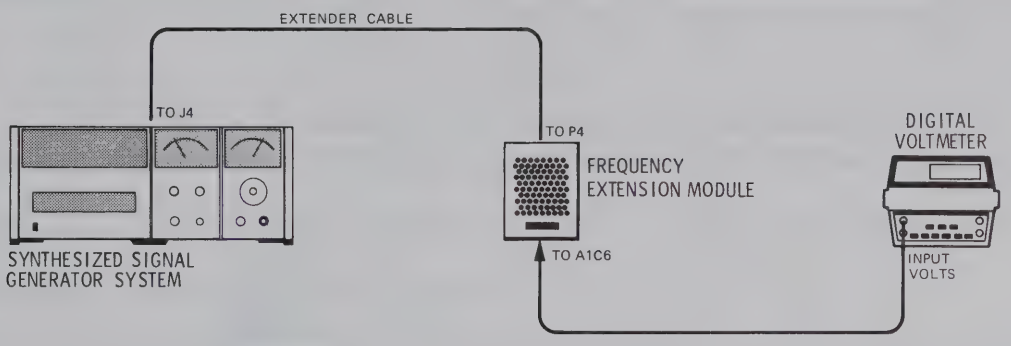


Figure 5-4. 3.95 to 4.05 GHz VCO Bias Adjustment Test Setup

EQUIPMENT:

Digital Voltmeter	HP 34740A/34702A
Extender Cable	HP 11672-60002

PROCEDURE:

1. Connect the digital voltmeter to the feedthrough capacitor A1C6. Refer to the last foldout for the location of A1C6.

2. Adjust the VCO bias control A14R3 for +9.5 Vdc as indicated on the DVM.

ADJUSTMENTS

5-31A. YIG PRETUNE DRIVER ADJUSTMENT (8660A MAINFRAMES)

REFERENCE:

Service Sheet 2.

DESCRIPTION:

Adjustments are made to the YIG Pretune Driver controls while the YIG drive voltage and YIG output are monitored by a DVM and an oscilloscope respectively. The GAIN ADJ control sets the range of the YIG drive voltage with the mainframe center frequency set to 0.0 GHz (less significant digits do not affect the adjustment). The digital-to-analog converter controls are then adjusted for specific YIG oscillator output frequencies which correspond to preset center frequencies.

NOTE

Due to hysteresis of the YIG oscillator,different adjustment procedures are provided depending on the frequency control capabilities of the mainframe.

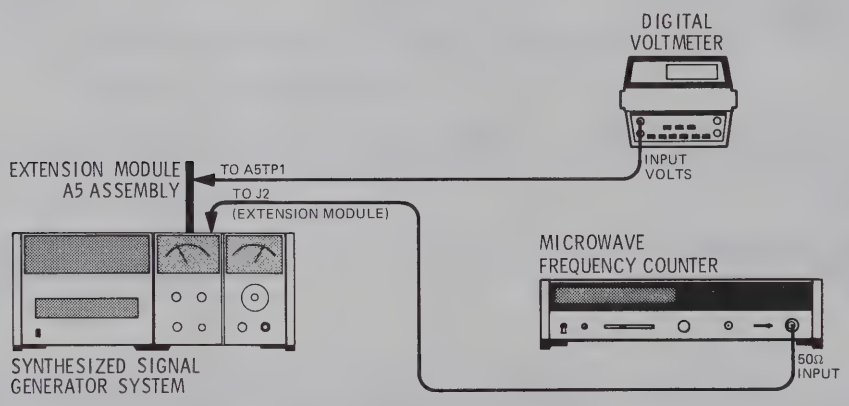


Figure 5-5. YIG Pretune Driver Adjustment Test Setup

EQUIPMENT:

Digital Voltmeter	HP 34740A/34702A
Microwave Frequency Counter	HP 5340A

PROCEDURE:

1. Prior to installing the A5 YIG Pretune Driver Assembly into the Extension Module, center the adjustment potentiometers so the DVM indicates resistance values in accordance with those listed in Table 5-2. Measure the resistance on the resistance scales of the DVM.

ADJUSTMENTS

5-31A. YIG PRETUNE DRIVER ADJUSTMENT (8660A MAINFRAMES) (Cont'd)

Table 5-2. Preliminary Resistance Settings of YIG Pretune Driver Adjustment Potentiometers

Potentiometer	Function	Centered Value
A5R39	Gain Adj	100 Ohms
A5R29	Offset Adj	100 Ohms
A5R13	"1" Adj	1000 Ohms
A5R15	"2" Adj	500 Ohms
A5R17	"4" Adj	250 Ohms
A5R19	"8" Adj	100 Ohms
A5R21	"10" Adj	100 Ohms

2. Install the A5 circuit board in the Extension Module (A6 should NOT be installed at this time).

3. Connect the microwave frequency counter to the Extension Module output jack J2.

4. Set the system center frequency to 0 (zero) GHz.

5. Adjust the Gain Adj. control A5R39 for an output frequency from J2 of 3.950 ± 0.001 GHz. Record the frequency to 5 significant digits.

_____ GHz

6. Set the center frequency to 1 GHz and record the J2 output frequency to 5 significant digits.

_____ GHz

7. Calculate the difference frequency from the recorded values of steps 5 and 6. If the frequency is 1.0000 ± 0.0005 GHz, proceed to the step 11.

8. If the tolerance of the difference frequency is not achieved, set the Offset control A5R29 for a frequency output of 2.950 ± 0.001 GHz. Record the frequency to 5 significant figures.

_____ GHz

9. Set the center frequency back to 0 (zero) GHz. Record the difference frequency to five significant figures.

_____ GHz

10. Calculate the difference frequency from those recorded in steps 8 and 9. If the frequency difference is 1.0000 ± 0.0005 GHz proceed to step 11. If the difference frequency tolerance is not achieved, repeat steps 5 through 9 until the tolerance is achieved.

NOTE

The following series of adjustments must be performed in the exact manner stated in order to eliminate errors due to YIG hysteresis.

ADJUSTMENTS

5-31A. YIG PRETUNE DRIVER ADJUSTMENT (8660A MAINFRAMES) (Cont'd)

11. Set the system center frequency to 0 (zero) MHz.

12. Set the center frequency to 100 MHz and adjust the appropriate control for the correct output frequency from J2 (refer to Table 5-3). Repeat this process at 200, 400, and 800 MHz ALWAYS INCREASING the frequency to the next setting. Record the frequency to five significant digits.

NOTE

If any one of the "1" through "8" controls needs more range (set full CW or CCW) the "10 Adj" control, which is normally centered, may be reset to bring the frequencies within the required tolerance. (To increase the frequency, the "10 Adj" control A5R21 should be set more CCW.) If the "10 Adj" Control is reset, repeat steps 11 and 12.

Table 5-3. YIG Pretune Drive Digital-To-Analog Convertor Adjustments

Center Frequency (MHz)	Adjust	J2 Output Frequency and Tolerance (GHz)	Actual Frequency in GHz
100	A5R13	3.8500 ± 0.0010	_____
200	A5R15	3.7500 ± 0.0010	_____
400	A5R17	3.5500 ± 0.0010	_____
800	A5R19	3.1500 ± 0.0010	_____
1100	A5R13	2.8500 ± 0.0010	_____
1200	A5R15	2.7500 ± 0.0010	_____

13. INCREASE the center frequency to 1100 MHz. If the frequency is close to the tolerance limit or out of tolerance, set the A5R13 control for a frequency closer to the desired frequency shown in Table 5-3.

14. Set the center frequency to 0 (zero) MHz; then to 100 MHz. Check the frequency from J2. Knowing how much the frequency changed from the original 100 MHz setting, reset the A6R13 control so the actual frequency is as close to the desired frequency (Table 5-3) as possible for both the 100 and 1100 MHz center frequencies.

15. Set the center frequency to 1200 MHz. Repeat steps 13 and 14 for the 200 and 1200 MHz center frequencies.

16. Set the system center frequency to 0 MHz, then to the frequencies listed in Table 5-4. Verify the output frequency from J2 is within tolerance. If any of the frequencies are not within tolerance, repeat this entire procedure.

ADJUSTMENTS

5-31A. YIG PRETUNE DRIVER ADJUSTMENT (8660A MAINFRAMES) (Cont'd)

Table 5-4. Center Frequency versus YIG Loop Output

Center Frequency (MHz)	YIG Loop Output Frequency From J2 (GHz)
0	3.9500 ± 0.0015
100	3.8500 ± 0.0015
200	3.7500 ± 0.0015
300	3.6500 ± 0.0015
400	3.5500 ± 0.0015
500	3.4500 ± 0.0015
600	3.3500 ± 0.0015
700	3.2500 ± 0.0015
800	3.1500 ± 0.0015
900	3.0500 ± 0.0015
1000	2.9500 ± 0.0015
1100	2.8500 ± 0.0015
1200	2.7500 ± 0.0015

5-31B. YIG PRETUNE DRIVER ADJUSTMENT (8660B/C MAINFRAMES)

REFERENCE:

Service Sheet 2.

DESCRIPTION:

Adjustments are made to the YIG Pretune Driver controls while the YIG drive voltage and YIG output are monitored by a DVM and an oscilloscope respectively. The GAIN ADJ control sets the range of the YIG drive voltage with the mainframe center frequency set to 0.0 GHz (less significant digits do not affect the adjustment). The digital-to-analog converter controls are then adjusted for specific YIG oscillator output frequencies which correspond to preset center frequencies.

NOTE

Due to hysteresis of the YIG Oscillator, slightly different adjustment procedures are performed depending on the frequency control capabilities of the mainframe.

EQUIPMENT:

Digital Voltmeter ..... HP 34740A/34702A
Microwave Frequency Counter ..... HP 5340A

ADJUSTMENTS

5-31B. YIG PRETUNE DRIVER ADJUSTMENT (8660B/C MAINFRAMES) (Cont'd)

PROCEDURE:

1. Prior to installing the A5 YIG Pretune Driver Assembly into the Extension Module, center the adjustment potentiometers so the DVM indicates resistance values in accordance with those listed in Table 5-2. Measure the resistance on the resistance scales of the DVM.

2. Install the A5 circuit board in the Extension Module (A6 should NOT be installed at this time).

3. Connect the microwave frequency counter to the Extension Module output jack J2.

4. Set the system center frequency to 0 MHz. Note the frequency of the signal from J2.

   _____MHz

5. Set the center frequency to 1000 MHz in one step. Adjust the Gain Adj control A5R39 for a frequency difference of 1000 ± 1 MHz. Record the frequency.

   _____MHz

6. Repeat steps 5 and 6 until the frequency difference is consistently 1000 ± 1 MHz with each 1000 MHz change in center frequency.

NOTE

Turning A5R39 ccw increases the change in frequency, cw rotation decreases the frequency change.

7. Set the center frequency to 0 MHz in one step.

8. Set the Offset Adj control A5R29 for a frequency reading of 3950 ± 1 MHz.

9. Set the Gain Adj control A5R39 for a frequency reading of 3970 ± 1 MHz.

10. Set A5R29 for 3950 MHz ± 200 kHz.

11. Set the center frequency to 0 MHz. In 100 MHz steps, step the center frequency to 1200 MHz. Then step the frequency back to 0 MHz in 100 MHz steps. Verify the frequency reading (at 0 MHz) of 3949.000 MHz ± 200 kHz. Readjust A5R29 if necessary.

12. Step the frequency in 100 MHz steps to 1200 MHz. Adjust the controls at the appropriate step as shown in the following table. The controls are to be adjusted *only when increasing* the frequency in 100 MHz steps from 0 MHz.

NOTE

If a frequency selection mistake is made, in 100 MHz steps, step up to 1200 MHz, down to 0 GHz, and then up to the desired frequency.

ADJUSTMENTS

5-31B. YIG PRETUNE DRIVER ADJUSTMENT (8660B/C MAINFRAMES) (Cont'd)

Center Frequency (MHz)	Frequency Adj Controls	Yig Loop Output Frequency (MHz)	
		Min	Max
0	Offset Adj	3948.500	3949.500
100	"1" Adj	3850.800	3851.800
200	"2" Adj	3750.800	3751.800
400	"4" Adj	3550.800	3551.800
800	"8" Adj	3150.800	3151.800
1000	"10" Adj	2950.800	2951.800

NOTE

If any of the "1" Adj, "2" Adj, "4" Adj, or "8" Adj controls is out of range, the "10" Adj control must be reset. If the control in question is against the clockwise stop, reset "1" Adj 2½ turns counterclockwise. If the control is full counterclockwise, reset "10" Adj 2½ turns clockwise. If necessary, repeat the procedure beginning with step 4.

13. Step the center frequency from 0 to 1200 MHz and back to 0 MHz in 100 MHz steps. After each step verify that each frequency falls within the tolerance shown in the table. If necessary, repeat step 12.

Center Frequency (MHz)	Yig Loop Output Frequency (MHz)	
	Min	Max
0	3947	3953
100	3847	3853
200	3747	3753
300	3647	3653
400	3547	3553
500	3447	3453
600	3347	3353
700	3247	3253
800	3147	3153
900	3047	3053
1000	2947	2953
1100	2847	2853
1200	2747	2753

14. Step the center frequency to 1200 MHz and back to 0 MHz in a 1200 MHz step. The frequency measured at a center frequency of 1200 MHz should be 2750 +6 MHz. The frequency measured at a center frequency of 0 MHz should be 3950 ± 6 MHz.

ADJUSTMENTS

5-32. YIG LOOP PHASE DETECTOR ADJUSTMENTS

REFERENCE:

Service Sheets 4 and 5.

DESCRIPTION:

The YIG phase lock loop feedback path is opened by removing the 20 MHz signal (which is obtained by mixing and sampling the YIG Oscillator output). The YIG Feedback Loop Gain control is centered, the Phase Ref Adj control is set to trigger the search signal on, and the DC Offset Adj centers the search waveform about 0 Vdc. The feedback path is closed and the Offset Adj on the YIG Pretune Driver Assembly sets the locked search output (a dc level) as close to ground potential as possible. The phase Adj control is set to obtain 90° phase shift between the 20 MHz REF signal and the 20 MHz IF signal. (The quadrature phase detector output is at a maximum negative dc level at 90° phase shift).

The 4.43 GHz Oscillator frequency control is readjusted to obtain a 3.95 GHz output from the YIG Oscillator with the center frequency set to 0.0 GHz (less significant digits do not affect the adjustment).

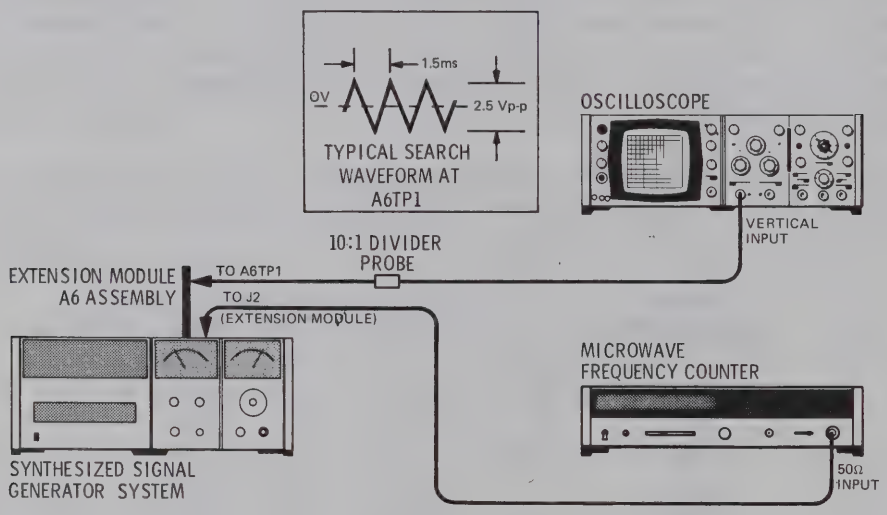


Figure 5-6. YIG Loop Phase Detector Adjustment Test Setup

EQUIPMENT:

Microwave Frequency Counter	HP 5340A
Oscilloscope	HP 180A/1801A/1821A
10:1 Divider Probe	HP 10004

PROCEDURE:

1. Connect YIG FM Driver board assembly A6 to an extender board and insert into the Extension Module.

2. Adjust YIG loop gain potentiometer A7R20 to the center of its range.

3. Connect oscilloscope probe to A7TP1 and adjust A7C2 for most negative dc voltage (typically −1.5 Vdc) as observed on the oscilloscope.

ADJUSTMENTS

5-32. YIG LOOP PHASE DETECTOR ADJUSTMENTS (Con't)

4. Disconnect 20 MHz output cable W4 from A1J4. Center the Search Width Adj control A6R20.

5. Connect oscilloscope probe to A6TP1. Adjust oscilloscope to display a triangular waveform of approximately 2. 5 volts peak-to-peak with a period of approximately 1.5 milliseconds.

6. Adjust DC Offset potentiometer A6R6 so triangular search waveform is centered across 0 Vdc reference line on oscilloscope.

7. Reconnect 20 MHz output cable W4 A1J4. The triangular waveform displayed on the oscilloscope should disappear.

NOTE

When false lock occurs, the output is locked and stable but the output frequency is incorrect.

8. Set the center frequency to 0 (zero) MHz. Then step the frequency in 100 MHz steps to 1200 MHz to verify that loop remains locked at all frequencies.

9. Set the center frequency to 0 (zero) MHz. While monitoring the YIG loop output at J2 with frequency counter, adjust A14R2 so the YIG loop output frequency is 3.9500 ± 0.0005 GHz.

10. Set the center frequency to 0 (zero) MHz and then to 1200 MHz while monitoring dc level at A6TP1. Adjust offset potentiometer A5R29 for best compromise setting that makes A6TP1 level as close to 0 volts as possible for all center frequency settings of 0 to 1200 MHz (100 MHz steps).

5-33. YIG LOOP GAIN AND BANDWIDTH ADJUSTMENT

REFERENCE:

Service Sheet 4

DESCRIPTION:

To simulate phase modulation, a manually swept 19 to 21 MHz signal is superimposed on the 20 MHz second IF signal. The output signal from the RF Section plug-in is monitored by a Spectrum Analyzer. The YIG loop gain is set for the maximum flatness across the 1 MHz bandwidth.

ADJUSTMENTS

5-33. YIG LOOP GAIN AND BANDWIDTH ADJUSTMENT (Cont'd)

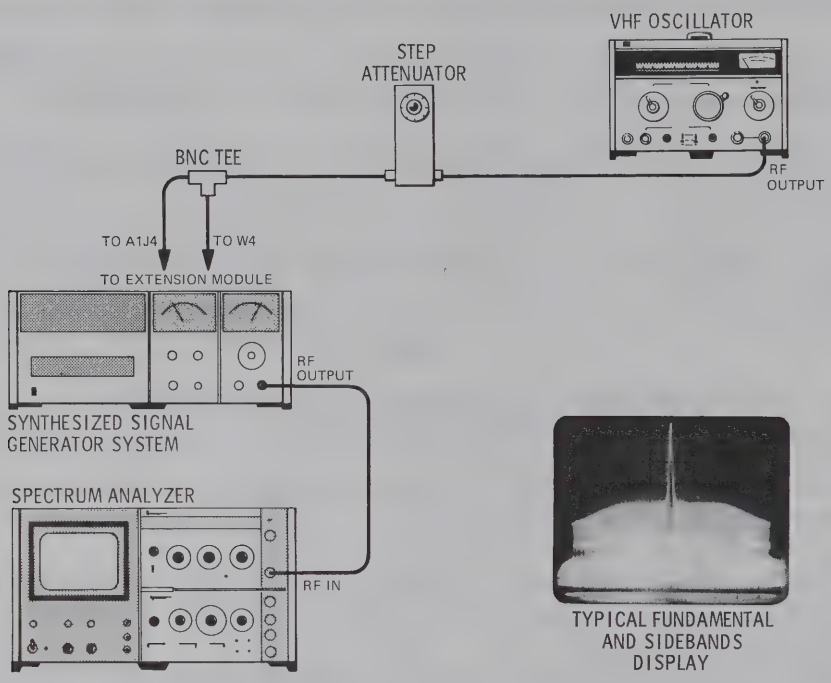


Figure 5-7. YIG Loop Gain and Bandwidth Adjustment Test Setup

EQUIPMENT:

VHF Oscillator	HP 8654A
Step Attenuator (10 dB)	HP 355D
Spectrum Analyzer	HP 8555A/8552B/140T

PROCEDURE:

1. Interconnect equipment as illustrated in Figure 5-7. The TEE connection is made as follows:
   a. disconnect W4 from A1J4.
   b. connect W4 to one port of TEE connector.
   c. connect one port of TEE connector to A1J4.
   d. connect variable attenuator to remaining port of TEE connector.
2. Set Step Attenuator for 60 dB attenuation.
3. Adjust VHF Oscillator output to 19 MHz and set output Vernier to mid-range.
4. Adjust Synthesized Signal Generator mainframe and RF Section output to 400 MHz at −10 dBm.
5. To initiate the YIG loop search, disconnect the 20 MHz reference (unplug and remove the Modulation Section).

ADJUSTMENTS

5-33. YIG LOOP GAIN AND BANDWIDTH ADJUSTMENT (Cont'd)

6. Set the Spectrum Analyzer controls to display the effect of YIG loop seach on the system's RF output signal.
7. Set the Search Width Adj control A6R20 for a search width of 40 ± 4 MHz centered around 400 MHz.
8. Calibrate Spectrum Analyzer to make attenuation measurement.
9. Adjust Spectrum Analyzer for logarithmic display of 400 MHz fundamental plus both sidebands out to 500 kHz from fundamental. Adjust Spectrum Analyzer as follows: BANDWIDTH, 10 kHz; SCAN WIDTH, 200 kHz; SCAN TIME, 5 microseconds; and INPUT ATTENUATION, 20 dB. Use Spectrum Analyzer level controls to adjust display so fundamental peak is near top reference level line.
10. Vary signal generator output frequency from 19 to 21 MHz.
11. Adjust YIG loop gain potentiometer A7R20 until flatness of sidebands (about 40 dB below fundamental) is ⩽ 3 dB within ±500 kHz of fundamental. Refer to Figure 5-7 for illustration of typical waveform. Record the YIG Loop 3 dB bandwidth.

_____ 3 dB BW

5-34. SUM LOOP PRETUNE ADJUSTMENT

REFERENCE:

Service Sheet 6

DESCRIPTION:

The Sum Loop Phase Error output voltage is set by adjusting the Sum Loop Pretune resistance ladder controls (part of the digital-to-analog convertor).

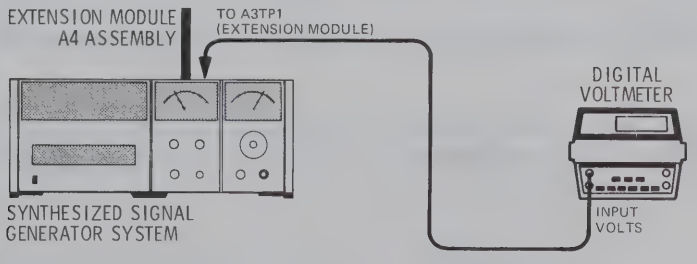


Figure 5-8. Sum Loop Pretune Adjustment Test Setup

EQUIPMENT:

Digital Voltmeter	HP 34740A/34702A

PROCEDURE:

1. Install Sum Loop Phase Detector board assembly A3 into the Extension Module.
2. Center all adjustment potentiometers, including "B" potentiometer, on the A4 Assembly.
3. Connect the A4 Assembly circuit board to an extender board and install it into the Extension Module.
4. Connect Digital voltmeter to A3TP1.

5-34. SUM LOOP PRETUNE ADJUSTMENT (Cont'd)

5. Set Synthesized Signal Generator System center frequency to 5 MHz and adjust A4R5 for a voltage at A3TP1 of +11.0 ± 0.5 Vdc.

6. Set the center frequency in 10 MHz steps from 5 to 95 MHz. Adjust appropriate potentiometer for +11.0 volts level at A3TP1 in accordance with Table 5-5. Adjust potentiometers as close to +11.0 volts as possible.

Table 5-5. Sum Loop Pretune Potentiometer Adjustment

Center Frequency (MHz)	Potentiometer	Function	A3TP1 Level* (Volts)	Sum Loop Frequencies (GHz)
05	A4R5	0 Adj	+11.0 ± 0.5	3.955
15	A4R11	1 Adj	+11.0 ± 0.5	3.965
25	A4R16	2 Adj	+11.0 ± 0.5	3.975
35	A4R20	3 Adj	+11.0 ± 0.5	3.985
45	A4R23	4 Adj	+11.0 ± 0.5	3.995
55	A4R26	5 Adj	+11.0 ± 0.5	4.005
65	A4R29	6 Adj	+11.0 ± 0.5	4.015
75	A4R32	7 Adj	+11.0 ± 0.5	4.025
85	A4R35	8 Adj	+11.0 ± 0.5	4.035
95	A4R38	9 Adj	+11.0 ± 0.5	4.045

*If the range of any of the controls is not enough to bring the voltage into the tolerance shown, repeat the 3.95 to 4.05 GHz VCO Adjustment procedure.

7. Repeat step 6 to verify that all adjustments are within voltage level tolerance.

5-35. SUM LOOP BANDWIDTH ADJUSTMENT

REFERENCE:

Service Sheets 6 and 7.

DESCRIPTION:

A Spectrum Analyzer is used to monitor the RF Section RF OUTPUT while a 25 to 26 MHz signal is injected at the 20 to 30 MHz input to the Frequency Extension Module. The "0" control A4R5 is adjusted to obtain a 3 dB bandwidth equal to the Yig Loop 3 dB bandwidth ±150 kHz, as observed on the Spectrum Analyzer. Then, the "B" control A4R6 is adjusted to maintain approximately +11 Vdc at A3TP1. The external 25 MHz signal is removed from the 20 to 30 MHz input and 20 to 30 MHz signal from the mainframe is reconnected. With the mainframe center frequency set to 1.005 GHz, the A14R2 control is adjusted to vary the 4.43 GHz oscillator frequency until the VCO output is 3.9550 ± 0.0001 GHz. The "B" control A4R6 is readjusted to obtain +11.0 ± 0.5 Vdc at A3TP1. Finally, the center frequency is stepped from 1.005 GHz to 1.095 GHz in 0.010 GHz (10 MHz) steps and the appropriate control listed in Table 5-5 is set to maintain the A3TP1 voltage at +11.0 ± 0.5 Vdc at each frequency.

ADJUSTMENTS

5-35. SUM LOOP BANDWIDTH ADJUSTMENT (Cont'd)

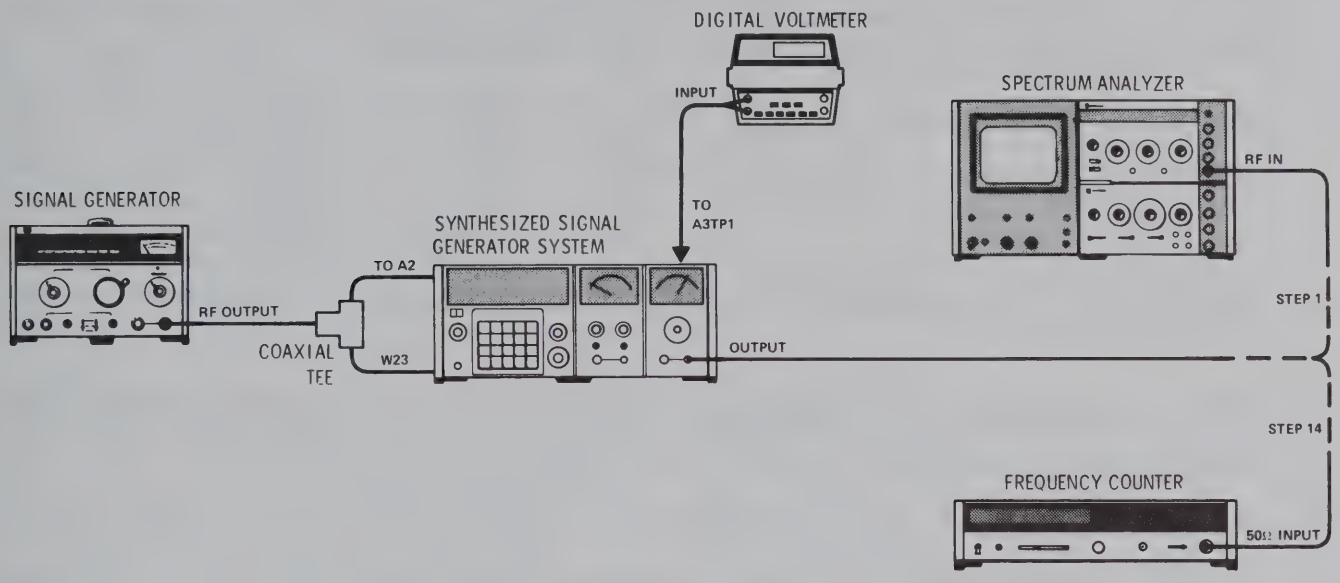


Figure 5-9. Sum Loop Bandwidth Adjustment Test Setup

EQUIPMENT:

Spectrum Analyzer	HP 8555A/8552B/140T
Signal Generator	HP 8654A
Frequency Counter	HP 5340A
Coaxial Tee	HP 1250-0781
Digital Voltmeter	HP 34740A/34702A

PROCEDURE:

1. Remove the left side cover of the mainframe and disconnect the white/orange cable W23 from the A2 Assembly connector. Reconnect the cable through a coaxial tee connector. Connect the signal genenator to the open port of the tee.

2. Set the signal generator controls for an output of −50 dBm at 25 MHz.

3. Set the mainframe center frequency to 1.085 GHz, RF Section OUTPUT RANGE switch to 0 dBm, and adjust the VERNIER control for a 0 dB meter indication.

4. Adjust the spectrum analyzer controls for center frequency 1.085 GHz, frequency span per division 0.2 MHz, resolution bandwidth 30 kHz, input attenuation 20 dB, reference level +10 dBm, sweep time per division 1 ms, video filter off, scan mode internal and trigger auto.

5. Adjust the signal generator for sidebands approximately −40 dB from the carrier as observed on the spectrum analyzer display. Tune the frequency from 25 to 26 MHz while observing the sidebands. The 3 dB bandwidth should match the Yig Loop bandwidth ±150 kHz.

ADJUSTMENTS

5-35. SUM LOOP BANDWIDTH ADJUSTMENT (Cont'd)

6. If the 3 dB bandwidth is not correct, select A4C8 for the correct response. The normal value range is 200 to 560 pF. (The bandwidth increases with an increased capacitance.)

7. Set the mainframe center frequency to 1.005 GHz.

8. Adjust the spectrum analyzer frequency to 1.005 GHz.

9. Connect the DVM to A3TP1.

10. Tune the signal generator frequency from 25 to 26 MHz while observing the sidebands displayed on the spectrum analyzer.

11. Adjust the "0" control A4R5 to obtain a 3 dB bandwidth the same as the Yig Loop bandwidth ±150 kHz. It may be necessary at this point to adjust the "B" control A4R6 for an indication of +11.0 ± 0.5 Vdc on the DVM to achieve the 3 dB bandwidth.

12. Adjust the "B" control for +11.0 ± 0.5 Vdc at A3TP1.

13. Disconnect the signal generator and coaxial tee from W23 and reconnect the cable to the jack of the mainframe's A2 Assembly.

14. Disconnect the gray cable where it connects to jack J1 on the 11661B. Connect the frequency counter to J1.

15. Adjust A14R2 for a frequency output of 3.9550 ± 0.0001 GHz as indicated by the frequency counter.

16. Disconnect the frequency counter and reconnect the gray cable to J1.

17. Adjust the "B" control A4R6 for a reading of +11.0 ± 0.5 Vdc at A3TP1.

18. Step the center frequency from 1.005 to 1.095 GHz in 0.010 GHz (10 MHz) steps. Adjust the appropriate sum loop pretune controls for a reading of +11.0 ± 0.5 Vdc at A3TP1 as shown in Table 5-5.

SECTION VI
REPLACEABLE PARTS

6-1. INTRODUCTION

6-2. This section contains information for ordering replacement parts for the HP Model 11661B Frequency Extension Module. Table 6-1 lists abbreviations used in the parts list and throughout the manual. Table 6-2 lists all replaceable parts in reference designator order. Table 6-3 contains the names and addresses that correspond to the manufacturer's code number.

6-3. ABBREVIATIONS

6-4. Table 6-1 gives a list of abbreviations used in the parts list, schematics, and throughout the manual. In some cases, two forms of the abbreviations are given, one all capital letters and one partial or no capitals. This occurs because the abbreviations in the parts list are always all capitals. However, in the schematics and other parts of the manual, other abbreviation forms are used with both lower case and upper case letters.

6-5. REPLACEABLE PARTS LIST

6-6. Table 6-2 is the list of replaceable parts and is organized as follows:

a. Electrical assemblies and their components in alpha-numeric order by reference designation.

b. Chassis-mounted parts in alpha-numeric order by reference designation.

c. Miscellaneous parts.

The information given for each part consists of the following:

a. The Hewlett-Packard part number.

b. Part number check digit (CD).

c. The total quantity (Qty) used in the instrument.

d. The description of the part.

e. Typical manufacturer of the part in a five-digit code.

f. Manufacturer code number for the part.

The total quantity for each part is given only once; at the first appearance of the part number in the list.

6.7 ORDERING INSTRUCTIONS

6-8. To order a part listed in the replaceable parts table, quote the Hewlett-Packard part number, indicate quantity required, and address the order to the nearest Hewlett-Packard office. The check digit will ensure accurate and timely processing of your order.

6-9. To order a part that is not listed in the replaceable parts table, include the instrument model number, instrument serial number, the description and function of the part, and the number of parts required. Address the order to the nearest Hewlett-Packard office.

6-10. PARTS PROVISIONING

6-11. Stocking spare parts for an instrument is often done to insure quick return to service after a malfunction occurs. Hewlett-Packard has a "Spare Parts Kit" available for this purpose. The kit consists of selected replaceable assemblies and components for this instrument. The contents of the kit and the "Recommended Spares" list are based on failure reports and repair data, and parts support for one year. A complimentary "Recommended Spares" list for this instrument may be obtained on request, and the "Spare Parts Kit" may be ordered through your nearest Hewlett-Packard office.

Table 6-1. Reference Designations and Abbreviations (1 of 2)

REFERENCE DESIGNATIONS

A . . . . . assembly
AT . . attenuator; isolator; termination
B . . . . . fan; motor
BT . . . . . battery
C . . . . . capacitor
CP . . . . . coupler
CR . . . . . diode; diode thyristor; varactor
DC . . . directional coupler
DL . . . . . delay line
DS . . . . . annunciator; signaling device (audible or visual); lamp; LED
E . . . . . miscellaneous electrical part
F . . . . . fuse
FL . . . . . filter
H . . . . . hardware
HY . . . . . circulator
J . . . electrical connector (stationary portion); jack
K . . . . . relay
L . . . . . coil; inductor
M . . . . . meter
MP . . . . . miscellaneous mechanical part
P . . . electrical connector (movable portion); plug
Q . . . . . transistor: SCR; triode thyristor
R . . . . . resistor
RT . . . . . thermistor
S . . . . . switch
T . . . . . transformer
TB . . . . . terminal board
TC . . . . . thermocouple
TP . . . . . test point
U . . . . . integrated circuit; microcircuit
V . . . . . electron tube
VR . . . . voltage regulator; breakdown diode
W . . . . cable; transmission path; wire
X . . . . . socket
Y . . . . crystal unit (piezoelectric or quartz)
Z . . . . tuned cavity; tuned circuit

ABBREVIATIONS

A . . . . . ampere
ac . . . . alternating current
ACCESS . . . . . accessory
ADJ . . . . . adjustment
A/D . . . . analog-to-digital
AF . . . . . audio frequency
AFC . . . . . automatic frequency control
AGC . . . . . automatic gain control
AL . . . . . aluminum
ALC . . . . . automatic level control
AM . . . amplitude modulation
AMPL . . . . . amplifier
APC . . . . automatic phase control
ASSY . . . . . assembly
AUX . . . . . auxiliary
avg . . . . . average
AWG . . . . American wire gauge
BAL . . . . . balance
BCD . . . . . binary coded decimal
BD . . . . . board
BE CU . . . . . beryllium copper
BFO . . . . . beat frequency oscillator
BH . . . . . binder head
BKDN . . . . . breakdown
BP . . . . . bandpass
BPF . . . . . bandpass filter
BRS . . . . . brass
BWO . . . . . backward-wave oscillator
CAL . . . . . calibrate
ccw . . counter-clockwise
CER . . . . . ceramic
CHAN . . . . . channel
cm . . . . . centimeter
CMO . . cabinet mount only
COAX . . . . . coaxial
COEF . . . . . coefficient
COM . . . . . common
COMP . . . . . composition
COMPL . . . . . complete
CONN . . . . . connector
CP . . . . . cadmium plate
CRT . . . cathode-ray tube
CTL . . . . complementary transistor logic
CW . . . . . continuous wave
cw . . . . . clockwise
cm . . . . . centimeter
D/A . . . . digital-to-analog
dB . . . . . decibel
dBm . . . . decibel referred to 1 mW
dc . . . . . direct current
deg . . degree (temperature interval or difference)
° . . . . . degree (plane angle)
°C . . . . . degree Celsius (centigrade)
°F . . . . degree Fahrenheit
°K . . . . . degree Kelvin
DEPC . . deposited carbon
DET . . . . . detector
diam . . . . . diameter
DIA . . . diameter (used in parts list)
DIFF AMPL . . differential amplifier
div . . . . . division
DPDT . . . . . double-pole, double-throw
DR . . . . . drive
DSB . . . . double sideband
DTL . . . . diode transistor logic
DVM . . . digital voltmeter
ECL . . . . emitter coupled logic
EMF . . electromotive force
EDP . . . . . electronic data processing
ELECT . . . . . electrolytic
ENCAP . . . . encapsulated
EXT . . . . . external
F . . . . . farad
FET . . . . . field-effect transistor
F/F . . . . . flip-flop
FH . . . . . flat head
FIL H . . . . . fillister head
FM . . frequency modulation
FP . . . . . front panel
FREQ . . . . . frequency
FXD . . . . . fixed
g . . . . . gram
GE . . . . . germanium
GHz . . . . . gigahertz
GL . . . . . glass
GRD . . . . . ground(ed)
H . . . . . henry
h . . . . . hour
HET . . . . . heterodyne
HEX . . . . . hexagonal
HD . . . . . head
HDW . . . . . hardware
HF . . . . . high frequency
HG . . . . . mercury
HI . . . . . high
HP . . . . . Hewlett-Packard
HPF . . . . . high pass filter
HR . . . . . hour (used in parts list)
HV . . . . . high voltage
Hz . . . . . Hertz
IC . . . integrated circuit
ID . . . . . inside diameter
IF . . . . . intermediate frequency
IMPG . . . . . impregnated
in . . . . . inch
INCD . . . . . incandescent
INCL . . . . . include(s)
INP . . . . . input
INS . . . . . insulation
INT . . . . . internal
kg . . . . . kilogram
kHz . . . . . kilohertz
kΩ . . . . . kilohm
kV . . . . . kilovolt
lb . . . . . pound
LC . . . . . inductance-capacitance
LED . . light-emitting diode
LF . . . . . low frequency
LG . . . . . long
LH . . . . . left hand
LIM . . . . . limit
LIN . . . linear taper (used in parts list)
lin . . . . . linear
LK WASH . . . lock washer
LO . . . low; local oscillator
LOG . . . . logarithmic taper (used in parts list)
log . . . . . logrithm(ic)
LPF . . . . low pass filter
LV . . . . . low voltage
m . . . . . meter (distance)
mA . . . . . milliampere
MAX . . . . . maximum
MΩ . . . . . megohm
MEG . . . . meg (10^6) (used in parts list)
MET FLM . . . . metal film
MET OX . . metallic oxide
MF . . . medium frequency; microfarad (used in parts list)
MFR . . . . . manufacturer
mg . . . . . milligram
MHz . . . . . megahertz
mH . . . . . millihenry
mho . . . . . mho
MIN . . . . . minimum
min . . . . . minute (time)
. . .' . . . . . minute (plane angle)
MINAT . . . . . miniature
mm . . . . . millimeter

NOTE

All abbreviations in the parts list will be in upper-case.

Table 6-1. Reference Designations and Abbreviations (2 of 2)

MOD . . . . . modulator
MOM . . . . . momentary
MOS . . . . . metal-oxide semiconductor
ms . . . . . millisecond
MTG . . . . . mounting
MTR . . . meter (indicating device)
mV . . . . . millivolt
mVac . . . . . millivolt, ac
mVdc . . . . . millivolt, dc
mVpk . . . . millivolt, peak
mVp-p . . . millivolt, peak-to-peak
mVrms . . . . millivolt, rms
mW . . . . . milliwatt
MUX . . . . . multiplex
MY . . . . . mylar
μA . . . . . microampere
μF . . . . . microfarad
μH . . . . . microhenry
μmho . . . . . micromho
μs . . . . . microsecond
μV . . . . . microvolt
μVac . . . . . microvolt, ac
μVdc . . . . . microvolt, dc
μVpk . . . microvolt, peak
μVp-p . . . microvolt, peak-to-peak
μVrms . . . . microvolt, rms
μW . . . . . microwatt
nA . . . . . nanoampere
NC . . . . . no connection
N/C . . . . normally closed
NE . . . . . neon
NEG . . . . . negative
nF . . . . . nanofarad
NI PL . . . . . nickel plate
N/O . . . . . normally open
NOM . . . . . nominal
NORM . . . . . normal
NPN . . . negative-positive-negative
NPO . . . . negative-positive zero (zero temperature coefficient)
NRFR . . not recommended for field replacement
NSR . . . . . not separately replaceable
ns . . . . . nanosecond
nW . . . . . nanowatt
OBD . . . order by description
OD . . . . . outside diameter
OH . . . . . oval head
OP AMPL . . . operational amplifier
OPT . . . . . option
OSC . . . . . oscillator
OX . . . . . oxide
oz . . . . . ounce
Ω . . . . . ohm
P . . . . peak (used in parts list)
PAM . . . . pulse-amplitude modulation
PC . . . . . printed circuit
PCM . . pulse-code modulation; pulse-count modulation
PDM . . . . . pulse-duration modulation
pF . . . . . picofarad
PH BRZ phosphor bronze
PHL . . . . . Phillips
PIN . . . positive-intrinsic-negative
PIV . . . . . peak inverse voltage
pk . . . . . peak
PL . . . . . phase lock
PLO . . . . . phase lock oscillator
PM . . . . phase modulation
PNP . . . positive-negative-positive
P/O . . . . . part of
POLY . . . . . polystyrene
PORC . . . . . porcelain
POS . . positive; position(s) (used in parts list)
POSN . . . . . position
POT . . . . . potentiometer
p-p . . . . . peak-to-peak
PP . . . peak-to-peak (used in parts list)
PPM . . . . . pulse-position modulation
PREAMPL . . . preamplifier
PRF . . . . pulse-repetition frequency
PRR . . . . pulse repetition rate
ps . . . . . picosecond
PT . . . . . point
PTM . . . . . pulse-time modulation
PWM . . . . . pulse-width modulation
PWV . . . . . peak working voltage
RC . . . . . resistance-capacitance
RECT . . . . . rectifier
REF . . . . . reference
REG . . . . . regulated
REPL . . . . . replaceable
RF . . . . . radio frequency
RFI . . . . radio frequency interference
RH . . . . round head; right hand
RLC . . . . . resistance-inductance-capacitance
RMO . . . rack mount only
rms . . . . root-mean-square
RND . . . . . round
ROM . . read-only memory
R&P . . . . . rack and panel
RWV . . . reverse working voltage
S . . . scattering parameter
s . . . . . second (time)
. . . " . second (plane angle)
S-B . . . . . slow-blow (fuse) (used in parts list)
SCR . . . silicon controlled rectifier; screw
SE . . . . . selenium
SECT . . . . . sections
SEMICON . . . . . semiconductor
SHF . . . . . superhigh frequency
SI . . . . . silicon
SIL . . . . . silver
SL . . . . . slide
SNR . . signal-to-noise ratio
SPDT . . . . . single-pole, double-throw
SPG . . . . . spring
SR . . . . . split ring
SPST . . . . . single-pole, single-throw
SSB . . . . . single sideband
SST . . . . . stainless steel
STL . . . . . steel
SQ . . . . . square
SWR . . standing-wave ratio
SYNC . . . . . synchronize
T . . timed (slow-blow fuse)
TA . . . . . tantalum
TC . . . . . temperature compensating
TD . . . . . time delay
TERM . . . . . terminal
TFT . . thin-film transistor
TGL . . . . . toggle
THD . . . . . thread
THRU . . . . . through
TI . . . . . titanium
TOL . . . . . tolerance
TRIM . . . . . trimmer
TSTR . . . . . transistor
TTL . . transistor-transistor logic
TV . . . . . television
TVI television interference
TWT . . traveling wave tube
U . . . . . micro (10^{-6}) (used in parts list)
UF . . . microfarad (used in parts list)
UHF . . ultrahigh frequency
UNREG . . . . unregulated
V . . . . . volt
VA . . . . . voltampere
Vac . . . . . volts, ac
VAR . . . . . variable
VCO . . . voltage-controlled oscillator
Vdc . . . . . volts, dc
VDCW . . volts, dc, working (used in parts list)
V(F) . . . . . volts, filtered
VFO . . variable-frequency oscillator
VHF . . . . . very-high frequency
Vpk . . . . . volts, peak
Vp-p . . volts, peak-to-peak
Vrms . . . . . volts, rms
VSWR . . . voltage standing wave ratio
VTO . . . . . voltage-tuned oscillator
VTVM . . . . vacuum-tube voltmeter
V(X) . . . . . volts, switched
W . . . . . watt
W/ . . . . . with
WIV . . . . working inverse voltage
WW . . . . . wirewound
W/O . . . . . without
YIG . . yttrium-iron-garnet
Z_o . . . . . characteristic impedance

NOTE

All abbreviations in the parts list will be in upper-case.

MULTIPLIERS

Abbreviation	Prefix	Multiple
T	tera	10^{12}
G	giga	10^{9}
M	mega	10^{6}
k	kilo	10^{3}
da	deka	10
d	deci	10^{-1}
c	centi	10^{-2}
m	milli	10^{-3}
μ	micro	10^{-6}
n	nano	10^{-9}
p	pico	10^{-12}
f	femto	10^{-15}
a	atto	10^{-18}

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A1	11661-60019	5	1	OSCILLATOR/MIXER HOUSING ASSY	28480	11661-60019
A1C1	0160-2437	1	6	CAPACITOR-FDTHRU 5000PF +80 -20% 200V	28480	0160-2437
A1C2	0160-2437	1		CAPACITOR-FDTHRU 5000PF +80 -20% 200V	28480	0160-2437
A1C3	0160-2437	1		CAPACITOR-FDTHRU 5000PF +80 -20% 200V	28480	0160-2437
A1C4	0160-2437	1		CAPACITOR-FDTHRU 5000PF +80 -20% 200V	28480	0160-2437
A1C5	0160-4023	5	1	CAPACITOR-FDTHRU 680PF 20% 500V CER	28480	0160-4023
	0360-1155	2	2	TERMINAL-SLDR LUG PL-MTG FOR-#12-SCR	28480	0360-1155
A1C6	0160-2437	1		CAPACITOR-FDTHRU 5000PF +80 -20% 200V	28480	0160-2437
A1C7	0160-2437	1		CAPACITOR-FDTHRU 5000PF +80 -20% 200V	28480	0160-2437
A1J1	1250-0901	2	7	CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1J2	1250-0901	2		CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1J3	1250-0901	2		CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1J4	1250-0901	2		CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1J5	1250-0901	2		CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1J6	1250-0901	2		CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1J7	1250-0901	2		CONNECTOR-RF SMB M SGL-HOLE-FR 50-OHM	28480	1250-0901
A1R1†	0698-3435	0	1	RESISTOR 38.3 1% .125W F TC=0+-100	24546	C4-1/8-T0-38R3-F
A1R2	0698-7192	4	1	RESISTOR 14.7 1% .05W F TC=0+-100	24546	C3-1/8-T00-14R7-G
A1R3	0698-7217	4	2	RESISTOR 162 1% .05W F TC=0+-100	24546	C3-1/8-T0-162R-G
A1R4†	0757-0401	0	3	RESISTOR 100 1% .125W F TC=0+-100	24546	C4-1/8-T0-101-F
A1U1	5086-7055	6	1	SAMPLER, 1.8 GHZ LOW PASS FILTER ASSY	28480	5086-7055
A1U2††	11661-67002	0	1	VCO/MIXER ASSY	28480	11661-67002
A1U3†	11661-67001	9	1	4.43 GHZ OSC/MIXER ASSY	28480	11661-67001
			1	A1 MISCELLANEOUS		
	0360-1155	2		TERMINAL-SLDR LUG PL-MTG FOR-#12-SCR	28480	0360-1155
	0380-0793	4	2	SPACER-RND .156-IN-LG .093-IN-ID	28480	0380-0793
	11661-00004	2	1	COVER, YIG LOOP	28480	11661-00004
	11661-00006	4	2	CLAMP,MICROCIRCUIT OSCILLATOR	28480	11661-00006
	11661-00016	6	1	COVER, FILTER	28480	11661-00016
	11661-00008	6	1	CLMP, SAMPLER-FILTER	28480	11661-00008
	11661-00009	7	1	COVER, SUM LOOP	28480	11661-00009
	11661-20040	8		SPRING, RFI	28480	11661-20040
	11661-20044	2	1	POLYIRON SHEET	28480	11661-20044
A1A1	11661-60007	1	1	20 MHZ IF AMPLIFIER ASSY	28480	11661-60007
A1A1C1	0121-0448	8	1	CAPACITOR-V TRMR-CER 2.5-5PF 63V PC-MTG	28480	0121-0448
A1A1C2	0160-3878	6	10	CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A1A1C3	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A1A1C4	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A1A1C5	0160-3879	7	18	CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A1A1C6	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A1A1C7	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A1A1C8	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A1A1C9	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A1A1CR1	1901-0040	1	11	DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A1A1L1	9140-0144	0	5	COIL-MLD 4.7UH 10% Q=45 .095DX.25LG-NOM	28480	9140-0144
A1A1L2	9100-1618	1	1	COIL-MLD 5.6UH 10% Q=45 .155DX.375LG-NOM	28480	9100-1618
A1A1L3	9140-0144	0		COIL-MLD 4.7UH 10% Q=45 .095DX.25LG-NOM	28480	9140-0144
A1A1Q1	1853-0015	7	1	TRANSISTOR PNP SI PD=200MW FT=500MHZ	28480	1853-0015
A1A1Q2	1854-0009	1	1	TRANSISTOR NPN SI PD=300MW FT=600MHZ	04713	2N709
A1A1Q3	1855-0081	1	1	TRANSISTOR J-FET N-CHAN D-MODE SI	01295	2N5245
A1A1R1	0698-7260	7	7	RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-G
A1A1R2	0698-7236	7	21	RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A1A1R3	0698-7243	6	5	RESISTOR 1.96K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1961-G
A1A1R4	0698-7212	9	7	RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A1A1R5	0698-7243	6		RESISTOR 1.96K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1961-G
A1A1R6	0698-7247	0	2	RESISTOR 2.87K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2871-G
A1A1R7	0698-7195	7	1	RESISTOR 19.6 1% .05W F TC=0+-100	24546	C3-1/8-T00-19R6-G
A1A1R8	0698-7234	5	1	RESISTOR 825 1% .05W F TC=0+-100	24546	C3-1/8-T0-825R-G
A1A1R9	0698-7219	6	8	RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A1A1R10	0698-7245	8	2	RESISTOR 2.37K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2371-G
A1A1R11	0698-7205	0	3	RESISTOR 51.1 1% .05W F TC=0+-100	24546	C3-1/8-T00-51R1-G
A1A2	11661-60079	7	1	380-480 MHZ IF AMPLIFIER ASSY	28480	11661-60079
A1A2C1	0180-0197	8	7	CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A1A2C2	0180-1746	5	1	CAPACITOR-FXD 15UF+-10% 20VDC TA	56289	150D156X9020B2
A1A2C3	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A1A2C4	0160-2266	4	2	CAPACITOR-FXD 24PF +-5% 500VDC CER 0+-30	28480	0160-2266
A1A2C5	0160-2266	4		CAPACITOR-FXD 24PF +-5% 500VDC CER 0+-30	28480	0160-2266
A1A2C6	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A1A2C7	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A1A2C8	0160-3874	2	1	CAPACITOR-FXD 10PF +-.5PF 200VDC CER	28480	0160-3874

See introduction to this section for ordering information
*Indicates factory selected value
†BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A1A2L1	9140-0144	0		COIL-MLD 4.7UH 10% Q=45 .095DX.25LG-NOM	28480	9140-0144
A1A2L2	08660-80009	7	2	INDUCTOR	28480	08660-80009
A1A2L3	08660-80009	7		INDUCTOR	28480	08660-80009
A1A2Q1	1854-0540	5	2	TRANSISTOR NPN SI TO-72 PD=200MW FT=1GHZ	04713	MM8006
A1A2Q2	1854-0540	5		TRANSISTOR NPN SI TO-72 PD=200MW FT=1GHZ	04713	MM8006
A1A2R1†	0698-3429	2	3	RESISTOR 19.6 1% .125W F TC=0+-100	03888	PME55-1/8-T0-19R6-F
A1A2R2	0698-3429	2		RESISTOR 19.6 1% .125W F TC=0+-100	03888	PME55-1/8-T0-19R6-F
A1A2R3†				NOT ASSIGNED		
A1A2R4	0698-7256	1	2	RESISTOR 6.81K 1% .05W F TC=0+-100	24546	C3-1/8-T0-6811-G
A1A2R5	0698-7248	1	3	RESISTOR 3.16K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3161-G
A1A2R6	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A1A2R7	0698-7256	1		RESISTOR 6.81K 1% .05W F TC=0+-100	24546	C3-1/8-T0-6811-G
A1A2R8	0698-7248	1		RESISTOR 3.16K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3161-G
A1A2R9	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A1A2R10*	0698-7209	4		RESISTOR 75 1% .05W F TC=0+-100	24546	C3-1/8-T00-75R0-G
A1A3	11661-60014	0	1	4 GHZ LOW PASS FILTER ASSY	28480	11661-60014
A1A4†	11661-60078	6	1	4.43 GHZ OSCILLATOR TUNING ASSY	28480	11661-60078
A1A4C1	0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A1A4C2	0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A1A4C3	0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A1A4R1††	0698-3429	2		RESISTOR 19.6 1% .125W F TC=0+-100	03888	PME55-1/8-T0-19R6-F
A1A4R2 ††				DELETED		
A1A4VR1 ††				DELETED		
A2	11661-60006	0	1	MOTHER BOARD ASSY	28480	11661-60006
A2C1	0160-2055	9	9	CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A2C2	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A2C3	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A2J1	1250-1377	8	5	CONNECTOR-RF SMB FEM PC 50-OHM	28480	1250-1377
A2J2	1250-1377	8		CONNECTOR-RF SMB FEM PC 50-OHM	28480	1250-1377
A2J3	1250-1377	8		CONNECTOR-RF SMB FEM PC 50-OHM	28480	1250-1377
A2J4	1250-1377	8		CONNECTOR-RF SMB FEM PC 50-OHM	28480	1250-1377
A2J5	1250-1377	8		CONNECTOR-RF SMB FEM PC 50-OHM	28480	1250-1377
A2XA3	1251-1626	2	5	CONNECTOR-PC EDGE 12-CONT/ROW 2-ROWS	28480	1251-1626
A2XA4	1251-1626	2		CONNECTOR-PC EDGE 12-CONT/ROW 2-ROWS	28480	1251-1626
A2XA5	1251-1626	2		CONNECTOR-PC EDGE 12-CONT/ROW 2-ROWS	28480	1251-1626
A2XA6	1251-1626	2		CONNECTOR-PC EDGE 12-CONT/ROW 2-ROWS	28480	1251-1626
A2XA7	1251-1626	2		CONNECTOR-PC EDGE 12-CONT/ROW 2-ROWS	28480	1251-1626
A3	11661-60004	8	1	SUM LOOP PHASE DETECTOR ASSY	28480	11661-60004
A3C1	0180-2208	6	2	CAPACITOR-FXD 220UF+-10% 10VDC TA	56289	150D227X9010B2
A3C2	0180-2208	6		CAPACITOR-FXD 220UF+-10% 10VDC TA	56289	150D227X9010B2
A3C3	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A3C4	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A3C5	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A3C6	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A3C7	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A3C8	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A3C9	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A3C10	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A3C11	0160-3878	6		CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480	0160-3878
A3C12	0160-3873	1	3	CAPACITOR-FXD 4.7PF +-.5PF 200VDC CER	28480	0160-3873
A3C13	0160-3873	1		CAPACITOR-FXD 4.7PF +-.5PF 200VDC CER	28480	0160-3873
A3C14	0160-3873	1		CAPACITOR-FXD 4.7PF +-.5PF 200VDC CER	28480	0160-3873
A3C15	0160-3875	3	4	CAPACITOR-FXD 22PF +-5% 200VDC CER 0+-30	28480	0160-3875
A3C16	0160-3875	3		CAPACITOR-FXD 22PF +-5% 200VDC CER 0+-30	28480	0160-3875
A3C17	0160-3875	3		CAPACITOR-FXD 22PF +-5% 200VDC CER 0+-30	28480	0160-3875
A3C18	0160-3875	3		CAPACITOR-FXD 22PF +-5% 200VDC CER 0+-30	28480	0160-3875
A3C19	0160-3548	7	2	CAPACITOR-FXD .01UF +-1% 100VDC MICA	28480	0160-3548
A3C20	0160-3094	8	7	CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A3C21	0160-3094	8		CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A3C22	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A3C23	0160-3094	8		CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A3C24	0160-2306	3	1	CAPACITOR-FXD 27PF +-5% 300VDC MICA	28480	0160-2306
A3C25	0160-3548	7		CAPACITOR-FXD .01UF +-1% 100VDC MICA	28480	0160-3548
A3L1	9140-0179	1	1	COIL-MLD 22UH 10% Q=75 .155DX.375LG-NOM	28480	9140-0179
A3L2*	9140-0105	3	1	COIL-MLD 8.2UH 10% Q=50 .155DX.375LG-NOM	28480	9140-0105
A3L3	9100-2551	3	1	COIL-MLD 12UH 10% Q=50 .156DX.375LG-NOM	28480	9100-2551
A3L4	9140-0238	3	2	COIL-MLD 82UH 5% Q=50 .155DX.375LG-NOM	28480	9140-0238
A3L5	9140-0238	3		COIL-MLD 82UH 5% Q=50 .155DX.375LG-NOM	28480	9140-0238
A3Q1	1853-0007	7	17	TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A3Q2	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A3Q3	1854-0221	9	2	TRANSISTOR-DUAL NPN PD=750MW	28480	1854-0221
A3Q4	1855-0049	1	1	TRANSISTOR-JFET DUAL N-CHAN D-MODE SI	28480	1855-0049
A3Q5	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251

See introduction to this section for ordering information
*Indicates factory selected value
† BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A3R1	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-F
A3R2	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-F
A3R3	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-F
A3R4	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-F
A3R5	0698-7224	3	8	RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-F
A3R6	0698-7222	1	3	RESISTOR 261 1% .05W F TC=0+-100	24546	C3-1/8-T0-261R-F
A3R7	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-F
A3R8	0698-7222	1		RESISTOR 261 1% .05W F TC=0+-100	24546	C3-1/8-T0-261R-F
A3R9	0698-7225	4	4	RESISTOR 348 1% .05W F TC=0+-100	24546	C3-1/8-T0-348R-F
A3R10	0698-7225	4		RESISTOR 348 1% .05W F TC=0+-100	24546	C3-1/8-T0-348R-F
A3R11	0698-7218	5	2	RESISTOR 178 1% .05W F TC=0+-100	24546	C3-1/8-T0-178R-F
A3R12	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-F
A3R13	0698-7253	8	4	RESISTOR 5.11K 1% .05W F TC=0+-100	24546	C3-1/8-T0-5111-F
A3R14	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-F
A3R15	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-F
A3R16	0698-7253	8		RESISTOR 5.11K 1% .05W F TC=0+-100	24546	C3-1/8-T0-5111-F
A3R17	0698-7244	7	7	RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-F
A3R18	0698-7244	7		RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-F
A3R19	0698-7244	7		RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-F
A3R20	0698-7244	7		RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-F
A3R21	0698-7253	8		RESISTOR 5.11K 1% .05W F TC=0+-100	24546	C3-1/8-T0-5111-F
A3R22	0698-7244	7		RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-F
A3R23	0698-7244	7		RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-F
A3R24	0698-7188	8	5	RESISTOR 10 1% .05W F TC=0+-100	24546	C3-1/8-T00-10R-F
A3R25	0698-7277	6	2	RESISTOR 51.1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-5112-F
A3R26	0698-7188	8		RESISTOR 10 1% .05W F TC=0+-100	24546	C3-1/8-T00-10R-F
A3R27	0698-7205	0		RESISTOR 51.1 1% .05W F TC=0+-100	24546	C3-1/8-T00-51R1-F
A3R28	0698-7260	7		RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-F
A3R29	0698-7249	2	2	RESISTOR 3.48K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3481-F
A3R30	0698-7205	0		RESISTOR 51.1 1% .05W F TC=0+-100	24546	C3-1/8-T00-51R1-F
A3R31	0698-7241	4	2	RESISTOR 1.62K 1% .05W F TC=0+-100	28480	0698-7241
A3R32	0698-7277	6		RESISTOR 51.1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-5112-F
A3R33	0698-7253	8		RESISTOR 5.11K 1% .05W F TC=0+-100	24546	C3-1/8-T0-5111-F
A3R34	0698-7260	7		RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-F
A3R35	0698-3154	0	1	RESISTOR 4.22K 1% .125W F TC=0+-100	24546	C4-1/8-T0-4221-F
A3U1	1820-2034	5	2	IC QUAD 2-INPT NAND	01295	SN92502N
A3U2	1820-0685	8	1	IC GATE TTL 8 NAND TPL 3-INP	01295	8N74810N
A3U3	1820-2034	5		IC QUAD 2-INPT NAND	01295	SN92502N
				A3 MISCELLANEOUS		
	0360-0124	3	2	CONNECTOR-BGL CONT PIN .04-IN-BSC-BZ RND	28480	0360-0124
	1480-0073	6	10	PIN-ROLL .062-IN-DIA .25-IN-LG BE-CU	28480	1480-0073
	4040-0748	3	6	EXTR-PC BD BLK POLYC .062-BD-THKNS	28480	4040-0748
	4040-0753	0	1	EXTR-PC BD GRN POLYC .062-BD-THKNS	28480	4040-0753
A4	11661-60005	9	1	SUM LOOP PRETUNE ASSY	28480	11661-60005
A4C1	0160-0127	2	7	CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A4C2	0160-0127	2		CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A4C3	0180-0183	2	1	CAPACITOR-FXD 10UF+75-10% 50VDC AL	56289	30D106G050CB2
A4C4	0160-2254	0	1	CAPACITOR-FXD 7.5PF +-.25PF 500VDC CER	28480	0160-2254
A4C5	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A4C6	0160-3094	8		CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A4C7	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A4C8*†	0160-2207	3	1	CAPACITOR-FXD 300PF +-5% 300VDC MICA	28480	0160-2207
A4CR1	1901-0050	3	1	DIODE-SWITCHING 80V 200MA 2NS DO-35	28480	1901-0050
A4L1	9140-0138	2	1	COIL-MLD 180UH 5% Q=65 .155DX.375LG-NOM	28480	9140-0138
A4L2	9100-2261	2	1	COIL-MLD 2.7UH 10% Q=40 .095DX.25LG-NOM	28480	9100-2261
A4Q1	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q2	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q3	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q4	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q5	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q6	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q7	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q8	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q9	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q10	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q11	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4Q12	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
A4R1	0698-7188	8		RESISTOR 10 1% .05W F TC=0+-100	24546	C3-1/8-T00-10R-F
A4R2	0757-0441	8	1	RESISTOR 8.25K 1% .125W F TC=0+-100	24546	C4-1/8-T0-8251-F
A4R3	0698-7217	4		RESISTOR 162 1% .05W F TC=0+-100	24546	C3-1/8-T0-162R-F
A4R4	0698-7240	3	2	RESISTOR 1.47K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1471-F
A4R5	2100-1986	9	4	RESISTOR-TRMR 1K 10% C TOP-ADJ 1-TRN	73138	82PR1K

See introduction to this section for ordering information
*Indicates factory selected value
†BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A4R6	2100-1986	9		RESISTOR-TRMR 1K 10% C TOP-ADJ 1-TRN	73138	82PR1K
A4R7	0698-3101	7	1	RESISTOR 2.87K 1% .5W F TC=0+-100	28480	0698-3101
A4R8	0698-7212	9		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A4R9	0698-7218	5		RESISTOR 178 1% .05W F TC=0+-100	24546	C3-1/8-T0-178R-G
A4R10	0698-7241	4		RESISTOR 1.62K 1% .05W F TC=0+-100	28480	0698-7241
A4R11†	2100-1788	9	1	RESISTOR-TRMR 500 10% C TOP-ADJ 1-TRN	73138	82PR500
A4R12†	0698-7258	3	1	RESISTOR 8.25K 1% .05W F TC=0+-100	24546	C3-1/8-T0-8251-G
A4R13	0698-7212	9		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A4R14	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A4R15	0698-7242	5	1	RESISTOR 1.78K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1781-G
A4R16†	2100-1788	9	3	RESISTOR-TRMR 500 10% C TOP-ADJ 1-TRN	73138	82PR500
A4R17	0698-7276	5	1	RESISTOR 46.4K 1% .05W F TC=0+-100	24546	C3-1/8-T0-4642-G
A4R18	0698-7220	9	1	RESISTOR 215 1% .05W F TC=0+-100	24546	C3-1/8-T0-215R-G
A4R19	0698-7243	6		RESISTOR 1.96K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1961-G
A4R20†	2100-1788	9		RESISTOR-TRMR 500 10% C TOP-ADJ 1-TRN	73138	82PR500
A4R21	0698-7221	0	1	RESISTOR 237 1% .05W F TC=0+-100	24546	C3-1/8-T0-237R-G
A4R22	0698-7244	7		RESISTOR 2.15K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2151-G
A4R23	2100-1788	9		RESISTOR-TRMR 500 10% C TOP-ADJ 1-TRN	73138	82PR500
A4R24	0698-7222	1		RESISTOR 261 1% .05W F TC=0+-100	24546	C3-1/8-T0-261R-G
A4R25	0698-7245	8		RESISTOR 2.37K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2371-G
A4R26†	2100-1986	9		RESISTOR-TRMR 1K 10% C TOP-ADJ 1-TRN	73138	82PR1K
A4R27	0698-7223	2	2	RESISTOR 287 1% .05W F TC=0+-100	24546	C3-1/8-T0-287R-G
A4R28	0698-7246	9	1	RESISTOR 2.61K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2611-G
A4R29	2100-1986	9		RESISTOR-TRMR 1K 10% C TOP-ADJ 1-TRN	73138	82PR1K
A4R30	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-G
A4R31	0698-7247	0		RESISTOR 2.87K 1% .05W F TC=0+-100	24546	C3-1/8-T0-2871-G
A4R32†	2100-2497	9	2	RESISTOR-TRMR 2K 10% C TOP-ADJ 1-TRN	73138	82PR2K
A4R33	0698-7225	4		RESISTOR 348 1% .05W F TC=0+-100	24546	C3-1/8-T0-348R-G
A4R34	0698-7248	1		RESISTOR 3.16K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3161-G
A4R35†	2100-2216	0		RESISTOR-TRMR 5K 10% C TOP-ADJ 1-TRN	73138	82PR5K
A4R36	0698-7226	5	1	RESISTOR 383 1% .05W F TC=0+-100	24546	C3-1/8-T0-383R-G
A4R37†	0698-7249	2		RESISTOR 3.48K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3481-G
A4R38†	2100-1738	9	2	RESISTOR-TRMR 10K10% C TOP-ADJ 1-TRN	73138	82PR10K
A4U1	1820-2082	3	1	IC DCDR TTL BCD-TO-DEC 4-TO-10-LINE	01698	SN5442AJ
				A4 MISCELLANEOUS		
	1480-0073	6		PIN-ROLL .062-IN-DIA .25-IN-LG BE-CU	28480	1480-0073
	4040-0748	3		EXTR-PC BD BLK POLYC .062-BD-THKNS	28480	4040-0748
	4040-0752	9	1	EXTR-PC BD YEL POLYC .062-BD-THKNS	28480	4040-0752
A5	11661-60001	5	1	YIG LOOP PRETUNE ASSY	28480	11661-60001
A5C1	0180-2207	5	1	CAPACITOR-FXD 100UF+-10% 10VDC TA	56289	150D107X9010R2
A5C2	0180-2206	4	2	CAPACITOR-FXD 60UF+-10% 6VDC TA	56289	150D606X9006B2
A5C3	0160-2204	0	1	CAPACITOR-FXD 100PF +-5% 300VDC MICA	28480	0160-2204
A5C4	0160-3456	6	1	CAPACITOR-FXD 1000PF +-10% 1KVDC CER	28480	0160-3456
A5C5	0160-3094	8		CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A5C6	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A5C7	0160-3094	8		CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A5C8	0180-0291	3	6	CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A5CR1	1901-0376	6	2	DIODE-GEN PRP 35V 50MA DO-35	28480	1901-0376
A5CR2	1901-0376	6		DIODE-GEN PRP 35V 50MA DO-35	28480	1901-0376
A5Q1	1854-0071	7	5	TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480	1854-0071
A5Q2	1854-0071	7		TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480	1854-0071
A5Q3	1854-0071	7		TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480	1854-0071
A5Q4	1854-0071	7		TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480	1854-0071
A5Q5	1854-0071	7		TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480	1854-0071
A5Q6	1854-0221	9		TRANSISTOR-DUAL NPN PD=750MW	28480	1854-0221
A5Q7	1855-0020	8	2	TRANSISTOR J-FET N-CHAN D-MODE TO-18 SI	28480	1855-0020
A5Q8	1854-0062	6	2	TRANSISTOR NPN 2N1701 SI TO-8 PD=25W	01928	2N1701
A5Q9	1854-0062	6		TRANSISTOR NPN 2N1701 SI TO-8 PD=25W	01928	2N1701
A5R1	0757-0421	4	1	RESISTOR 825 1% .125W F TC=0+-100	24546	C4-1/8-T0-825R-F
A5R2	0698-7229	8	7	RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-T0-511R-G
A5R3	0698-7229	8		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-T0-511R-G
A5R4	0698-7229	8		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-T0-511R-G
A5R5	0698-7229	8		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-T0-511R-G
A5R6	0698-7229	8		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-T0-511R-G
A5R7	0698-7272	1	5	RESISTOR 31.6K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3162-G
A5R8	0698-7272	1		RESISTOR 31.6K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3162-G
A5R9	0698-7272	1		RESISTOR 31.6K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3162-G
A5R10	0698-7272	1		RESISTOR 31.6K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3162-G
A5R11	0698-7272	1		RESISTOR 31.6K 1% .05W F TC=0+-100	24546	C3-1/8-T0-3162-G
A5R12	0757-0280	3	2	RESISTOR 1K 1% .125W F TC=0+-100	24546	C4-1/8-T0-1001-F
A5R13	2100-3056	8	1	RESISTOR-TRMR 5K 10% C SIDE-ADJ 17-TRN	02111	43P502
A5R14	0698-8553	3	2	RESISTOR 40K .5% .1W F TC=0+-15	28480	0698-8553
A5R15	2100-3109	2	1	RESISTOR-TRMR 2K 10% C SIDE-ADJ 17-TRN	02111	43P202

See introduction to this section for ordering information
*Indicates factory selected value
†BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A5R16	0698-8552	2	2	RESISTOR 20K .5% .1W F TC=0+-15	28480	0698-8552
A5R17	2100-3154	7	3	RESISTOR-TRMR 1K 10% C SIDE-ADJ 17-TRN	02111	43P102
A5R18	0698-8551	1	2	RESISTOR 10K .5% .1W F TC=0+-15	28480	0698-8551
A5R19	2100-3123	0	1	RESISTOR-TRMR 500 10% C SIDE-ADJ 17-TRN	02111	43P501
A5R20	0698-8548	6	2	RESISTOR 5K .5% .1W F TC=0+-5	28480	0698-8548
A5R21	2100-3213	9	1	RESISTOR-TRMR 200K 10% C TOP-ADJ 1-TRN	28480	2100-3213
A5R22	0698-8547	5	2	RESISTOR 4K .5% .1W F TC=0+-5	28480	0698-8547
A5R23	0698-8553	3		RESISTOR 40K .5% .1W F TC=0+-15	28480	0698-8553
A5R24	0698-8552	2		RESISTOR 20K .5% .1W F TC=0+-15	28480	0698-8552
A5R25	0698-8551	1		RESISTOR 10K .5% .1W F TC=0+-15	28480	0698-8551
A5R26	0698-8548	6		RESISTOR 5K .5% .1W F TC=0+-5	28480	0698-8548
A5R27	0698-8547	5		RESISTOR 4K .5% .1W F TC=0+-5	28480	0698-8547
A5R28	0698-8550	0	2	RESISTOR 1K .5% .1W F TC=0+-5	28480	0698-8550
A5R29	2100-3229	7	2	RESISTOR-TRMR 200 10% C SIDE-ADJ 17-TRN	28480	2100-3229
A5R30	0698-8550	0		RESISTOR 1K .5% .1W F TC=0+-5	28480	0698-8550
A5R31	0698-0024	7	1	RESISTOR 2.61K 1% .5W F TC=0+-100	28480	0698-0024
A5R32	0698-3457	6	2	RESISTOR 316K 1% .125W F TC=0+-100	28480	0698-3457
A5R33	0698-7284	5	4	RESISTOR 100K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1003-G
A5R34	0698-3457	6		RESISTOR 316K 1% .125W F TC=0+-100	28480	0698-3457
A5R35	0698-7284	5		RESISTOR 100K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1003-G
A5R36	0698-7260	7		RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-G
A5R37	0698-7260	7		RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-G
A5R38	0698-7260	7		RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-G
A5R39	2100-3229	7		RESISTOR-TRMR 200 10% C SIDE-ADJ 17-TRN	28480	2100-3229
A5R40	0698-0549	1	1		28480	0698-0549
A5R41	0698-7188	8		RESISTOR 10 1% .05W F TC=0+-100	24546	C3-1/8-T00-10R-G
A5R42	0698-7243	6		RESISTOR 1.96K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1961-G
A5R43	0811-3256	5	2	RESISTOR 100 .25% 3W PW TC=0+-20	28480	0811-3256
A5R44	0698-7188	8		RESISTOR 10 1% .05W F TC=0+-100	24546	C3-1/8-T00-10R-G
A5R45	0811-3256	5		RESISTOR 100 .25% 3W PW TC=0+-20	28480	0811-3256
A5U1	1820-0174	0	1	IC INV TTL HEX	01295	SN7404N
A5U2	1820-0223	0	1	OP AMP GP TO-99	04713	MLM301AG
A5VR1	1902-1216	7	2	DIODE-ZNR 1N938A 9V 5% DO-7 PD=.5W	12954	1N938A
A5VR2	1902-0202	9	2	DIODE-ZNR 15V 5% DO-15 PD=1W TC=+.057%	28480	1902-0202
A5VR3	1902-1216	7		DIODE-ZNR 1N938A 9V 5% DO-7 PD=.5W	12954	1N938A
A5VR4	1902-0202	9		DIODE-ZNR 15V 5% DO-15 PD=1W TC=+.057%	28480	1902-0202
				A5 MISCELLANEOUS		
	0360-0124	3		CONNECTOR-BGL CONT PIN .04-IN-BSC-BZ RND	28480	0360-0124
	1480-0073	6		PIN-ROLL .062-IN-DIA .25-IN-LG BE-CU	28480	1480-0073
	4040-0748	3		EXTR-PC BD BLK POLYC .062-BD-THKNS	28480	4040-0748
	4040-0751	8	1	EXTR-PC BD ORN POLYC .062-BD-THKNS	28480	4040-0751
A6	11661-60002	6	1	FM DRIVER ASSY	28480	11661-60002
A6C1	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A6C2	0180-0291	3		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A6C3	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A6C4	0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A6C5†	0180-0100	3	2	CAPACITOR-FXD 4.7UF+-10% 35VDC TA	56289	150D475X9035B2
A6C6†	0180-0100	3		CAPACITOR-FXD 4.7UF+-10% 35VDC TA	56289	150D475X9035B2
A6C7*	0160-2204	2	1	CAPACITOR-FXD 10UF +-20% 10VDC TA	28480	0160-2204
A6C8	0180-2206	4		CAPACITOR-FXD 60UF+-10% 6VDC TA	56289	150D606X9006B2
A6C9†	0160-0575	4	4	CAPACITOR-FXD .047UF +-20% 50VDC CER	28480	0160-0575
A6C10	0160-3536	3	1	CAPACITOR-FXD 620PF +-5% 100VDC MICA	28480	0160-3536
A6C11	0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A6C12	0160-3451	1	1	CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-3451
A6C13††	0160-0574	3	2	CAPACITOR-FXD .022UF +-20% 100VDC CER	28480	0160-0574
A6C14†	0160-0574	3		CAPACITOR-FXD .022UF +-20% 100VDC CER	28480	0160-0574
A6C15	0180-0291	3		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A6C16	0160-3094	8		CAPACITOR-FXD .1UF +-10% 100VDC CER	28480	0160-3094
A6C17	0160-0127	2		CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A6C18	0160-0127	2		CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A6C19	0160-0127	2		CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A6C20	0160-0158	9	1	CAPACITOR-FXD 5600PF +-10% 200VDC POLYE	28480	0160-0158
A6C21	0160-0127	2		CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A6CR1	1910-0022	8	2	DIODE-GE 5V 60MA 3.5NS DO-7	28480	1910-0022
A6CR2	1910-0022	8		DIODE-GE 5V 60MA 3.5NS DO-7	28480	1910-0022
A6CR3	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR4	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR5	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR6	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR7	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR8	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR9	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR10	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040

See introduction to this section for ordering information
*Indicates factory selected value
† BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A6CR11	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6CR12	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A6L1	9140-0144	0		COIL-MLD 4.7UH 10% Q=45 .095DX.25LG-NOM	28480	9140-0144
A6Q1	1853-0034	0	1	TRANSISTOR PNP SI TO-18 PD=360MW	28480	1853-0034
A6Q2	1854-0053	5	1	TRANSISTOR NPN 2N2218 SI TO-5 PD=800MW	04713	2N2218
A6Q3	1854-0039	7	1	TRANSISTOR NPN 2N30538 SI TO-39 PD=1W	01928	2N30538
	1205-0011	0	3	HEAT SINK TO-5/TO-39-CS	28480	1205-0011
A6Q4	1853-0209	1	1	TRANSISTOR PNP SI TO-39 PD=1W FT=150MHZ	28480	1853-0209
	1205-0011	0		HEAT SINK TO-5/TO-39-CS	28480	1205-0011
A6Q5	1853-0020	4	1	TRANSISTOR PNP SI PD=300MW FT=150MHZ	28480	1853-0020
A6Q6	1855-0020	8		TRANSISTOR J-FET N-CHAN D-MODE TO-18 SI	28480	1855-0020
A6R1	0698-7215	2	2	RESISTOR 133 1% .05W F TC=0+-100	24546	C3-1/8-T0-133R-G
A6R2	0698-7215	2		RESISTOR 133 1% .05W F TC=0+-100	24546	C3-1/8-T0-133R-G
A6R3	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A6R4	0698-3405	4	1	RESISTOR 422 1% .5W F TC=0+-100	28480	0698-3405
A6R5	0698-7209	4	1	RESISTOR 75 1% .05W F TC=0+-100	24546	C3-1/8-T00-75R0-G
A6R6†	2100-2060	2	1	RESISTOR-TRMR 50 20% C TOP-ADJ 1-TRN	73138	82PR50
A6R7	0698-7211	8	1	RESISTOR 90.9 1% .05W F TC=0+-100	24546	C3-1/8-T00-90R9-G
A6R8	0698-7230	1	2	RESISTOR 562 1% .05W F TC=0+-100	24546	C3-1/8-T0-562R-G
A6R9	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-G
A6R10	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A6R11	0698-7260	7		RESISTOR 10K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1002-G
A6R12	0698-7264	1	2	RESISTOR 14.7K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1472-G
A6R13	0698-7223	2		RESISTOR 287 1% .05W F TC=0+-100	24546	C3-1/8-T0-287R-G
A6R14	0698-7284	5		RESISTOR 100K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1003-G
A6R15	0698-7252	7	1	RESISTOR 4.64K 1% .05W F TC=0+-100	24546	C3-1/8-T0-4641-G
A6R16	0698-7284	5		RESISTOR 100K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1003-G
A6R17	0698-7264	1		RESISTOR 14.7K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1472-G
A6R18	0698-7224	3		RESISTOR 316 1% .05W F TC=0+-100	24546	C3-1/8-T0-316R-G
A6R19	0698-7240	3		RESISTOR 1.47K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1471-G
A6R20†	2100-2216	0		RESISTOR-TRMR 5K 10% C TOP-ADJ 1-TRN	73138	82PR5K
A6R21	0698-7230	1		RESISTOR 562 1% .05W F TC=0+-100	24546	C3-1/8-T0-562R-G
A6R22	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A6R23	0698-7212	9		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A6R24	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A6R25	0757-1094	9	1	RESISTOR 1.47K 1% .125W F TC=0+-100	24546	C4-1/8-T0-1471-F
A6R26	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A6R27	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A6R28	0698-7212	9		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A6R29	0698-7243	6		RESISTOR 1.96K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1961-G
A6R30	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A6R31	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A6R32	0757-0280	3		RESISTOR 1K 1% .125W F TC=0+-100	24546	C4-1/8-T0-1001-F
A6R33	0683-0475	1	2	RESISTOR 4.7 5% .25W FC TC=-400/+500	01121	CB47G5
A6R34	0683-0475	1		RESISTOR 4.7 5% .25W FC TC=-400/+500	01121	CB47G5
A6R35	0683-0275	9	1	RESISTOR 2.7 5% .25W FC TC=-400/+500	01121	CB27G5
A6R36	0698-3427	0	3	RESISTOR 13.3 1% .125W F TC=0+-100	03888	PME55-1/8-T0-13R3-F
A6R37	0757-0346	2	1	RESISTOR 10 1% .125W F TC=0+-100	24546	C4-1/8-T0-10R0-F
A6R38	0698-3427	0		RESISTOR 13.3 1% .125W F TC=0+-100	03888	PME55-1/8-T0-13R3-F
A6R39	0698-3427	0		RESISTOR 13.3 1% .125W F TC=0+-100	03888	PME55-1/8-T0-13R3-F
A6R40	0757-0795	5	1	RESISTOR 75 1% .5W F TC=0+-100	19701	MF-1/2-T0-75R0-F
A6R41†	0698-7212	9		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A6U1	1821-0001	4	1	TRANSISTOR ARRAY	01928	CA3046
A6U2	1820-0054	5	1	IC GATE TTL NAND QUAD 2-INP	01295	SN7400N
A6VR1	1902-3048	7	1	DIODE-ZNR 3.48V 5% DO-7 PD=.4W TC=-.058%	28480	1902-3048
A6VR2	1902-3002	3	1	DIODE-ZNR 2.37V 5% DO-7 PD=.4W TC=-.074%	28480	1902-3002
				A6 MISCELLANEOUS		
	0360-1514	7	2	TERMINAL-STUD SGL-PIN PRESS-MTG	28480	0360-1514
	1480-0073	6		PIN-ROLL .062-IN-DIA .25-IN-LG BE-CU	28480	1480-0073
	4040-0748	3		EXTR-PC BD BLK POLYC .062-BD-THKNS	28480	4040-0748
	4040-0750	7	1	EXTR-PC BD RED POLYC .062-BD-THKNS	28480	4040-0750
A7†	11661-60086	6	1	YIG LOOP PHASE DETECTOR ASSEMBLY	28480	11661-60086
A7C1	0180-0291	3		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C2	0121-0036	0	1	CAPACITOR-V TRMR-CER 5.5-18PF 350V	52763	304324 5.5/18PF NPO
A7C3	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A7C4	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A7C5	0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A7C6	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A7C7	0180-0291	3		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C8	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A7C9	0180-0291	3		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C10	0180-0374	3	1	CAPACITOR-FXD 10UF+-10% 20VDC TA	56289	150D106X9020B2

See introduction to this section for ordering information
*Indicates factory selected value
†BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A7C11	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A7C12	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A7C13	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A7C14	0160-0575	4		CAPACITOR-FXD .047UF +-20% 50VDC CER	28480	0160-0575
A7C15†	0160-0939	4	1	CAPACITOR-FXD 430PF +-5% 300VDC MICA	28480	0160-0939
A7C16	0140-0194	1	1	CAPACITOR-FXD 110PF +-5% 300VDC MICA	72136	DM15F111J0300WV1CR
A7C17†	0160-3879	7		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879
A7C18	0160-0945	2	1	CAPACITOR-FXD 910PF +-5% 100VDC MICA	28480	0160-0945
A7C19	0160-0127	2		CAPACITOR-FXD 1UF +-20% 25VDC CER	28480	0160-0127
A7C20-A7C22†				DELETED		
A7CR1	1901-0025	2	1	DIODE-GEN PRP 100V 200MA DO-7	28480	1901-0025
A7CR2†	1901-0639	4	1	DIODE-PIN 110V	28480	5082-3060
A7L1	9140-0144	0		COIL-MLD 4.7UH 10% Q=45 .095DX.25LG-NOM	28480	9140-0144
A7L2	9140-0142	8	1	COIL-MLD 2.2UH 10% Q=32 .095DX.25LG-NOM	28480	9140-0142
A7Q1	1854-0019	3	2	TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0019
A7Q2	1853-0001	1	1	TRANSISTOR PNP SI TO-39 PD=600MW	28480	1853-0001
	1205-0011	0		HEAT SINK TO-5/TO-39-CS	28480	1205-0011
A7Q3	1854-0019	3		TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0019
A7R1	0757-0416	7	2	RESISTOR 511 1% .125W F TC=0+-100	24546	C4-1/8-TO-511R-F
A7R2	0698-7212	9		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-TO-100R-G
A7R3	0757-0420	3	1	RESISTOR 750 1% .125W F TC=0+-100	24546	C4-1/8-TO-751-F
A7R4	0698-3444	1	3	RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-TO-316R-F
A7R5	0698-3430	5	4	RESISTOR 21.5 1% .125W F TC=0+-100	03888	PME55-1/8-TO-21R5-F
A7R6	0698-3440	7	4	RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-TO-196R-F
A7R7	0698-3440	7		RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-TO-196R-F
A7R8	0698-3430	5		RESISTOR 21.5 1% .125W F TC=0+-100	03888	PME55-1/8-TO-21R5-F
A7R9	0698-3430	5		RESISTOR 21.5 1% .125W F TC=0+-100	03888	PME55-1/8-TO-21R5-F
A7R10	0698-7227	6	3	RESISTOR 422 1% .05W F TC=0+-100	24546	C3-1/8-TO-422R-G
A7R11	0698-7227	6		RESISTOR 422 1% .05W F TC=0+-100	24546	C3-1/8-TO-422R-G
A7R12	0698-7227	6		RESISTOR 422 1% .05W F TC=0+-100	24546	C3-1/8-TO-422R-G
A7R13	0757-0416	7		RESISTOR 511 1% .125W F TC=0+-100	24546	C4-1/8-TO-511R-F
A7R14	0698-3388	2	1	RESISTOR 14.7 1% .5W F TC=0+-100	28480	0698-3388
A7R15	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R16	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R17†	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R18	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R19	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R20	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R21†	0698-3132	4	2	RESISTOR 261 1% .125W F TC=0+-100	24546	C4-1/8-TO-2610-F
A7R22 †	0698-3132	4		RESISTOR 261 1% .125W F TC=0+-100	24546	C4-1/8-TO-2610-F
A7R23	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R24	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R25	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R26	0698-7229	8		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-TO-511R-G
A7R27	0698-7225	4		RESISTOR 348 1% .05W F TC=0+-100	24546	C3-1/8-TO-348R-G
A7R28	0698-7219	6		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-TO-196R-G
A7R29	0698-7229	8		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-TO-511R-G
A7R30	0698-3444	1		RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-TO-316R-F
A7R31	2100-2413	9	1	RESISTOR-TRMR 200 10% C SIDE-ADJ 1-TRN	30983	ET50X201
A7R32	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7R33	0698-7236	7		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-TO-1001-G
A7T1	08552-6024	9	1	TRANSFORMER, RF YELLOW	28480	08552-6024
A7TP1	1251-0600	0	2	CONNECTOR-SGL CONT PIN 1.14-MM-BSC-BZ SG	28480	1251-0600
A7TP2	1251-0600	0		CONNECTOR-SGL CONT PIN 1.14-MM-BSC-BZ SG	28480	1251-0600
A7U1	1820-0810	1	1	IC RCVR ECL LINE RCVR TPL 2-INP	04713	MC10116P
A7U2	1820-0802	1	1	IC GATE ECL NOR QUAD 2-INP	04713	MC10102P
A7U3	1826-0092	3	1	OP AMP GP DUAL TO-99	28480	1826-0092
A7U4	10534C	8	1	MIXER, DOUBLE BALANCE	28480	10534C
A7U5-A7U6†				DELETED		
A7VR1	1902-0025	4	1	DIODE-ZNR 10V 5% DO-7 PD=.4W TC=+.06%	28480	1902-0025
A7VR2	1902-0041	4	1	DIODE-ZNR 5.11V 5% DO-7 PD=.4W TC=-.009%	28480	1902-0041
A7VR3	1902-3059	0	1	DIODE-ZNR 3.83V 5% DO-7 PD=.4W TC=-.051%	28480	1902-3059
				A7 MISCELLANEOUS PARTS		
	1480-0073	6		PIN-ROLL .062-IN-DIA .25-IN-LG BE-CU	28480	1480-0073
	4040-0748	3		EXTR-PC BD BLK POLYC .062-BD-THKNS	28480	4040-0748
	4040-0749	4	2	EXTR-PC BD BRN POLYC .062-BD-THKNS	28480	4040-0749
A8†	11661-60074	2	1	50 MHZ FILTER ASSY (NON-REPAIRABLE)	28480	11661-60074
A9	11661-60073	1	1	20 MHZ FILTER ASSY (NON-REPAIRABLE)	28480	11661-60073

See introduction to this section for ordering information
*Indicates factory selected value
†BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A10	5086-7023	8	1	YIG OSCILLATOR ASSY	28480	5086-7023
A10J1	5086-6023	7		RESTORED 5086-7023 REQUIRES EXCHANGE NSR, MATES WITH A13P1	28480	5086-6023
A11	11661-60072	0	1	2.6-4.1 GHZ BAND PASS FILTER ASSY	28480	11661-60072
A11MP1	1250-1295	9	1	CONNECTOR, RF SMA M (PART OF A11W1)	28480	1250-1295
A11W1				NSR, YIG OUTPUT CABLE,BLACK(INCL A11MP1)		
A11W2				NSR, YIG LOOP OUTPUT CABLE,BLACK(INCL J2		
A11W3				NSR,YIG LOOP FEEDBACK CABLE, GRAY/GREEN (INCLUDES MP2)		
A12†	11661-60076	4	1	4.43 GHZ BAND PASS FILTER ASSY	28480	11661-60076
A12W1				NSR,4.43GHZ BPF INPT,GRAY/RED(INCL MP2)		
A12W2				NSR,4.43GHZ BPF OUPT,GRAY/ORG,(INCL MP2)		
				A12 MISCELLANEOUS		
	11661-00017	7	2	COVER, FILTER HOUSING	28480	11661-00017
	11661-00018	8	1	BRACKET, FILTER	28480	11661-00018
A13	11661-60049	1	1	YIG WIRING HARNESS ASSY	28480	11661-60049
A13MP1	1251-2570	7	7	CONTACT-CONN U/W-MICRO MALE CRP	28480	1251-2570
A13P1	1251-2581	0	1	HSG-STRP CONN,MICRO SER, 9 POS	28480	1251-2581
A14	11661-60054	1	1	OSCILLATOR-REGULATOR ASSEMBLY	28480	11661-60054
A14C1	0160-0575	4		CAPACITOR-FXD .047UF +-20% 50VDC CER	28480	0160-0575
A14C2	0160-0575	4		CAPACITOR-FXD .047UF +-20% 50VDC CER	28480	0160-0575
A14Q1	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
	1205-0202	1	2	THERMAL LINK DUAL TO-18-PKG	28480	1205-0202
A14Q2	1854-0210	6	2	TRANSISTOR NPN 2N2222 SI TO-18 PD=500MW	04713	2N2222
A14Q3	1853-0007	7		TRANSISTOR PNP 2N3251 SI TO-18 PD=360MW	04713	2N3251
	1205-0202	1		THERMAL LINK DUAL TO-18-PKG	28480	1205-0202
A14Q4	1854-0210	6		TRANSISTOR NPN 2N2222 SI TO-18 PD=500MW	04713	2N2222
A14R1	0698-3444	1		RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-T0-316R-F
A14R2	2100-3154	7		RESISTOR-TRMR 1K 10% C SIDE-ADJ 17-TRN	02111	43P102
A14R3	2100-3154	7		RESISTOR-TRMR 1K 10% C SIDE-ADJ 17-TRN	02111	43P102
A14R4	0757-0438	3	1	RESISTOR 5.11K 1% .125W F TC=0+-100	24546	C4-1/8-T0-5111-F
A14R5	0698-3440	7		RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-T0-196R-F
A14R6	0757-0279	0	2	RESISTOR 3.16K 1% .125W F TC=0+-100	24546	C4-1/8-T0-3161-F
A14R7	0757-0401	0		RESISTOR 100 1% .125W F TC=0+-100	24546	C4-1/8-T0-101-F
A14R8	0698-3440	7		RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-T0-196R-F
A14R9	0757-0279	0		RESISTOR 3.16K 1% .125W F TC=0+-100	24546	C4-1/8-T0-3161-F
A14R10	0757-0401	0		RESISTOR 100 1% .125W F TC=0+-100	24546	C4-1/8-T0-101-F
A14R11	0698-3430	5		RESISTOR 21.5 1% .125W F TC=0+-100	03888	PME55-1/8-T0-21R5-F
				A14 MISCELLANEOUS		
	0380-0636	4	1	STANDOFF-RVT-ON .25-IN-LG 2-56THD	00000	ORDER BY DESCRIPTION
	0340-0447	1	1	INSULATOR-XSTR DAP-GL	28480	0340-0447
				CHASSIS PARTS		
J1	1250-1221	1	2	SUM LOOP OUTPUT JACK (P/O W10)	28480	1250-1221
J2	1250-1221	1		YIG LOOP OUTPUT JACK (P/O A11W2)	28480	1250-1221
L1-L2†	9170-0499	1	2	CORE TOROID AL=2135-NH7T	28480	9170-0499
MP1	1251-0546	3	1	CONN: R&P CONT: RECT SER: COAXSKT (P/O W1, W3, W6, &W8; 1 EACH)	81312	III-170548
MP2	1250-1193	6	1	CONNECTOR-RF SM-SLD FEM UNMTD 50-OHM (P/O A11W3, A12W1, A12W2, AND W1, W3, W6, W7, W8, W9, W10, W12, W13; 1 EACH & P/O W2, W4, W5; 2 EACH)	28480	1250-1193
MP3	1250-0885	1	1	CONNECTOR, RF SMB FEM (P/O W7)	28480	1250-0885
P4	11661-60018	4	1	CONNECTOR ASSY(INCL W1, W3, W6 & W8)	28480	11661-60018
	5040-0380	0	1	CONNECTOR BODY	28480	5040-0380
	5040-0381	1	1	CONNECTOR FACE	28480	5040-0381
	1251-3087	3	1	CONTACT-CONN U/W-RECT FEM CRP	28480	1251-3087
R1†	11661-00019	9	1	OSCILLATOR-REGULATOR INSULATOR DELETED	28480	11661-00019
W1	11661-60067	3	1	CABLE ASSY, 20 MHZ FM/CW REFERENCE INPUT WHITE/RED (P/O P4, INCLUDES MP1 & MP2)	28480	11661-60067
W2	11661-60064	0	1	CABLE ASSY,20 MHZ FILTER OUTPUT, RED (INCLUDES MP2)	28480	11661-60064
W3	11661-60069	5	1	CABLE ASSY, 100 MHZ REFERENCE INPUT, WHITE/BROWN(P/O P4, INCLUDES MP1 & MP2)	28480	11661-60069

See introduction to this section for ordering information
*Indicates factory selected value
† BACKDATING INFORMATION IN SECTION VII

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
W4	11661-60065	1	1	CABLE ASSY, 20 MHZ FILTER INPUT, WHITE (INCLUDES MP2)	28480	11661-60065
W5	11661-60066	2	1	CABLE ASSY, 50 MHZ FILTER INPUT(INCL MP2	28480	11661-60066
W6	11661-60070	8	1	CABLE ASSY, 360 TO 450 MHZ INPUT, WHITE/ YELLOW (P/O P4; INCLUDES MP1 & MP2)	28480	11661-60070
W7	11661060056	8	1	CABLE ASSY,50 MHZ FILTER OUTPUT, YELLOW (INCLUDES MP2 AND MP3)	28480	11661060056
W8	11661-60068	4	1	CABLE ASSY, 20 TO 30 MHZ INPUT, WHITE/ ORANGE (P/O P4; INCLUDES MP1 & MP2)	28480	11661-60068
W9	11661-60057	1	1	CABLE ASSY, VCO CONTROL SIGNAL, BLUE (INCLUDES MP2)	28480	11661-60057
W10	11661-60058	2	1	CABLE ASSY, SUM LOOP OUTPUT, BLACK (INCLUDES MP2 AND J1)	28480	11661-60058
W11†				DELETED		
W12	11661-60028	6	1	CABLE ASSY, SUM LOOP INTERCONNECT, GRAY (INCLUDES MP2)	28480	11661-60028
W12	1250-1373	4	2	CONNECTOR	28480	1250-1373
W13	11661-60026	4	1	CABLE ASSY, YIG LOOP INTERCONNECT, GRAY/BLUE (INCLUDES MP2)	28480	11661-60026
W13	1250-1373	4		CONNECTOR-RF SM-SLD FEM SGL-HOLE-RR	28480	1250-1373
				CHASSIS MISCELLANEOUS		
	2360-0055	1	3	SCREW-MACH 6-32 .188-IN-LG BDG-HD-SLT	00000	ORDER BY DESCRIPTION
	3050-0003	3	3	WASHER-FL NM NO. 6 .141-IN-ID .375-IN-OD	28480	3050-0003
	6960-0016	0	11	PLUG-HOLE TR-HD FOR .125-D-HOLE NYL	28480	6960-0016
	11661-00003	1	1	CLAMP, YIG OSCILLATOR	28480	11661-00003
	11661-00013	3	1	FRAME	28480	11661-00013
	11661-00014	4	1	BRACKET, CONNECTOR	28480	11661-00014
	11661-00015	5	1	PANEL, TOP	28480	11661-00015
	11661-20022	6	1	GUIDE, PC BOARD	28480	11661-20022
	11661-60081	1	1	SHIELD ASSEMBLY, RF	28480	11661-60081
	11661-20031	7	1	WIRE GASKET	28480	11661-20031
	11661-20040	8	2	RFI SPRING CLIP	28480	11661-20040

Table 6-3. Manufacturers Code List

Mfr Code	Manufacturer Name	Address		Zip Code
00000	ANY SATISFACTORY SUPPLIER			
01121	ALLEN-BRADLEY CO	MILWAUKEE	WI	53204
01295	TEXAS INSTR INC SEMICOND CMPNT DIV	DALLAS	TX	75222
01928	RCA CORP SOLID STATE DIV	SOMERVILLE	NJ	08876
02111	SPECTROL ELECTRONICS CORP	CITY OF IND	CA	91745
03888	KDI PYROFILM CORP	WHIPPANY	NJ	07981
04713	MOTOROLA SEMICONDUCTOR PRODUCTS	PHOENIX	AZ	85062
12954	SIEMENS CORP COMPONENTS GROUP	SCOTTSDALE	AZ	85252
19701	MEPCO/ELECTRA CORP	MINERAL WELLS	TX	76067
24226	GOWANDA ELECTRONICS CORP	GOWANDA	NY	14070
24546	CORNING GLASS WORKS (BRADFORD)	BRADFORD	PA	16701
28480	HEWLETT-PACKARD CO CORPORATE HQ	PALO ALTO	CA	94304
30983	MEPCO/ELECTRA CORP	SAN DIEGO	CA	92121
52763	STETTNER-TRUSH INC	CAZENOVIA	NY	13035
56289	SPRAGUE ELECTRIC CO	NORTH ADAMS	MA	01247
72136	ELECTRO MOTIVE CORP SUB IEC	WILLIMANTIC	CT	06226
73138	BECKMAN INSTRUMENTS INC HELIPOT DIV	FULLERTON	CA	92634
81312	WINCHESTER ELEK DIV LITTON IND INC	OAKVILLE	CT	06779
99800	AMER PRCN IND INC DELEVAN DIV	AURORA	NY	14052

See introduction to this section for ordering information
*Indicates factory selected value
† BACKDATING INFORMATION IN SECTION VII

SECTION VII
MANUAL CHANGES

7-1. INTRODUCTION

7-2. This section contains manual change instructions for backdating this manual for HP Model 11661B Frequency Extension Module having serial number prefixes that are lower than 1801A. This section also contains instrument modification suggestions and procedures that are recommended to improve the performance and reliability of your instrument.

7-3. MANUAL CHANGES

7-4. To adapt this manual to your instrument, refer to Table 7-1 and make all of the manual changes listed opposite your instrument's serial number prefix. The manual changes are listed in serial number prefix sequence and should be made in the sequence listed. Table 7-2 is a summary of changes by component.

7-5. If your instrument's serial number prefix is not listed on the title page of this manual or in Table 7-1, it may be documented in a MANUAL CHANGES supplement. For additional important information about serial number coverage, refer to INSTRUMENTS COVERED BY MANUAL in Section I.

Table 7-1. Manual Changes by Serial Number Prefix

Serial Number Prefix	Make Manual Changes
1439A	N,M,L,K,J,I,H,G,F,E,D,C,B,A
1511A	N,M,L,K,J,I,H,G,F,E,D,C,B
1515A	N,M,L,K,J,I,H,G,F,E,D,C
1533A	N,M,L,K,J,I,H,G,F,E,D
1538A	N,M,L,K,J,I,H,G,F,E
1543A	N,M,L,K,J,I,H,G,F
1544A	N,M,L,K,J,I,H,G

Serial Number Prefix	Make Manual Changes
1545A	N,M,L,K,J,I,H
1604A	N,M,L,K,J,I
1619A	N,M,L,K,J
1729A	N,M,L,K
1734A	N,M,L
1801A	N,M
1805A	N

7-6. MANUAL CHANGE INSTRUCTIONS

CHANGE A

Page 6-4, Table 6-2:
Change A1A2C8 to 0160-2257, CAPACITOR-FXD 10 PF ±5% 500 WVDC CER.

CHANGE B

Page 6-10, Table 6-2:
Delete A7C21, 0160-0575, CAPACITOR-FXD .047 UF ±20% 50 WVDC CER.
Delete A7CR2, 1901-0639, DIODE PIN 110V.
Delete A7R21 and R22, 0698-7236, RESISTOR 1K 2% .05W F TC = 0 ± 100.
Change A7C15 and C17 to 0160-0161 CAPACITOR FXD .01 UF ±10% 200 WVDC POLYE.
Change A7R17 to 2100-2574 RESISTOR VAR TRMR 500 OHM 10% C.
Change A7U5 to 1820-0223 IC LIN LM301AH AMPLIFIER.

Table 7-2. Summary of Changes by Component

Change	A1	A4	A5	A6	A7	A8	A12	A14	No Prefix
A	A2C8								
B					C15, C17, C21, CR2, R17, R21, R22, U5				
C							Assy Part No.		
D									R1
E		C8	R13, R15	C5, C6,C9, C13, C14, R6, R20, R41					
F	R1, R4, U2, U3, A2, A2R1, A2R3, A4, A4R1, A4R2, A4VR1							Add Assy	
G		R16, R20, R26, R32, R35, R38							
H									L1
I						Assy Part No.			
J						Assy Part No.			
K		R35, R38							
L		R11							
M					C22				L2
N					Assy Part No.				

MANUAL CHANGES

CHANGE B (Cont'd)

Figure 8-8:
Change as shown in Figure 7-1 (Part of Change B).

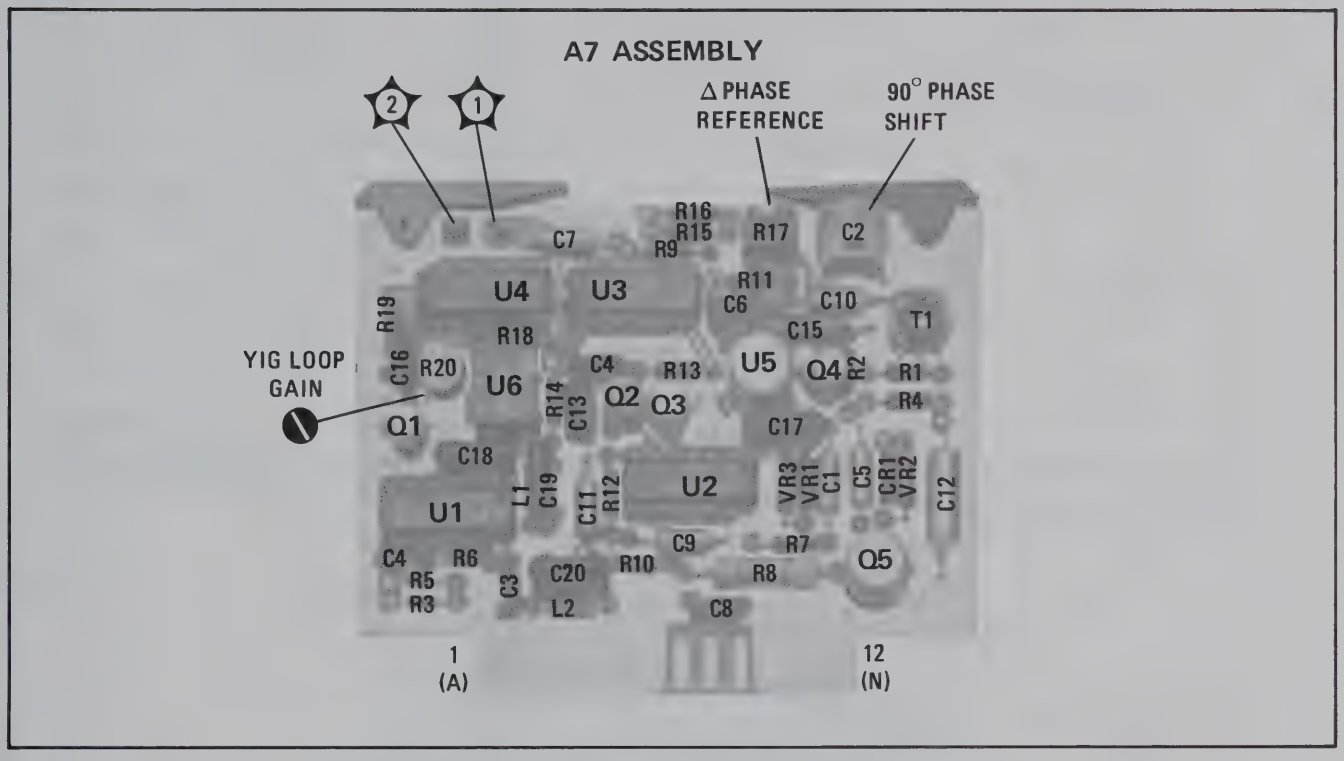


Figure 7-1. A7 YIG Loop Phase Detector Assembly Component and Test Point Location Backdating (Part of Change B)

Figure 8-9 (Service Sheet 4):
Change the diagram as shown in Figure 7-2, the partial schematic of YIG Loop Phase Detector Schematic Diagram (Part of Change B).

CHANGE C

Table 6-2:
Change A12 to 11661-60071 4.43 GHz BAND PASS FILTER ASSY.

CHANGE D

Table 6-2:
Change R1 to 2100-2646 RESISTOR VAR TRMR 100 OHM 10% C.

CHANGE E

Table 6-2:
Change A4C8* to 0140-0199 CAPACITOR-FXD 240 PF ±5% 300 WVDC MICA.

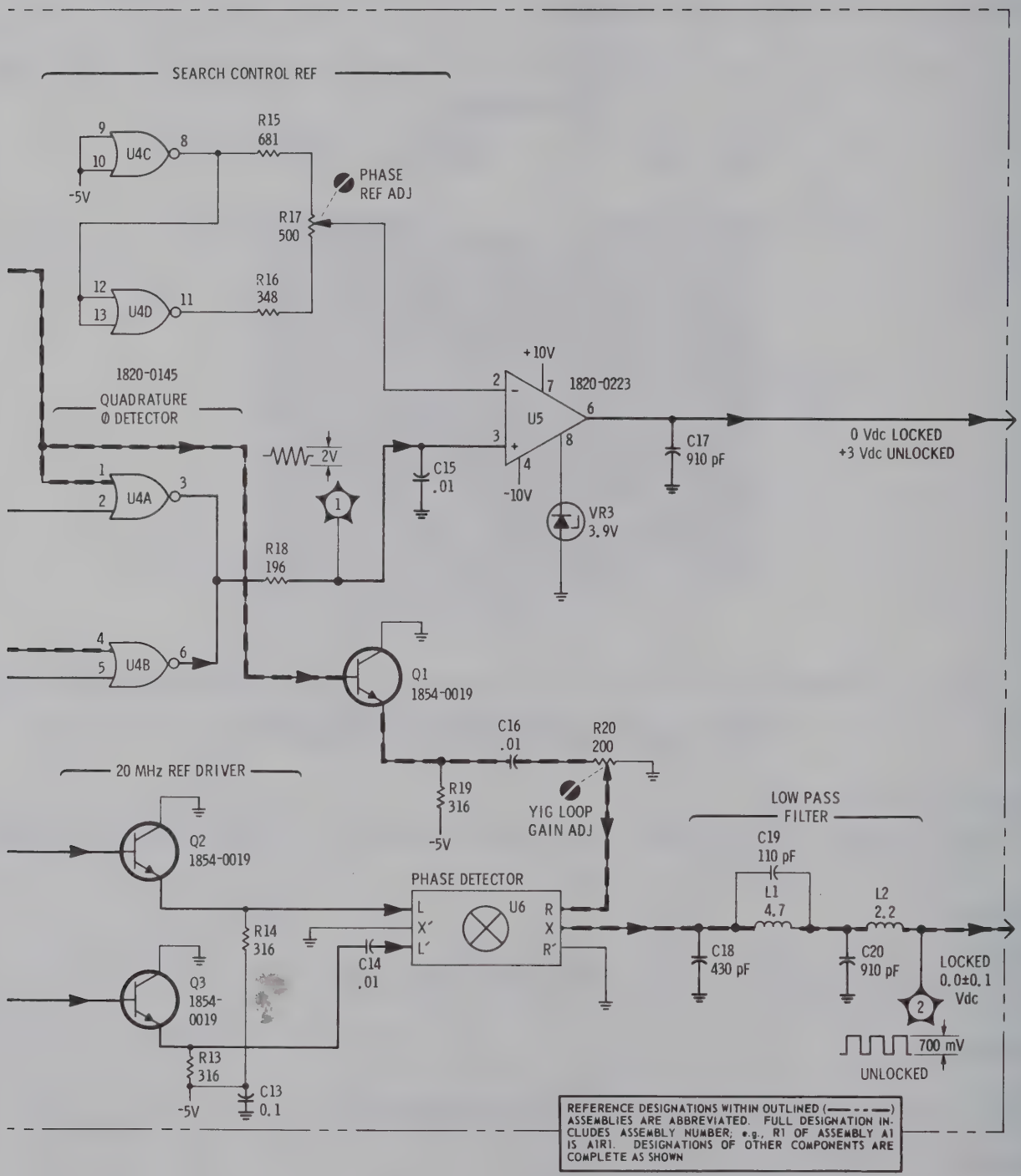


Figure 7-2. YIG Loop Phase Detector Schematic Backdating (Partial Diagram, Part of Change B)

MANUAL CHANGES

CHANGE E (Cont'd)

Table 6-2:

Change A5R13 to 2100-3109 RESISTOR VAR TRMR 2K OHM 10% C.

Change A5R15 to 2100-3154 RESISTOR VAR TRMR 1K OHM 10% C.

Change A6C5 and C6 to 0180-2141 CAPACITOR-FXD 3.3 UF ±10% 50 VDC TANT.

Change A6C9 to 0160-0166 CAPACITOR-FXD .068 UF ±10% 200 WVDC POLYE.

Change A6C13 and C14 to 0160-3879 CAPACITOR-FXD .01 UF ±20% 100 WVDC CER.

Change A6R6 to 2100-2520 RESISTOR VAR TRMR 50 OHM 20% C.

Change A6R20 to 0698-7236 RESISTOR 1K 2% .05W F TUBULAR.

Delete A6R41 0698-7212 RESISTOR FXD 100 OHM .05W F TC = 0 ±100.

Figure 8-5 (Service Sheet 2):

Change A5R13 to 2000.

Change A5R15 to 1000.

Figure 8-10:

Change as shown in Figure 7-3 (Part of Change E).

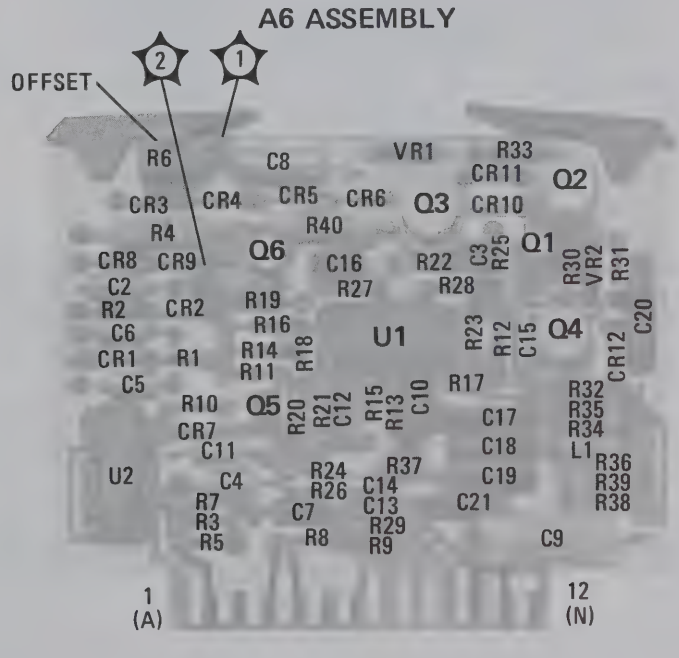


Figure 7-3. A6 FM Driver Assembly Component and Test Point Location Backdating (Part of Change E)

Figure 8-11 (Service Sheet 5):

Change A6C5 and C6 to 3.3 μF.

Change A6C9 to .068 μF.

Change R20 to a fixed 1000Ω resistor.

Delete A6R41.

MANUAL CHANGES

CHANGE E (Cont'd)

Figure 8-14:
Change as shown in Figure 7-4 (Part of Change E).

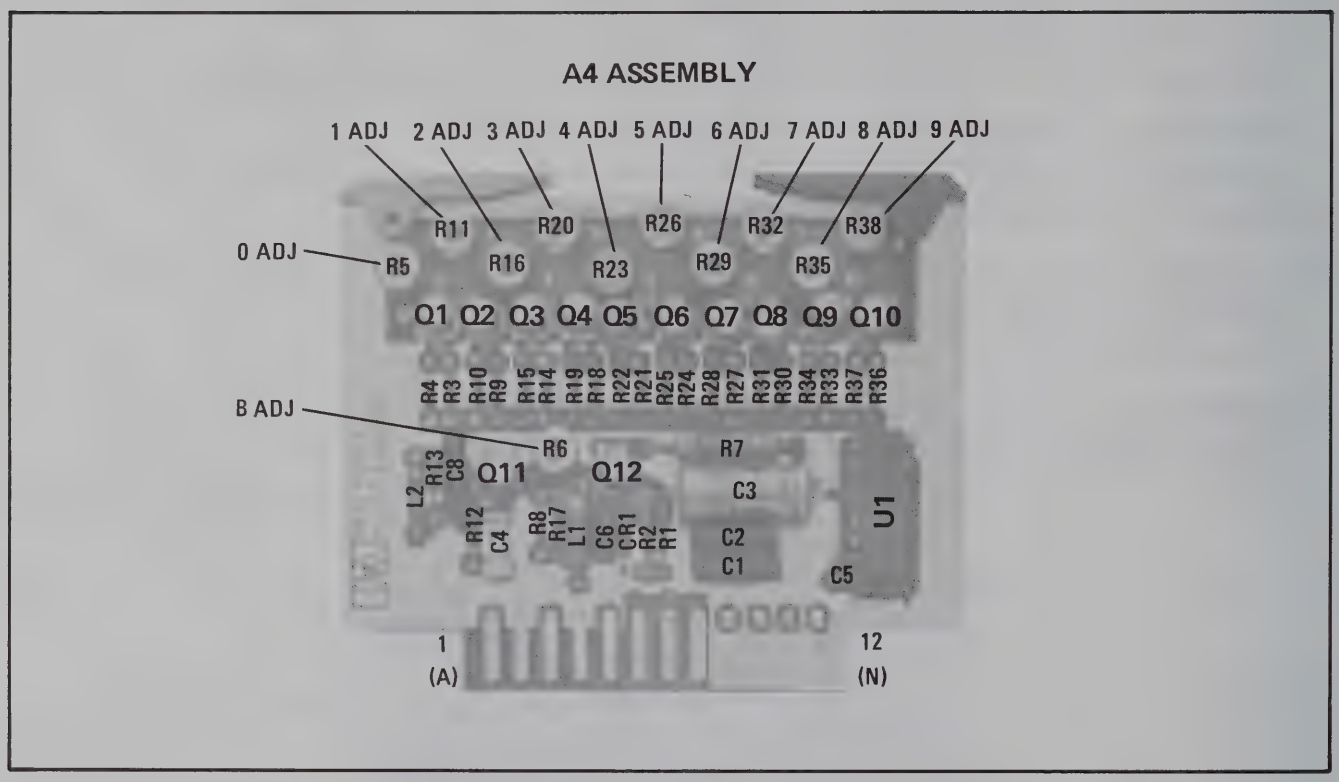


Figure 7-4. A4 Sum Loop Pretune Assembly Component Location Backdating (Part of Change E)

Figure 8-15 (Service Sheet 7):
Change A4C8 to 240 pF.

CHANGE F

Paragraph 5-27:
Step 4, change A14R2 to R1.

Paragraph 5-28:
Step 3, change A14R2 to R1.

Paragraph 5-30:
Change step 2 "Adjust the VCO bias control A1A2R3 for +10.0 Vdc as indicated on the DVM."

Paragraph 5-32:
Change in step 9 A14R2 to R1.

Table 6-2:
Change A1R1 to 0757-0401 RESISTOR 100 OHM 1% .125W F TUBULAR.

MANUAL CHANGES

CHANGE F (Cont'd)

Table 6-2:
Delete A1R4.
Change A1U2 to 5086-7054.
Change A1U3 to 5086-7053.
Change A1A2 to 11661-60008.
Change A1A2R1 to 0698-3440 RESISTOR FXD 196 OHM 1% .125W F TUBULAR.
Add A1A2R3 2100-1984 RESISTOR VAR TRMR 100 OHM 10%.
Change A1A4 to 11661-60012.
Change A1A4R1 to 0698-7195 RESISTOR 19.6 OHM 2% .05W F TUBULAR.
Add A1A4R2 0757-0405 RESISTOR FXD 162 OHM 1% .125W F TUBULAR.
Add A1A4VR1 1901-1034 DIODE STABISTOR 90V.
Delete A14 Oscillator Regulator Assembly.
Add R1 2100-2646 RESISTOR VAR TRMR 100 OHM 10% C.

Page 8-6, paragraph entitled 4.43 GHz Oscillator:
Change A14R2 to R1.

Figure 8-3 (Service Sheet 1):
Change the diagram as shown in the partial schematic of the Troubleshooting Block Diagram (Part of Change F).

Figure 8-7 (Service Sheet 3):
Change the diagram as shown in the partial schematic of the Oscillator Mixer Section (Part of Change F).

CHANGE G

Table 6-2:
Change A4R16 and A4R20 to 2100-2061 RESISTOR VAR TRMR 200 OHM 10% C.
Change A4R26 to 2100-1788 RESISTOR VAR TRMR 500 OHM 10% C.
Change A4R32 and A4R35 to 2100-1986 RESISTOR VAR TRMR 1K OHM 10% C.
Change A4R38 to 2100-2497 RESISTOR VAR TRMR 2K OHM 10% C.

Figure 8-15 (Service Sheet 7):
Change A4R16 and A4R20 to 200.
Change A4R26 to 500.
Change A4R32 and A4R35 to 1000.
Change A4R38 to 5000.

CHANGE H

Table 6-2:
Delete L1.

Figure 8-7 (Service Sheet 3):
Delete L1 on the +20V line going to A1C3.

MANUAL CHANGES

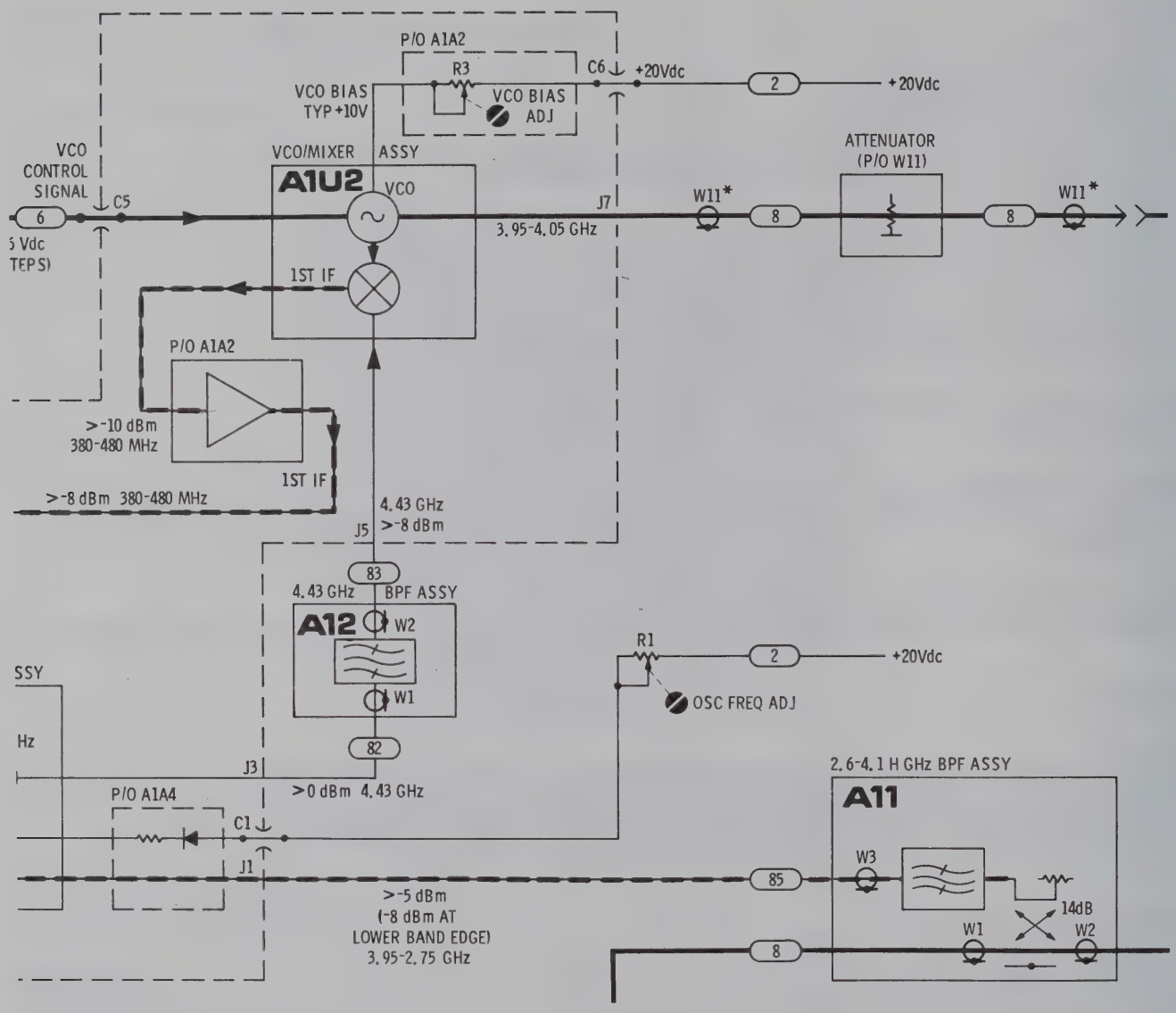


Figure 7-5. Troubleshooting Block Diagram Backdating (Partial Diagram Part of Change F)

CHANGE I

Table 6-2:

Change A8 to 11661-60074.

Figure 8-7 (Service Sheet 3):

Change A8 50 MHz Filter Assy to 11661-60074.

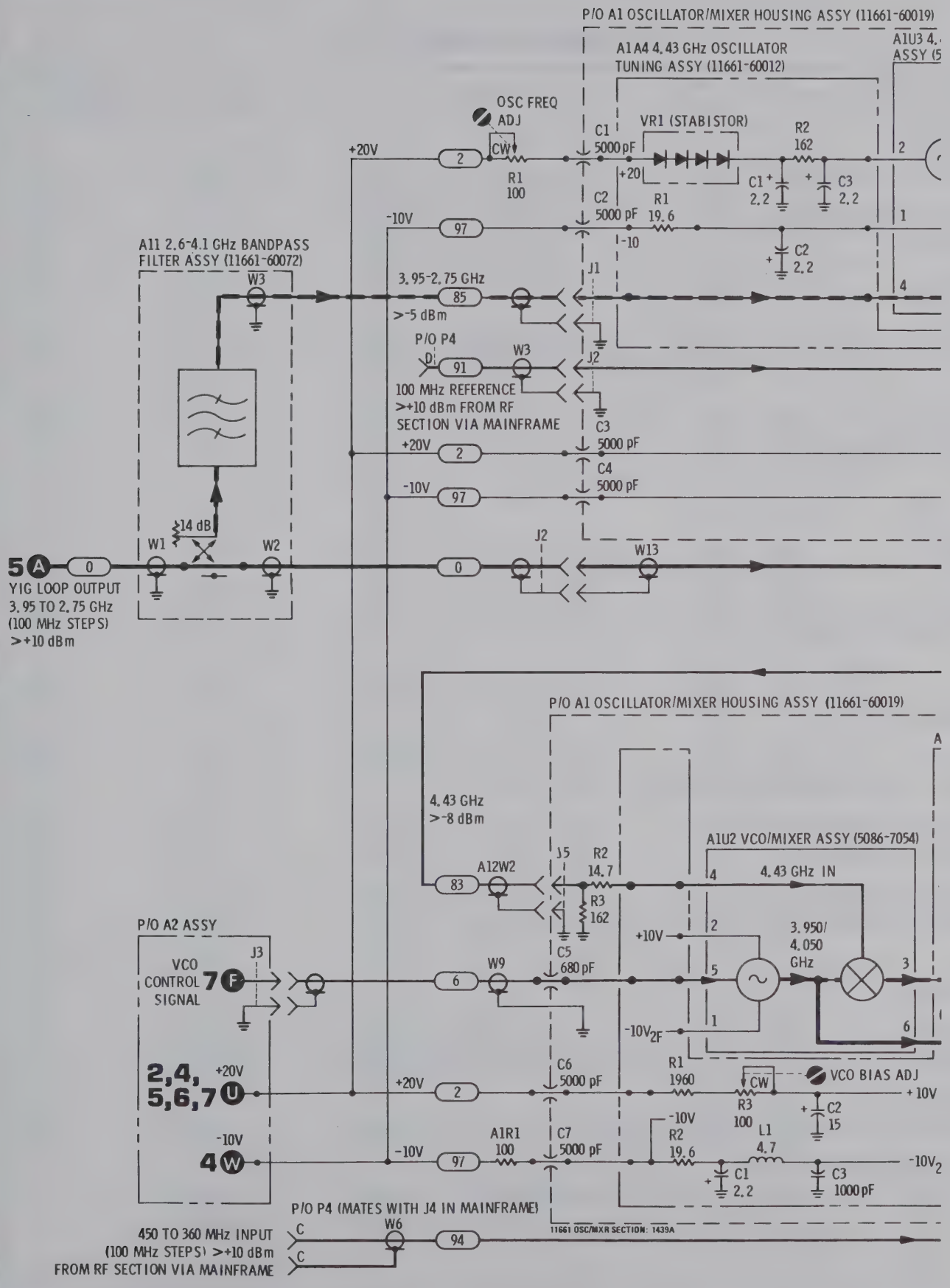


Figure 7-6. Oscillator/Mixer Section Schematic Diagram Backdating (Partial Diagram Part of Change F)

MANUAL CHANGES

CHANGE J

Table 6-2:
Change A8 to 11661-60081.

Figure 8-7 (Service Sheet 3):
Change A8 to 11661-60081.

CHANGE K

Table 6-2:
Change A4R35 to 2100-2497 RESISTOR VAR 2K OHM 10% C.
Change A4R38 to 2100-2216 RESISTOR VAR 5K OHM 10% C.

Figure 8-15 (Service Sheet 7):
Change A4R35 to 2000.
Change A4R38 to 5000.

CHANGE L

Table 6-2:
Change A4R11 to 2100-2061 RESISTOR VAR TRMR 200 OHM 10% C.

Figure 8-15 (Service Sheet 7):
Change R11 to 200.

CHANGE M

Table 6-2:
Delete A7C22, and L2 in CHASSIS PARTS.

Figure 8-9 (Service Sheet 4):
Delete A7C22.

Figure 8-11 (Service Sheet 5):
Delete L2 on the ground line going to the A10 assembly.

CHANGE N

Table 6-2:
Change A7 Replaceable Parts list as shown in Table 7-3 (Part of Change N).

Figure 8-8:
Change to Figure 7-7 (Part of Change N).

Figure 8-9 (Service Sheet 4):
Change to Figure 7-8 Partial Diagram (Part of Change N).

Table 7-3. Replaceable Parts (Part of Change N)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A7	11661-60003	1	YIG LOOP PHASE DETECTOR ASSY	28480	11661-60003
A7C1	0180-0291		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C2	0121-0036	1	CAPACITOR-V TRMR-CER 5.5/18PF 350V	73899	DV11PR18A
A7C3	0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480	0160-2055
A7C4	0160-3879		CAPACITOR-FXD .01UF +-20% 100WVDC CER	28480	0160-3879
A7C5	0180-0197		CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289	150D225X9020A2
A7C6	0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480	0160-2055
A7C7	0180-0291		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C8	0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480	0160-2055
A7C9	0180-0291		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C10	0180-0291		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C11	0180-0291		CAPACITOR-FXD 1UF+-10% 35VDC TA	56289	150D105X9035A2
A7C12	0180-0374	1	CAPACITOR-FXD 10UF+-10% 20VDC TA	56289	150D106X9020B2
A7C13	0160-3094		CAPACITOR-FXD .1UF +-10% 100WVDC CER	28480	0160-3094
A7C14	0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480	0160-2055
A7C15†	0160-3879		CAPACITOR-FXD .01UF +-20% 100WVDC CER	28480	0160-3879
A7C16	0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480	0160-2055
A7C17†	0160-3879		CAPACITOR-FXD .01UF +-20% 100WVDC CER	28480	0160-3879
A7C18	0160-0939	1	CAPACITOR-FXD 430PF +-5% 300WVDC MICA	28480	0160-0939
A7C19	0140-0194	1	CAPACITOR-FXD 110PF +-5% 300WVDC MICA	72136	DM15F111J0300WV1CR
A7C20	0160-0945	1	CAPACITOR-FXD 910PF +-5% 100WVDC MICA	28480	0160-0945
A7C21†	0160-0575		CAPACITOR-FXD .047UF +-20% 50WVDC CER	28480	0160-0575
A7CR1	1901-0025	1	DIODE-GEN PRP 100V 200MA DO-7	28480	1901-0025
A7CR2†	1901-0639	1	DIODE-PIN 110V	28480	1901-0639
A7L1	9140-0144		COIL-FXD MOLDED RF CHOKE 4.7UH 10%	24226	10/471
A7L2	9140-0142	1	COIL-MLD 2.2UH 10% Q=32 .095DX.25LG	99800	1025-28
A7Q1	1854-0019	4	TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0019
A7Q2	1854-0019		TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0019
A7Q3	1854-0019		TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0019
A7Q4	1854-0019		TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0019
A7Q5	1853-0001	1	TRANSISTOR PNP SI TO-39 PD=600MW	28480	1853-0001
	1205-0011		HEAT SINK TO-5/TO-39-PKG	28480	1205-0011
A7R1	0757-0416	2	RESISTOR 511 1% .125W F TC=0+-100	24546	C4-1/8-T0-511R-F
A7R2	0698-7212		RESISTOR 100 1% .05W F TC=0+-100	24546	C3-1/8-T0-100R-G
A7R3	0757-0401		RESISTOR 100 1% .125W F TC=0+-100	24546	C4-1/8-T0-101-F
A7R4	0698-3444	5	RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-T0-316R-F
A7R5	0698-3440	5	RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-T0-196R-F
A7R6	0698-7227	1	RESISTOR 422 1% .05W F TC=0+-100	24546	C3-1/8-T0-422R-G
A7R7	0757-0416		RESISTOR 511 1% .125W F TC=0+-100	24546	C4-1/8-T0-511R-F
A7R8	0698-3388	1	RESISTOR 14.7 1% .5W F TC=0+-100	19701	MF7C-1
A7R9	0698-3440		RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-T0-196R-F
A7R10	0757-0401		RESISTOR 100 1% .125W F TC=0+-100	24546	C4-1/8-T0-101-F
A7R11	0698-3440		RESISTOR 196 1% .125W F TC=0+-100	24546	C4-1/8-T0-196R-F
A7R12	0698-3447	1	RESISTOR 422 1% .125W F TC=0+-100	24546	C4-1/8-T0-422R-F
A7R13	0698-3444		RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-T0-316R-F
A7R14	0698-3444		RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-T0-316R-F
A7R15	0698-7232	1	RESISTOR 681 1% .05W F TC=0+-100	24546	C3-1/8-T0-681R-G
A7R16	0698-7225		RESISTOR 348 1% .05W F TC=0+-100	24546	C3-1/8-T0-348R-G
A7R17†	0698-7229		RESISTOR 511 1% .05W F TC=0+-100	24546	C3-1/8-T0-511R-G
A7R18	0698-7219		RESISTOR 196 1% .05W F TC=0+-100	24546	C3-1/8-T0-196R-G
A7R19	0698-3444		RESISTOR 316 1% .125W F TC=0+-100	24546	C4-1/8-T0-316R-F
A7R20	2100-2061		RESISTOR-TRMR 200 10% C TOP-ADJ 1-TRN	73138	62-204-1
A7R21††	0698-7236		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A7R22 †	0698-7236		RESISTOR 1K 1% .05W F TC=0+-100	24546	C3-1/8-T0-1001-G
A7T1	08552-6024	1	TRANSFORMER, RF YELLOW	28480	08552-6024
A7U1	1820-0253	3	IC-DIGITAL MC1035P ECL DUAL	04713	MC1035P
A7U2	1820-0253		IC-DIGITAL MC1035P ECL DUAL	04713	MC1035P
A7U3	1820-0253		IC-DIGITAL MC1035P ECL DUAL	04713	MC1035P
A7U4	1820-0145	1	IC-DIGITAL MC1010P ECL QUAD 2 NOR	04713	MC1010P
A7U5†	1826-0092	1	IC MC 1458 OP AMP	28480	1826-0092
A7U6	10534C	1	MIXER	28480	10534C
A7VR1	1902-0025	1	DIODE-ZNR 10V 5% DO-7 PD=.4W TC=+.06%	28480	1902-0025
A7VR2	1902-0041	1	DIODE-ZNR 5.11V 5% DO-7 PD=.4W TC=-.009%	15818	CD 35622
A7VR3	1902-3059	1	DIODE-ZNR 3.83V 5% DO-7 PD=.4W TC=-.051%	15818	CD 35586
			A7 MISCELLANEOUS		
	0360-1514		TERMINAL-STUD SGL-PIN PRESS-MTG	28480	0360-1514
	1480-0073		PIN:DRIVE 0.250" LG	00000	OBD
	4040-0748		EXTRACTOR-PC BD BLK POLYC .062-BD-THKNS	28480	4040-0748
	4040-0749	1	EXTR-PC BD BRN POLYC .062-BD-THKNS	28480	4040-0749

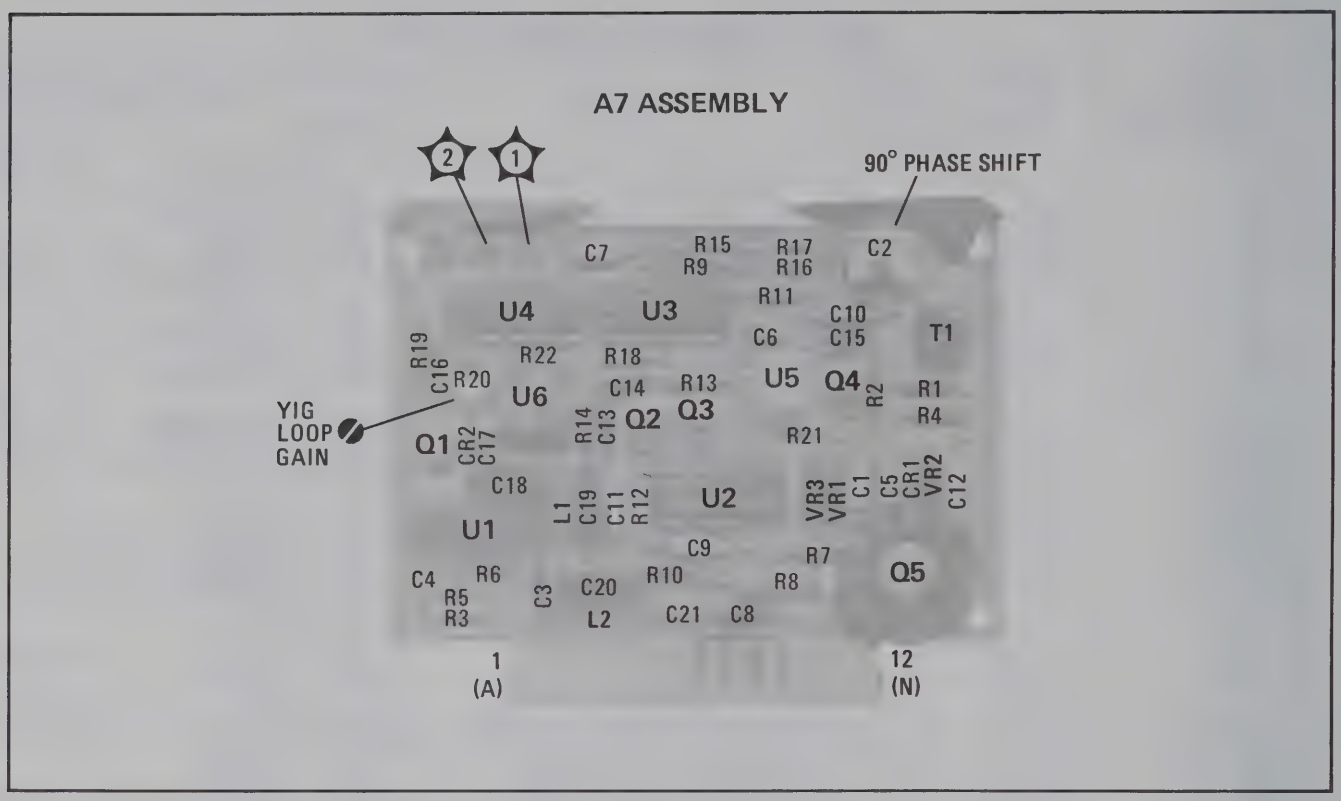


Figure 7-7. A7 YIG Loop Phase Detector Assembly Component and Test Point Locations (Part of Change N)

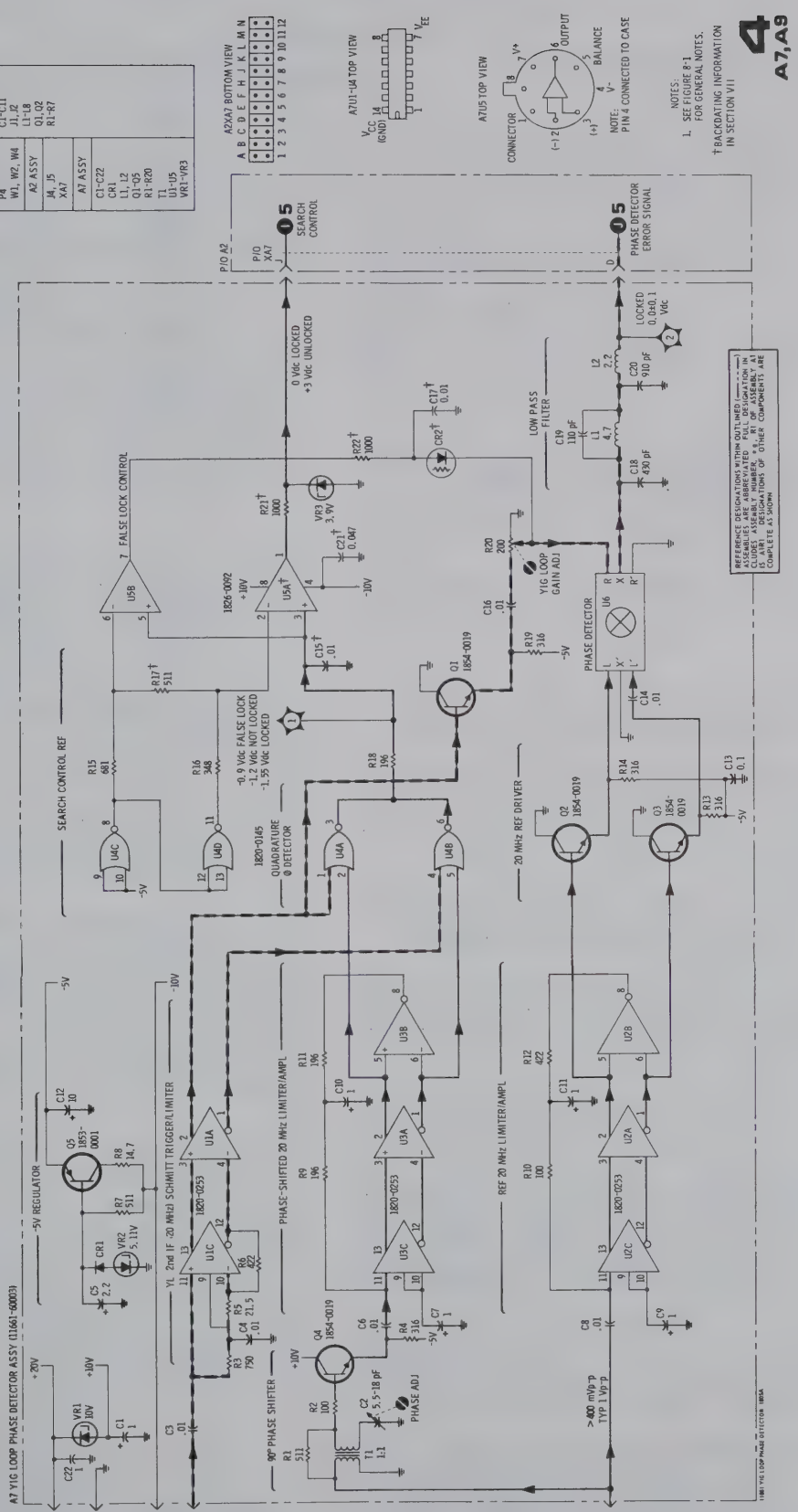


Figure 7-8. YIG Loop Phase Detector Section Schematic Diagram (Partial Diagram P/O Change N)

INSTRUMENT MODIFICATIONS

7-7. INSTRUMENT IMPROVEMENT MODIFICATIONS

7-8. Hewlett-Packard has developed certain recommended instrument modifications that can be used to improve the performance and reliability of earlier versions of the instrument. In some cases, replacing certain parts requires a modification to make these instruments compatible with parts now in use (if the original part is no longer available). These modifications are outlined in the following procedures and are keyed to instruments by serial number prefix.

7-9. Improvement of Sum Loop 1st I.F. Amplifier Output (Serial Number Prefix 1439A)

7-10. In instruments with serial number prefix 1439A change A1A2C8 and add R.F.I. spring mode suppressor to eliminate 2 MHz spur at the output of the A1A2 Sum Loop 1st I.F. Amplifier.

7-11. Improvement of YIG Loop Phase Detector (Serial Number Prefix 1511A and Below)

7-12. In instruments with serial number prefix 1511A and below add the operational amplifier A7U5 with associated circuitry to prevent a false lock condition.

7-13. Change to a More Reliable Potentiometer (Serial Number Prefix 1533A and Below)

7-14. In instruments with serial number prefix 1533A and below change the potentiometer R1 used to adjust the frequency of the 4.43 GHz oscillator.

7-15. Improvement in the FM Driver Board (Serial Number Prefix 1538A and Below)

7-16. In instruments with serial number prefix 1538A and below change A6R20 to a larger value. This will increase the range of the search control adjustment, and allow setting the proper search sweep range.

7-17. Improvement in the A5 YIG Loop Pretune Assembly (Serial Number Prefix 1538A and Below)

7-18. In instruments with serial number prefix 1538A and below change A5R13 and A5R15 to larger values to increase the adjustment range of the YIG Loop Pretune Assembly.

7-19. Improvement in the Reduction of Oscillator Drift (Serial Number Prefix 1543A and Below)

7-20. In instruments with serial number prefix 1543A and below add the A14 Oscillator Regulator Assembly to reduce frequency drift of the 4.43 GHz and voltage controlled oscillators.

7-21. Improvement in Sum Loop Pretune Board (Serial Number Prefix 1544A and Below)

7-22. In instruments with serial number prefix 1544A and below change the values of potentiometers A4R16, R20, R26, R32, R35, and R38. This will increase the range of tuning voltages available from the Sum Loop Pretune Assembly.

7-23. Improvement in the Reduction of the Spur Level (Serial Number Prefix 1545A and Below)

7-24. In instruments with serial number prefix 1545A and below add L1 to the +20V lead to the 20 MHz I.F. Amplifier Assembly. Addition of the toroid will reduce the level of the "J" spur.

7-25. Improvement in the Reduction of the Spur Level (Serial Number Prefix 1619A and Below)

7-26. In instruments with serial number prefix 1619A and below change the A8 50 MHz Filter Assembly. The new assembly will reduce the "O" spur seen at twice 11 MHz center frequencies caused by leakage of the 9th harmonic of the Sum Loop 2nd Mixer into the Sum Loop VCO output.

7-27. Improvement in Tuning Range of A4 Sum Loop Pretune (Serial Number Prefix 1729A and Below)

7-28. In instruments with serial number prefix 1729A and below increase the values of A4R35 and A4R38. The larger values allow an increase in the A4 Sum Loop Pretune tuning range.

SECTION VIII
SERVICE

8-1. INTRODUCTION

8-2. This section contains troubleshooting and repair information for the HP Model 11661B Frequency Extension Module. Safety considerations, principles of operation, and recommended test equipment are included.

8-3. The service sheets normally include principles of operation and troubleshooting information, a component location diagram, and a schematic, all relating to a specific portion of circuitry within the instrument.

8-4. Service Sheet 1 includes an overview of the instrument operation, troubleshooting to an assembly or stage level, and a troubleshooting block diagram. The block diagram also serves as an "index" for the other service sheets.

8-5. The last foldout in this section gives disassembly procedures, adjustment locations, test point locations, and a table which cross-references pictorial and schematic locations of each assembly and chassis mounted component.

8-6. SAFETY CONSIDERATIONS

8-7. Although this instrument has been designed in accordance with international safety standards, this manual contains information, catuions, and warnings which must be followed to ensure safe operation and to retain the instrument in safe condition (see Sections II, III, and V). Service and adjustments should be performed only by qualified service personnel.

8-8. Any adjustment, maintenance, and repair of the opened instrument under voltage should be avoided as much as possible and, when inevitable, should be carried out only by a skilled person who is aware of the hazard involved.

8-9. Capacitors inside the instrument may still be charged even if the instrument has been disconnected from its source of supply.

WARNING

The service information is often used with power supplied and protective covers removed from the instrument. Energy available at many points may, if contacted, result in personal injury.

8-10. PRINCIPLES OF OPERATION

8-11. Instrument operation is described under the Principles of Operation on the service sheets. Service Sheet 1, in conjunction with the Troubleshooting Block Diagram, describes overall operation of the Frequency Extension Module. Service Sheets 2 through 7 explain the function of each circuit within the unit. The particular circuit described is shown in schematic form on the accompanying circuit diagram.

8-12. TROUBLESHOOTING

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

8-13. Because feedback circuits extend over several assemblies for each of the phase lock loops, the major troubleshooting tests are on Service Sheet 1 with the Troubleshooting Block Diagram. Once the fault is localized, additional tests on the remaining Service Sheets help locate the defective component.

8-14. Troubleshooting Aids

8-15. Circuit Board Aids. Test points are physically located on the circuit boards as metal posts or circuit pads and usually have either a reference designator (such as TP1) or a label which relates to

the function (+20V, 20 MHz IN, etc.) Transistor emitters, diode cathodes, the positive lead of electrolytic capacitors, and pin 1 of integrated circuits are indicated by some special symbol such as E, a diode symbol, +, a teardrop shape or square circuit pad.

8-16. Service Sheet Aids. Signal levels, dc voltages, and logic states are shown as an aid in troubleshooting on the schematic diagrams. Individual circuit areas are given descriptive names to identify functions and provide easy means for reference. Where needed, notes are used to explain circuits or mechanical configurations not easily shown on the schematic.

8-17. The locations of individual components mounted on printed circuit boards are shown on the pictorial representation of the circuit boards of the related service sheet. Chassis mounted parts, major assemblies, and adjustment locations are found on the last foldout in this manual.

8-18. Figure 8-1, Schematic Diagram Notes, provides information relative to symbols shown on the schematic diagrams.

8-19. Service Kit and Extender Boards. The HP 11672A Service Kit contains interconnect cables, RF cables, various coaxial adaptors, and an adjustment tool, all of which are useful in servicing the Frequency Extension Module. Refer to the HP 11672A Operating Note and the 8660-series mainframe manual for a listing and pictorial representation of the contents.

8-20. Circuit board extenders are provided with the mainframe. These extender boards enable the technician to extend plug-in boards clear of the assembly to provide easy access to components and test points.

8-21. RECOMMENDED TEST EQUIPMENT

8-22. Table 1-1 lists the test equipment and accessories recommended for use in servicing the instrument. If any of the recommended test equipment is unavailable, instruments with equivalent specifications may be used.

8-23. REPAIR

8-24. Non-Repairable Assemblies

8-25. Repairs should not be attempted on the following assemblies if any is found to be defective during troubleshooting:

A1A3	4 GHz Low Pass Filter Assy
A1U1	Sampler/1.8 GHz Low Pass Filter Assy
A1U2	VCO/Mixer Assy
A1U3	4.43 GHz Oscillator/Mixer Assembly
A8	50 MHz Filter Assy
A9	20 MHz Filter Assy
A10	YIG Oscillator Assy
A11	2.6 — 4.1 GHz Bandpass Filter Assy
A12	4.43 GHz Bandpass Filter Assy

8-26. Removal and Disassembly Procedures

8-27. The procedures for removing the Frequency Extension Module from the mainframe, removing the cover, and gaining access to internal assemblies are found on the left hand foldout page which faces the last foldout in this manual.

8-28. The machine screws used throughout the Frequency Extension Module have a Pozidriv head. Pozidriv is very similar in appearance to the Phillips head, but using a Phillips screwdriver may damage the Pozidriv screw head. A Pozidriv screwdriver is recommended.

Resistance in ohms, capacitance in microfarads, inductance in microhenries unless otherwise noted.

Asterisk denotes a factory-selected value. Value shown is typical. Part might be omitted. See Table 5-1.

Tool-aided adjustment. ○ Manual control.

Encloses front-panel designation.

Encloses rear-panel designation.

Circuit assembly borderline.

Other assembly borderline. Also used to indicate mechanical interconnection (ganging) and RF shielding.

Heavy line with arrows indicates path and direction of main signal.

Heavy dashed line with arrows indicates path and direction of main feedback.



Wiper moves toward CW with clockwise rotation of control (as viewed from shaft or knob).



Numbered Test point. Measurement aid (metal post, circuit pad, etc.) provided.



Lettered Test point. No measurement aid provided.

Encloses wire color code. Code used is the same as the resistor color code. First number identifies the base color, second number identifies the wider stripe, third number identifies the narrower stripe. E.G., (947) denotes white base, yellow wide stripe, violet narrow stripe.

Stabistor

Coaxial or shielded cable.

Stripline (i.e., RF transmission line above ground).

Figure 8-1. Schematic Diagram Notes (1 of 2)

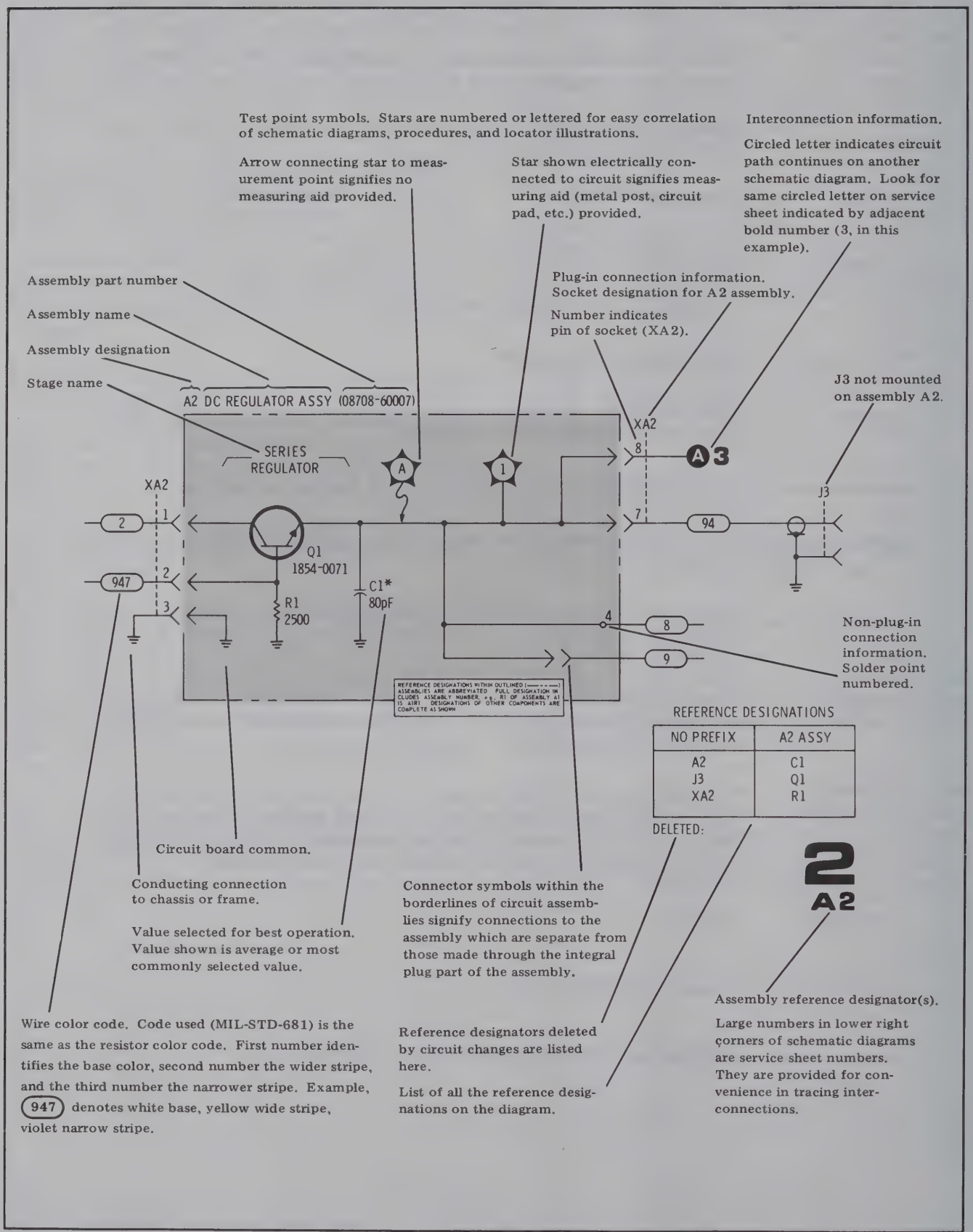


Figure 8-1. Schematic Diagram Notes (2 of 2)

SERVICE SHEET 1

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, this Service Sheet contains troubleshooting procedures for isolating the problem to a circuit board or module.

FREQUENCY EXTENSION MODULE

TROUBLESHOOTING TESTS

Malfunctions in the Frequency Extension Module generally fall into one of the following three catagories as observed at the RF Section front panel output: no output; an unwanted FM or sluggish frequency change; wrong output frequency. The tests on this service sheet are designed for a logical sequence of tests to determine the part or parts that need replacement. It is therefore recommended that the tests be performed in the order given. Refer to the Simplified Block Diagram for the functional relationship of the Frequency Extension Module circuits.

a. No RF Output. After verifying that the power supply voltages are correct, troubleshooting begins by verifying that both RF outputs are present. If the Sum Loop voltage controlled oscillator (VCO), the YIG oscillator, or the 4.43 GHz local oscillator are not working, there will be no output at the RF Section output port. This is checked in Tests 2 and 3.

b. Unwanted FM or sluggish change of frequency. If the YIG Loop search circuits do not turn off, the output will include an FM sweep of about 40 MHz at one kilohertz rate. If the search circuit is inoperative, a change of frequency will appear sluggish. The search circuit is checked in Test 7.

c. Wrong Frequency. If only certain frequencies are wrong the fault is probably in one of the pretune sections. If these are among the 100 MHz steps (100 to 1200 MHz), the fault is probably in the YIG Loop Pretune Section, for smaller steps the fault is probably in the Sum Loop Pretune Section. Frequency problems are checked in tests 5 through 12 for the YIG Loop and tests 13 through 16 for the Sum Loop.

TROUBLESHOOTING BLOCK DIAGRAM

The troubleshooting block diagram on this Service Sheet shows the relationship between all printed circuit board assemblies and all modules. Use the block diagram and troubleshooting procedures following the principles of operation to isolate a trouble to a specific assembly. Then turn to the Service Sheet for that assembly and isolate the trouble to a specific component.

The large numbers in the lower right corner of each of the major blocks identify the Service Sheet which provides schematics and principles of operation for that particular assembly.

PRINCIPLES OF OPERATION

General

The Hewlett-Packard Model 11661B Frequency Extension Module (with a suitable RF Section) increases the frequency range of the Model 8660 mainframe above 160 MHz while maintaining 1 Hz frequency resolution. Four input signals from the mainframe are used to produce two output signals for the RF Section. These output signals are up-converted in frequency to ensure low spurious sidebands in the final output but still contain all frequency information selected in the mainframe.

A SUM phase lock loop combines 1 Hz step information, 10 MHz step information (from mainframe RF reference signal and BCD coded logic), and the 4.43 GHz free-running internal local oscillator. A YIG phase lock loop combines a 100 MHz reference, 100 MHz steps from BCD coded logic, a 20 MHz reference (frequency modulated if FM is present), and the 4.43 GHz internal local oscillator frequency. Note that both output signals contain the 4.43 GHz oscillator frequency. This frequency component (including any drift) is cancelled in the RF Section mixer.

Power supply and RF interconnections between the Frequency Extension Module and the mainframe pass through the RF Section. The RF

Section also contains a 20 MHz amplifier for the 20 MHz FM/CW reference signal. Digit 8, 9, and 10 BCD logic input lines do not pass through the RF Section but connect directly to the mainframe.

4.43 GHz Oscillator

The 4.43 GHz oscillator circuit is divided between the A1U3 and the A1A4 assemblies. The oscillator itself is located on the A1U3 module and receives two inputs from the A1A4 Oscillator Tuning Assembly: —10 Vdc filtered and an adjustable supply source derived from +20 Vdc whose value is adjusted by A14R2 to control the frequency of the oscillator. This oscillator is not phase locked as the 4.43 GHz frequency drift is cancelled out in the RF Section mixer.

Sum Loop

The Sum Loop inputs from the mainframe include 30 to 20 MHz (1 Hz steps), 450 to 360 MHz (10 MHz steps), and Digit 8 BCD code logic. The Digit 8 input logic to the A4 Sum Loop Pretune Assembly is converted to an analog voltage and then combined with a phase error signal to tune the Sum Loop VCO (voltage controlled oscillator). The 450 to 360 MHz is mixed in the A8 module with the Sum Loop 1st IF to produce a 30 to 20 MHz Sum Loop 2nd IF. This signal goes to the A3 Sum Loop Phase Detector Assembly where it is compared with the 30 to 20 MHz signal from the mainframe as part of the phase lock loop. The Sum Loop therefore contains all frequencies up to and including the first eight digits (0 to 99.999 999 MHz in 1 Hz steps).

YIG Loop

The YIG Loop inputs from the mainframe include 20 MHz FM/CW reference, 100 MHz reference, and Digits 9 and 10 BCD code logic. The Digits 9 and 10 input logic to the A5 YIG Loop Pretune Assembly is converted to an analog current and used to drive the coarse tuning coil of the YIG oscillator. Part of the YIG output is fed back to the first mixer to produce YIG Loop 1st IF. The difference frequency between the 4.43 GHz oscillator and the YIG oscillator will be in the range of 480 MHz to 1680 MHz in 100 MHz steps. The step recovery diode on the A1U1 assembly generates harmonics of the 100 MHz reference input. The difference between one of these harmonics and the 1st IF will be 20 MHz which is the 2nd IF. For example, if the 1st IF is 680 MHz, the 7th harmonic of 100 MHz will produce the 20 MHz 2nd IF. This 20 MHz 2nd IF is locked to the 20 MHz FM/CW reference from the mainframe in the YIG loop phase detector circuits. If phase locked, the phase difference produces a dc error for fine tuning the YIG oscillator. If not phase locked, logic circuit activates the search waveform generator in the FM Driver Assembly. The YIG loop output is frequency dependent on the 100 MHz reference harmonic, the 4.43 GHz oscillator, and the 20 MHz FM/CW reference.

TROUBLESHOOTING

It is assumed that a problem has been isolated to the Frequency Extension Module as a result of using the System Troubleshooting Guide found in Section VIII of the HP Model 8660-series mainframe Operating and Service Manual and the Systems Troubleshooting information preceeding Service Sheet 1 in the RF Section manual. Troubleshoot the Frequency Extension Module using the test equipment, information, and procedures which follow.

Test Equipment

Microwave Frequency Counter	HP 5340A
Spectrum Analyzer	HP 8555A/8552B/140T
Oscilloscope	HP 180C/1801A/1821A
10:1 Divider Probe	HP 10004
Digital Voltmeter	HP 34740A/34702A
Extender Cable	HP 11672-60002
Extender Board	HP 5060-0258

Test 1. First check the power supply inputs to the Frequency Extension Module by removing the A3 printed circuit assembly (green extractor) and replacing it with the extender board. Check the voltages as listed below; the tolerance is ±0.1 volt:

Power Supply Voltages at A3 Connector

Pin	C	E	F	H	J
Volts	Ground	+5.25	—40.0	—10.0	+20.0

Replace the A3 printed circuit assembly.

CAUTION

Always turn instrument power off before removing or installing any assembly.

Test 2. Turn the instrument off, remove the Frequency Extension Module from the mainframe, and reconnect using the extender cable.

Unplug the 20 MHz output cable (W4) from the A1J4. Connect an oscilloscope to test point A6TP1 and check for a +2.5 Vpk sawtooth ramp. Adjust A7R17 so the ramp just turns on. Reconnect the 20 MHz output cable. The signal at A6TP1 should now be 0.0 ± 0.1 Vdc.

Tune mainframe from 0 to 1200 MHz center frequency in 100 MHz steps. Verify that the signal at A6TP1 remains at 0.0 ± 0.1 Vdc. If the signals at A6TP1 are correct, check the YIG loop output frequencies at J2 as given in Table 8-1. If these frequencies are correct go to test 3 to continue testing, if not, go to test 5.

Test 3. If the YIG loop is operating at the correct output frequencies (Test 2), reconnect the counter to J1 and check the SUM loop output frequencies as follows: Tune mainframe from 0 to 90 MHz in 10 MHz steps and verify that the SUM loop output steps up from 3.95 GHz to 4.05 GHz matching the mainframe 10 MHz steps. If these frequencies are correct go to test 4 to continue testing, if not, go to test 13.

Test 4. If the results of tests 2 and 3 are good, use the spectrum analyzer to measure the power output as follows:

Power Outputs to RF Section

Output	Connector	Power Level
SUM LOOP	J1	⩾ −4 dBm
YIG LOOP	J2	⩾ +10 dBm

If the SUM loop power output is low go to Service Sheet 3 and troubleshoot the VCO circuit. If the YIG loop power output is low go to Service Sheet 5 and check the YIG oscillator output. If no problem has been encountered as a result of these tests, check the interconnecting cables to the RF Section as the Frequency Extension Module is working properly.

Test 5. Remove the A6 printed circuit assembly (red extractor). Tune the mainframe 0 to 1200 MHz center frequency as shown in Table 8-1 to check the YIG Pretune Driver circuits. Use the tolerance values for YIG Loop Unlocked.

If the YIG loop frequencies are within tolerance, proceed to test 6, if not check the pretune input logic levels as shown on the block diagram for A5 pins 1 through 5. If the logic levels are correct go to SS2, if incorrect check interconnections and signals from the mainframe.

Test 6. Refer to the last foldout of this section for the procedure to gain access to W1 at A2J5. Use the frequency counter to measure the mainframe 20 MHz FM/CW reference at Cable W1. Connect the mainframe 10 MHz reference output to the counter reference input. Also check the 100 MHz reference from the W3 cable at connector A1J2. If incorrect check interconnections and signals from the mainframe. If correct proceed to test 7.

Test 7. Reinstall the A6 printed circuit assembly (red extractor). Install a TEE between W1 and A2J5 and patch into A2J4 leaving W2 discon-

Table 8-1. YIG Loop Output Frequency Versus Mainframe Tuning

Center Frequency	YIG Loop Output Frequency	YIG Loop Pretune Tolerance	YIG Loop Locked Tolerance
0000 MHz	3.950 GHz	±5 MHz	±1.5 MHz
0100 MHz	3.850 GHz	±5 MHz	±1.5 MHz
0200 MHz	3.750 GHz	±5 MHz	±1.5 MHz
0300 MHz	3.650 GHz	±5 MHz	±1.5 MHz
0400 MHz	3.550 GHz	±5 MHz	±1.5 MHz
0500 MHz	3.450 GHz	±5 MHz	±1.5 MHz
0600 MHz	3.350 GHz	±5 MHz	±1.5 MHz
0700 MHz	3.250 GHz	±5 MHz	±1.5 MHz
0800 MHz	3.150 GHz	±5 MHz	±1.5 MHz
0900 MHz	3.050 GHz	±5 MHz	±1.5 MHz
1000 MHz	2.950 GHz	±5 MHz	±1.5 MHz
1100 MHz	2.850 GHz	±5 MHz	±1.5 MHz
1200 MHz	2.750 GHz	±5 MHz	±1.5 MHz

nected. This connects the 20 MHz FM/CW reference into both sides of the phase detector for testing. Use a digital voltmeter to check A6TP1 for 0.0 ± 0.1 Vdc. If incorrect proceed to test 8, if correct proceed to test 10.

Test 8. Check the dc voltage at A7 pin J (search control output) for 0 Vdc (logic low). If correct, proceed to Service Sheet 5; if incorrect, got to Service Sheet 4 and troubleshoot the A7 Assembly.

Test 9. Move the digital voltmeter probe to A7TP2 which should also give a reading of 0.0 ± 0.1 Vdc. If this voltage is correct proceed to Service Sheet 5 and troubleshoot the A6 Assembly, if incorrect, proceed to Service Sheet 4 and troubleshoot the A7 Assembly.

Test 10. Connect the spectrum analyzer to A1J4 and measure the amplitude of the signal at 20 MHz. If the signal is less than −17 dBm, adjust A1A1C1 to peak signal. If the signal is equal or greater than −17 dBm proceed to test 12, if not go to test 11.

Test 11. Set the mainframe center frequency to 500 MHz. Disconnect cable A11W3 at A1J1 and connect the cable to the spectrum analyzer. Check the signal at 3.45 GHz for an amplitude of −5 dBm or greater. Also check the high and low band edges for frequency and levels shown on the block diagram. If these signals are correct proceed to Service Sheet 3 and troubleshoot the A1 Assembly. If the signal level is incorrect, check the A10 output level at 3.45 GHz. If the level is +10 dBm or greater replace A11; if less proceed to Service Sheet 5 and troubleshoot the A10 Assembly.

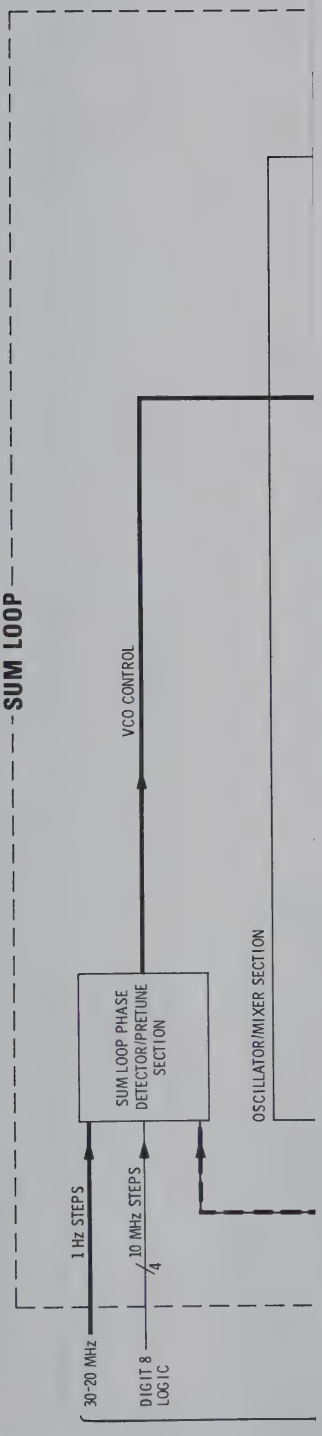
Test 12. Connect the spectrum analyzer to the end of cable W2 where it connects to A2J4 and measure the 20 MHz output of the A9 Assembly. If the level is −6 dBm or more check for intermittant or poor connections in the 2nd IF line. If the signal level is less than −6 dBm replace the A9 assembly.

Test 13. If the frequencies measured in Test 3 were incorrect, check the digital pretune logic levels at A4 pins K, L, M, and N. If correct, disconnect W8 from A2J2 and connect the cable to the spectrum analyzer and check for the 20 to 30 MHz signal level of −5 to −8 dBm. Connect the spectrum analyzer to W6 by disconnecting the cable at A8 J2 and check for an input level of from +13 to +15 dBm. If any measurement in this test is incorrect, trace the line back through the RF Section to the mainframe for continuity.

Test 14. Install a TEE between W8 and A2J2 and patch into A2J1. This connects the 20 to 30 MHz input from the mainframe to both sides of the SUM Loop phase detector for testing. Use a digital voltmeter to check A3TP1 for +12 ±3 Vdc. If this is out of range, proceed to Service Sheet 6 and troubleshoot the A3 Assembly. If the voltage is correct, remove the TEE and reconnect the cables and continue with test 15.

Test 15. Connect the spectrum analyzer to A1J6. If the 480-380 MHz signal has an amplitude of −6 dBm or more, replace the A8 Assembly. If the signal is incorrect go to test 16.

Test 16. Use an extender board to gain access to the edge connector of the A4 Sum Loop Pretune Assembly. Connect a digital voltmeter to pin 1 of the extender board and measure the dc voltage while tuning the mainframe from 0 to 90 MHz in 10 MHz steps. The voltage should change from −10 Vdc to −26 Vdc as the frequency is stepped. If voltages are correct proceed to Service Sheet 2 and troubleshoot A1U2 and A1A2. If they are incorrect go to Service Sheet 7 and troubleshoot the A4 Sum Loop Pretune Assembly.



SERVICE SHEET 2

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

A5 YIG PRETUNE DRIVER ASSEMBLY

PRINCIPLES OF OPERATION

The A5 YIG Pretune Driver Assembly converts the binary logic of the 0000 MHz, 0100 MHz . . . 1200 MHz (digits 9 and 10) to a dc current to pretune the YIG oscillator. The assembly includes a digital to analog converter, current driver, and reference voltages for the converter.

Digital/Analog Converter

Five potentiometers are provided to adjust each logic line for binary weighted current necessary to correctly tune the YIG pretune circuit.

For example if 1 GHz is entered on the mainframe, a logic high will be present on XA5 pin 5 of the YIG Pretune Driver Assembly. The output of A5U1C pin 8 is at a logic low turning off A5Q5 which depletes current from the node at A5TP2.

Current Summing Node (A5TP2)

The sum of the current from the D/A converter and the feedback from the Current Sense Resistor is constant at summing node A5TP2. The magnitude of this sum is set by Offset Adjustment A5R29. The higher the frequency entered on the mainframe, the higher the D/A current into the node and therefore the lower the feedback current.

Current Driver

A5Q6, A5U2, and A5Q7 form an operational amplifier circuit. The non-inverting input at A5Q6 pin 6 is grounded and inverting input pin 2 connects to the current node. A5U2 provides high

Troubleshooting Block Diagram
◀ SERVICE SHEET 1

nected. This connects the 20 MHz FM/CW reference into both sides of the phase detector for testing. Use a digital voltmeter to check A6TP1 for 0.0 ± 0.1 Vdc. If incorrect proceed to test 8, if correct proceed to test 10.

Test 8. Check the dc voltage at A7 pin J (search control output) for 0 Vdc (logic low). If correct, proceed to Service Sheet 5; if incorrect, got to Service Sheet 4 and troubleshoot the A7 Assembly.

Test 9. Move the digital voltmeter probe to A7TP2 which should also give a reading of 0.0 ± 0.1 Vdc. If this voltage is correct proceed to Service Sheet 5 and troubleshoot the A6 Assembly, if incorrect, proceed to Service Sheet 4 and troubleshoot the A7 Assembly.

Test 10. Connect the spectrum analyzer to A1J4 and measure the amplitude of the signal at 20 MHz. If the signal is less than —17 dBm, adjust A1A1C1 to peak signal. If the signal is equal or greater than —17 dBm proceed to test 12, if not go to test 11.

Test 11. Set the mainframe center frequency to 500 MHz. Disconnect cable A11W3 at A1J1 and connect the cable to the spectrum analyzer. Check the signal at 3.45 GHz for an amplitude of —5 dBm or greater. Also check the high and low band edges for frequency and levels shown on the block diagram. If these signals are correct proceed to Service Sheet 3 and troubleshoot the A1 Assembly. If the signal level is incorrect, check the A10 output level at 3.45 GHz. If the level is +10 dBm or greater replace A11; if less proceed to Service Sheet 5 and troubleshoot the A10 Assembly.

Test 12. Connect the spectrum analyzer to the end of cable W2 where it connects to A2J4 and measure the 20 MHz output of the A9 Assembly. If the level is —6 dBm or more check for intermittant or poor connections in the 2nd IF line. If the signal level is less than —6 dBm replace the A9 assembly.

Test 13. If the frequencies measured in Test 3 were incorrect, check the digital pretune logic levels at A4 pins K, L, M, and N. If correct, disconnect W8 from A2J2 and connect the cable to the spectrum analyzer and check for the 20 to 30 MHz signal level of —5 to —8 dBm. Connect the spectrum analyzer to W6 by disconnecting the cable at A8 J2 and check for an input level of from +13 to +15 dBm. If any measurement in this test is incorrect, trace the line back through the RF Section to the mainframe for continuity.

Test 14. Install a TEE between W8 and A2J2 and patch into A2J1. This connects the 20 to 30 MHz input from the mainframe to both sides of the SUM Loop phase detector for testing. Use a digital voltmeter to check A3TP1 for +12 ±3 Vdc. If this is out of range, proceed to Service Sheet 6 and troubleshoot the A3 Assembly. If the voltage is correct, remove the TEE and reconnect the cables and continue with test 15.

Test 15. Connect the spectrum analyzer to A1J6. If the 480-380 MHz signal has an amplitude of —6 dBm or more, replace the A8 Assembly. If the signal is incorrect go to test 16.

Test 16. Use an extender board to gain access to the edge connector of the A4 Sum Loop Pretune Assembly. Connect a digital voltmeter to pin 1 of the extender board and measure the dc voltage while tuning the mainframe from 0 to 90 MHz in 10 MHz steps. The voltage should change from —10 Vdc to —26 Vdc as the frequency is stepped. If voltages are correct proceed to Service Sheet 2 and troubleshoot A1U2 and A1A2. If they are incorrect go to Service Sheet 7 and troubleshoot the A4 Sum Loop Pretune Assembly.

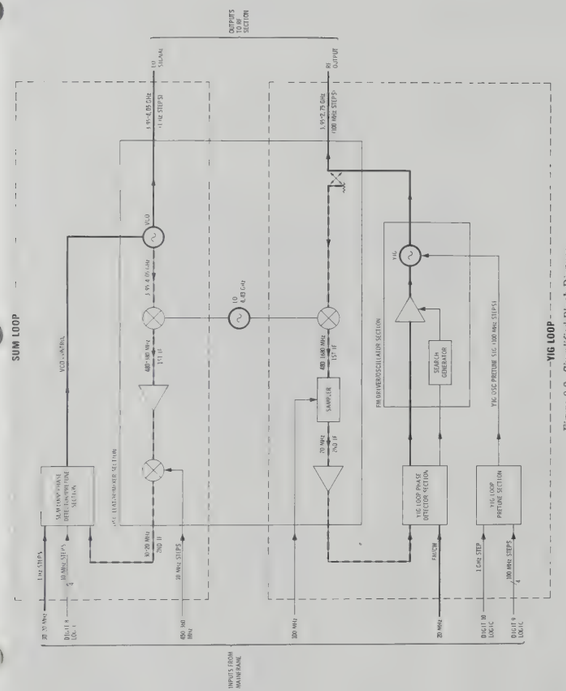


Figure 8-2. Simplified Block Diagram

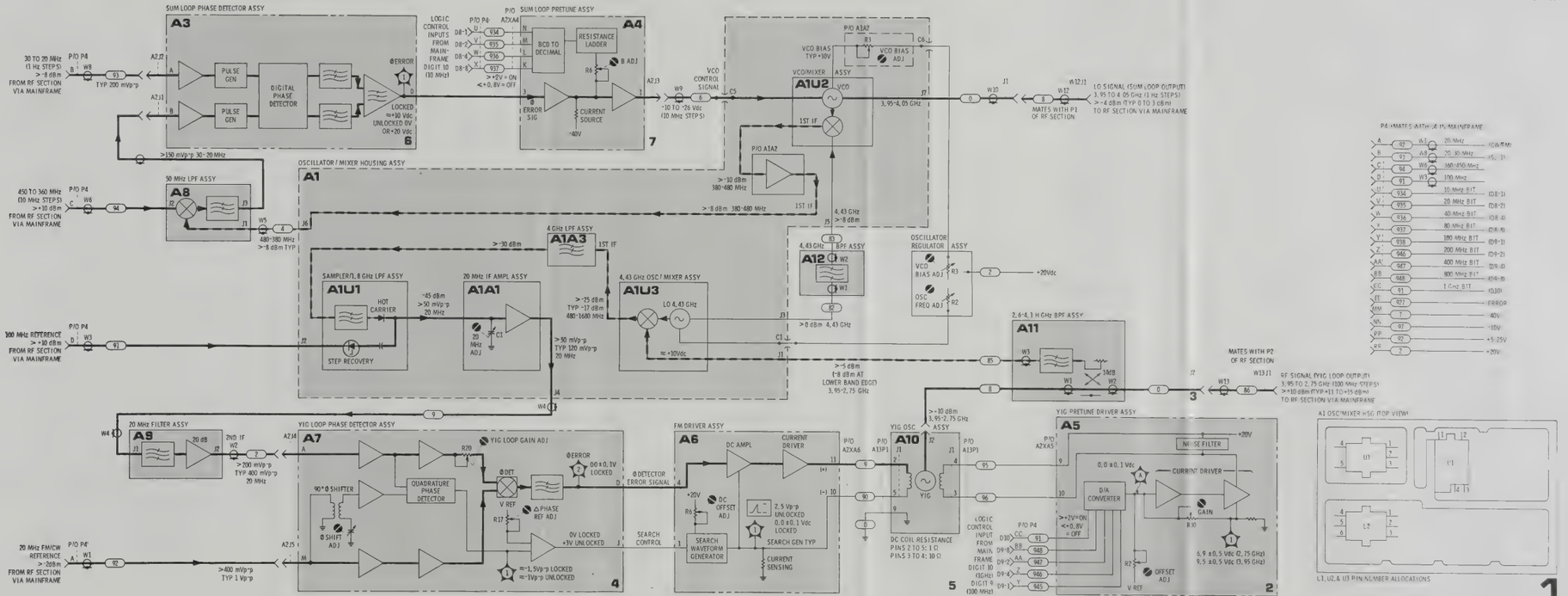


Figure 8-3. Troubleshooting Block Diagram

SERVICE SHEET 2 (Cont'd)

open loop gain and source follower A5Q7 ensures little loading of the integrated circuit by the output amplifier.

Output Amplifier

Parallel transistors A5Q8 and A5Q9 drive the YIG main tuning coil. A5C6, A5C7 and A5R42 prevent noise from reaching the YIG coil. VR4, CR2, and A5C8 suppress switching transients from the YIG coil, preventing them from reaching the current driver amplifiers or power supplies. Resistors A5R43 and A5R45 sense the current through the YIG coil and provide the source for the current feedback.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the YIG Pretune Driver Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Spectrum Analyzer	HP 8555A/8552B/140T
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A5 Assembly (+20V, —10V, and —40V). Also check the +9 VF, +5V, and the anode of A5CR3 (—9 Vdc). If correct, proceed to Test 2. Otherwise check for continuity of interconnections to mainframe or an A5 Assembly defect.

Test 2. If only one of the stepping codes gives improper tuning to the YIG oscillator, the problem is probably in one of the input inverter-transistor circuits. Enter the frequency indicated on the input line and check the output of the inverter for a logic low. The associated transistor should be conducting (collector-emitter = about 0.2 Vdc). Note that in this application the transistor collector acts as an emitter.

Test 3. If all steps give improper tuning, check the current driver section of the board. The collectors of A5Q6 and A5Q7 should be about +5 Vdc. At 0 GHz A5TP1 should be about 9.48 Vdc; A5Q8 and A5Q9 should be about +10.2 Vdc on their bases; A5Q7 should be about +11 Vdc at the gate and about +20 Vdc at the drain. The most likely components in this circuit to fail are operational amplifier A5U2 or FET A5Q7.

SERVICE SHEET 3 (Cont'd)

A1U3 4.43 GHz Oscillator/Mixer Assembly

This unit is a non-repairable microcircuit containing the 4.43 GHz oscillator and the YIG Loop 1st IF Mixer. An output of this oscillator is also used to drive SUM Loop 1st IF mixer which is part of the A1U2 microcircuit.

A1A3 4 GHz Low Pass Filter Assembly

This unit attenuates the level of the 4.43 GHz contained in the ouput of the YIG Loop 1st IF Mixer. It is a non-repairable assembly.

A1U1 Sampler/1.8 GHz Low Pass Filter Assembly

The output of the 4 GHz Low Pass Filter Assembly next passes through the 1.8 GHz Low Pass Filter in the A1U1 microcircuit. This attenuates the level of the 3.95 to 2.75 GHz decoupler RF contained in the YIG Loop 1st IF. A sampler generates the YIG Loop 2nd IF. It may be visualized as a harmonic mixer, in which a step recovery diode generates harmonics of the mainframe 100 MHz reference signal. This is combined with the 1st IF to produce many frequency products including the 20 MHz 2nd IF frequency.

A1A1 20 MHz IF Amplifier Assembly

This assembly is a printed circuit board which serves as an interface for the A1U1 microcircuit and the YIG Loop 20 MHz second IF amplifier. Adjustable capacitor A1A1C1 with inductor A1A1L2 provides a 20 MHz parallel resonant filter circuit. In spite of this, the output of the 20 MHz IF amplifier contains considerable 100 MHz feedthrough. Therefore the amplitude of the 20 MHz signal should be determined using a spectrum analyzer.

A1U2 VCO/Mixer Assembly

This unit is a non-repairable microcircuit containing the 3.950/4.050 GHz VCO and the Sum Loop 1st IF mixer. The output of the VCO is the Sum loop output to the RF Section (in the RF Section it serves as the local oscillator signal). The mixer combines the VCO signal with the 4.43 GHz signal from the A1U3 microcircuit to form the Sum Loop 1st IF.

A1A2 380-480 MHz IF Amplifier Assembly

This assembly is a printed circuit board which serves an interface for the A1U2 microcircuit and the Sum Loop 1st IF amplifier. A1A2R3 provides adjustment for the A1U2 VCO bias.

A12 4.43 GHz Bandpass Filter Assembly

This filter is a non-repairable assembly used to couple the 4.43 GHz oscillator output to the Sum Loop 1st IF mixer. This filter reduces spurious outputs between the two units.

Figure 8-

A5 Yig Loop Pretune Assembly
SERVICE SHEET 2

SERVICE SHEET 2 (Cont'd)

open loop gain and source follower A5Q7 ensures little loading of the integrated circuit by the output amplifier.

Output Amplifier

Parallel transistors A5Q8 and A5Q9 drive the YIG main tuning coil. A5C6, A5C7 and A5R42 prevent noise from reaching the YIG coil. VR4, CR2, and A5C8 suppress switching transients from the YIG coil, preventing them from reaching the current driver amplifiers or power supplies. Resistors A5R43 and A5R45 sense the current through the YIG coil and provide the source for the current feedback.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the YIG Pretune Driver Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Spectrum Analyzer	HP 8555A/8552B/140T
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A5 Assembly (+20V, —10V, and —40V). Also check the +9 VF, +5V, and the anode of A5CR3 (—9 Vdc). If correct, proceed to Test 2. Otherwise check for continuity of interconnections to mainframe or an A5 Assembly defect.

Test 2. If only one of the stepping codes gives improper tuning to the YIG oscillator, the problem is probably in one of the input inverter-transistor circuits. Enter the frequency indicated on the input line and check the output of the inverter for a logic low. The associated transistor should be conducting (collector-emitter = about 0.2 Vdc). Note that in this application the transistor collector acts as an emitter.

Test 3. If all steps give improper tuning, check the current driver section of the board. The collectors of A5Q6 and A5Q7 should be about +5 Vdc. At 0 GHz A5TP1 should be about 9.48 Vdc; A5Q8 and A5Q9 should be about +10.2 Vdc on their bases; A5Q7 should be about +11 Vdc at the gate and about +20 Vdc at the drain. The most likely components in this circuit to fail are operational amplifier A5U2 or FET A5Q7.

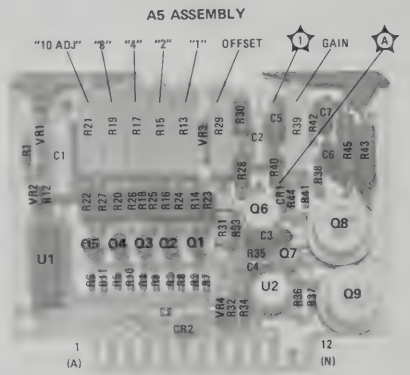


Figure 8-4. A5 Yig Loop Pretune Assembly Component and Test Point Locations

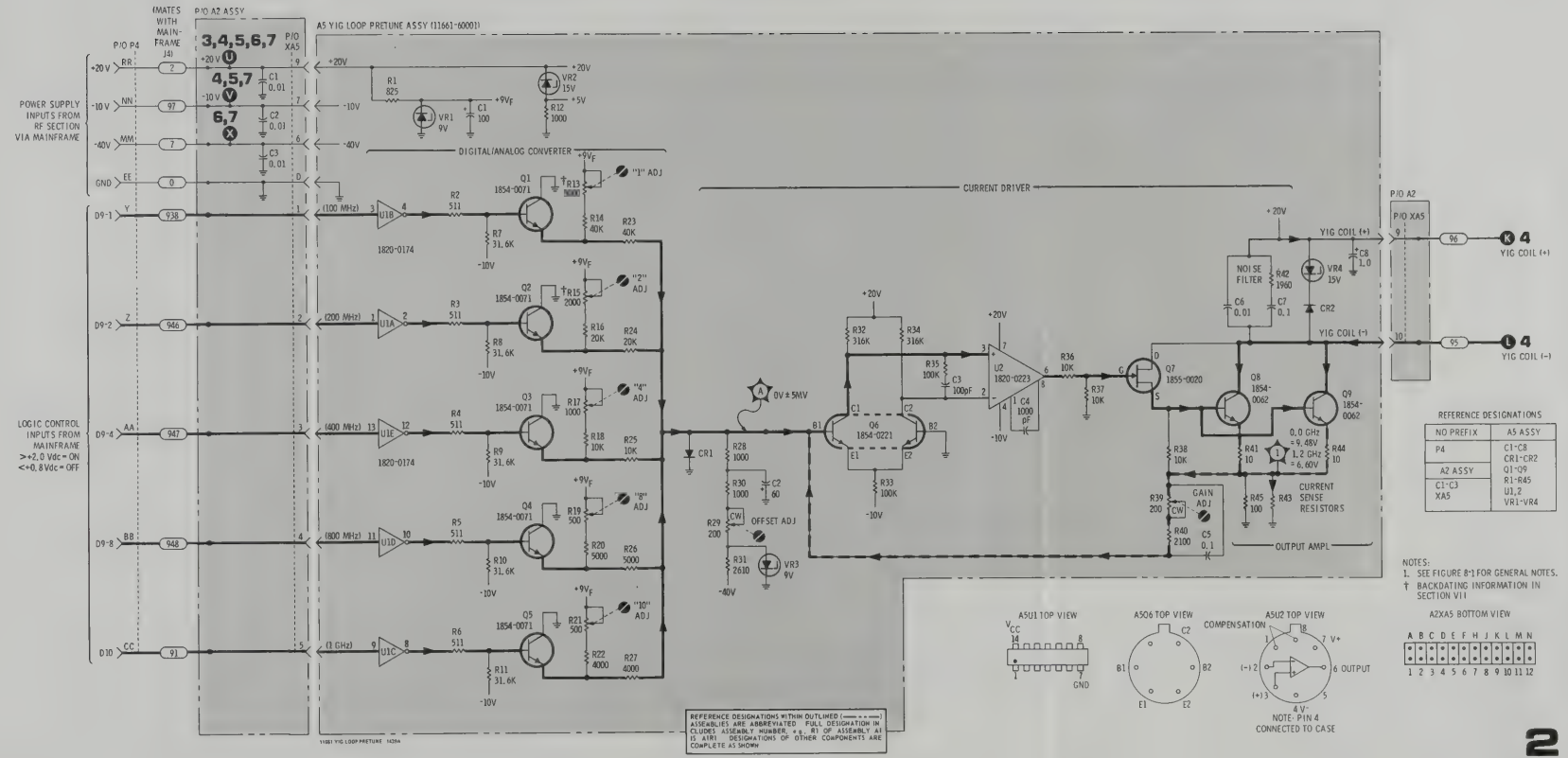


Figure 8-5. Yig Loop Pretune Section Schematic Diagram

SERVICE SHEET 3 (Cont'd)

A8 50 MHz Filter Assembly

This is also a non-repairable assembly and uses the Sum Loop 1st IF and the 450 to 360 MHz input from the mainframe to produce the Sum Loop 2nd IF. The 2nd IF will be in the range of 30 to 20 MHz and contains the 1 Hz step information.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the circuits using the test equipment and procedures given below.

Test Equipment	Model
Spectrum Analyzer	HP 8555A/8552B/140T
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to assemblies where a defect has been indicated.

Test 2. If a problem is indicated in A1U2 SUM loop VCO (Service Sheet 1) check the inputs and outputs as shown on the schematic diagram including the dc voltages. If all inputs are correct and either output incorrect replace the unit.

Test 3. If the A1U2 SUM loop VCO is operating properly but a problem exists in the 1st or 2nd IF, first check the associated dc voltage levels and then the signal levels as indicated on the schematic diagram.

Test 4. If the A1U3 4.43 GHz oscillator has no output or cannot be properly adjusted, check the dc voltage inputs to the microcircuit. If there is no tuning voltage at pin 2 of A1U3, use a voltmeter to trace the circuit back to R1. Note that R1 is located on the Frequency Extension Module housing.

Test 5. Sampler/1.8 GHz Low Pass Filter Assembly A1U1 is also a non-repairable assembly. If the inputs are correct and no or low output, the unit must be replaced. The output at pin 4 of A1U1 should be greater than 50 millivolts peak-to-peak.

Test 6. If the signal into the A1A1 assembly is correct but the output at A1J4 is incorrect, use an oscilloscope to trace the signal through the amplifier. The output at A1J4 may have considerable 100 MHz signal present which is normal and should not cause a problem.

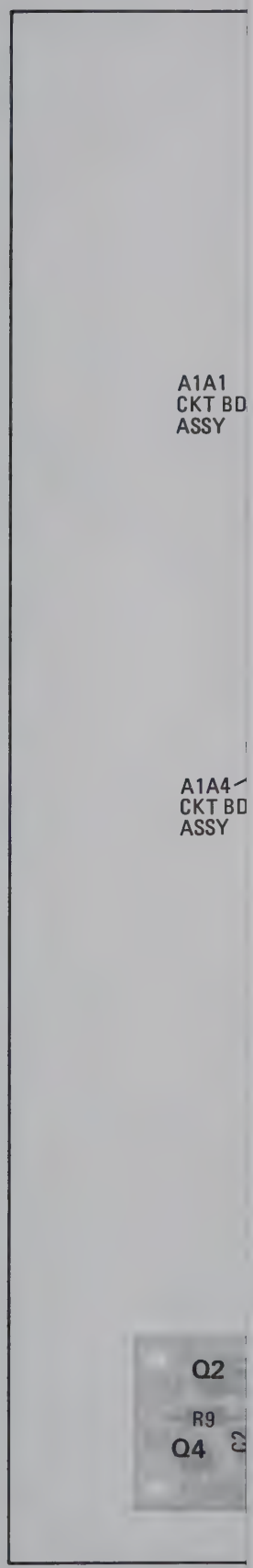


Figure 8-6.

SERVICE SHEET 4

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

YIG LOOP PHASE DETECTOR SECTION

This Service Sheet includes the YIG loop 2nd IF filter and two phase detectors. A quadrature phase detector supplies a search control output when loss of phase lock occurs. Another phase detector supplies a dc voltage (A7TP2) proportional to the phase error between the 20 MHz 2nd IF and the 20 MHz FM/CW reference signal.

A9 20 MHz Filter Assembly

This is a non-repairable assembly whose purpose is to remove the unwanted 100 MHz and other spurious signals on the YIG Loop 2nd IF signal. The 2nd IF signal is first processed by a 50 MHz low pass filter, then a 20 MHz bandpass filter, and then amplified by about 20 dB. The output of this assembly is an emitter follower which drives one input of the YIG Loop phase detector assembly.

A7 YIG Loop Phase Detector Assembly

The YIG Loop phase detector compares the 20 MHz 2nd IF with the 20 MHz reference input from the mainframe. The output of this assembly includes a dc error signal during phase lock and a search control command during an unlocked condition.

90° Phase Shifter. The input circuit to A7Q1 shifts the phase of the 20 MHz reference signal about 90°. Capacitor A7C2 is used to adjust the exact phase so that the search command will not be turned on when the YIG loop is phase locked.

20 MHz Limiter/Amplifiers. Three integrated circuits are used to amplify and limit the 20 MHz signals: one for the 20 MHz reference, one for the 90° phase shifted 20 MHz reference, and one 20 MHz 2nd IF.

A1 Oscillator/Mixer Housing Assembly
A8 50 MHz Filter Assembly
A11 2.6–4.1 GHz Bandpass Filter Assembly
A12 4.43 GHz Bandpass Filter Assembly
A14 Oscillator Regulator Assembly

◀ SERVICE SHEET 3

SERVICE SHEET 4

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

YIG LOOP PHASE DETECTOR SECTION

This Service Sheet includes the YIG loop 2nd IF filter and two phase detectors. A quadrature phase detector supplies a search control output when loss of phase lock occurs. Another phase detector supplies a dc voltage (A7TP2) proportional to the phase error between the 20 MHz 2nd IF and the 20 MHz FM/CW reference signal.

A9 20 MHz Filter Assembly

This is a non-repairable assembly whose purpose is to remove the unwanted 100 MHz and other spurious signals on the YIG Loop 2nd IF signal. The 2nd IF signal is first processed by a 50 MHz low pass filter, then a 20 MHz bandpass filter, and then amplified by about 20 dB. The output of this assembly is an emitter follower which drives one input of the YIG Loop phase detector assembly.

A7 YIG Loop Phase Detector Assembly

The YIG Loop phase detector compares the 20 MHz 2nd IF with the 20 MHz reference input from the mainframe. The output of this assembly includes a dc error signal during phase lock and a search control command during an unlocked condition.

90° Phase Shifter. The input circuit to A7Q1 shifts the phase of the 20 MHz reference signal about 90°. Capacitor A7C2 is used to adjust the exact phase so that the search command will not be turned on when the YIG loop is phase locked.

20 MHz Limiter/Amplifiers. Three integrated circuits are used to amplify and limit the 20 MHz signals: one for the 20 MHz reference, one for the 90° phase shifted 20 MHz reference, and one 20 MHz 2nd IF.

A1 Oscillator/Mixer Housing Assembly
A8 50 MHz Filter Assembly
A11 2.6–4.1 GHz Bandpass Filter Assembly
A12 4.43 GHz Bandpass Filter Assembly
A14 Oscillator Regulator Assembly
◀ SERVICE SHEET 3

SERVICE SHEET 4 (Cont'd)

Quadrature Phase Detector. The quadrature phase detector circuit compares the 20 MHz 2nd IF with the 90° phase shifted 20 MHz reference to detect an unlocked condition. Two gates on A7U2 form an exclusive OR gate where the output is low only when the inputs are out of phase.

20 MHz Phase Detector. Phase Detector A7U4 is a balanced mixer type detector which compares the 20 MHz 2nd IF with the 20 MHz reference. The output of the detector passes through a low pass filter to produce a dc voltage proportional to the phase difference.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assembly or cables shown in the accompanying diagram. Troubleshoot the A7 YIG Loop Phase Detector Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Oscilloscope	HP 180C/1801A/1821A
10:1 Divider Probe	HP 10004
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A7 Assembly (+20 Vdc and —10 Vdc). Also check the +10 Vdc from the zener diode A7VR1 and the regulated —5 Vdc. If incorrect troubleshoot these circuits. If correct proceed to Test 2.

Test 2. If there is either no or low output at J2 of the A9 Assembly, check the input, cables, and —10 Vdc power supply input at A9C1. If no fault is found, replace the A9 Assembly.

Test 3. If the search control output was found faulty on Service Sheet 1, proceed as follows: With both RF inputs of the A7 Assembly driven from the 20 MHz reference, check pin 2 of A7U3 for about —1.2 Vdc. Pin 3 of A7U3 for A7TP1) should be about —1.5 Vdc. (ECL logic levels are; on = —0.7 Vdc, off = —1.5 Vdc). If these inputs are correct A7U3 or A7VR3 is bad. If the voltage at A7TP1 is incorrect use an oscilloscope to check back to the RF inputs. The output of the Limiter Amplifiers (U1A pins 7 and 6, U1B pins 3 and 2) should be about 0.8 Vp-p.

Test 4. If the search control is working properly, compare the REF 20 MHz LIMITER/AMPL voltages and waveforms with the PHASE-SHIFTED 20 MHz LIMITER/AMPL voltages and waveforms. If one of the 20 MHz inputs is unplugged, the loop will be unlocked and this can be used for signal tracing through the phase detector and elliptic filter.

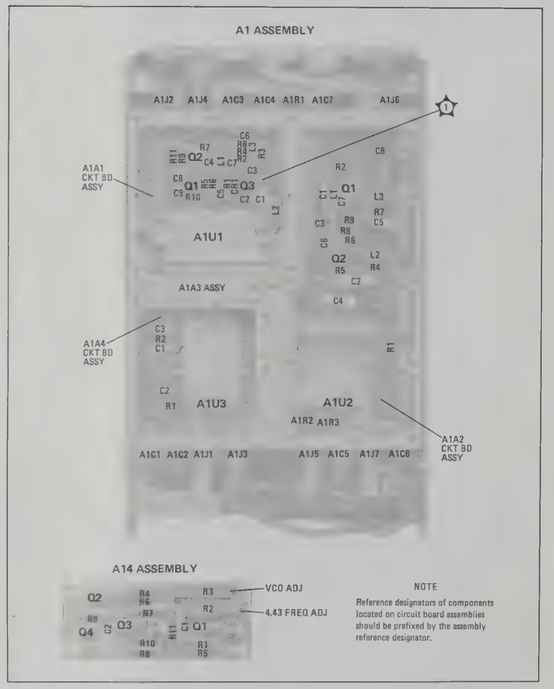
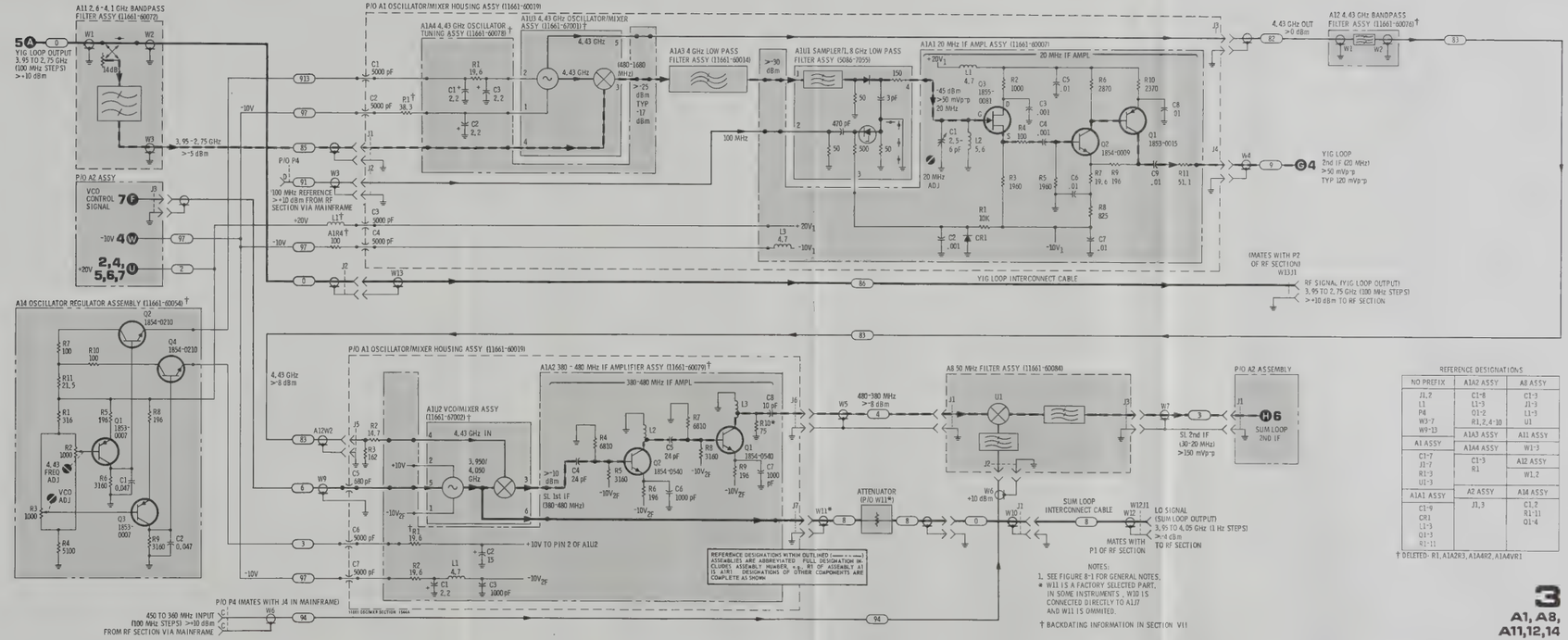


Figure 8-6. A1 Oscillator/Mixer Housing Assembly Component and Test Point Locations



3
A1, A8, A11,12,14

Figure 8-7. Oscillator/Mixer Section Schematic Diagram

SERVICE SHEET 4 (Cont'd)

Quadrature Phase Detector. The quadrature phase detector circuit compares the 20 MHz 2nd IF with the 90° phase shifted 20 MHz reference to detect an unlocked condition. Two gates on A7U2 form an exclusive OR gate where the output is low only when the inputs are out of phase.

20 MHz Phase Detector. Phase Detector A7U4 is a balanced mixer type detector which compares the 20 MHz 2nd IF with the 20 MHz reference. The output of the detector passes through a low pass filter to produce a dc voltage proportional to the phase difference.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assembly or cables shown in the accompanying diagram. Troubleshoot the A7 YIG Loop Phase Detector Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Oscilloscope	HP 180C/1801A/1821A
10:1 Divider Probe	HP 10004
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A7 Assembly (+20 Vdc and —10 Vdc). Also check the +10 Vdc from the zener diode A7VR1 and the regulated —5 Vdc. If incorrect troubleshoot these circuits. If correct proceed to Test 2.

Test 2. If there is either no or low output at J2 of the A9 Assembly, check the input, cables, and —10 Vdc power supply input at A9C1. If no fault is found, replace the A9 Assembly.

Test 3. If the search control output was found faulty on Service Sheet 1, proceed as follows: With both RF inputs of the A7 Assembly driven from the 20 MHz reference, check pin 2 of A7U3 for about —1.2 Vdc. Pin 3 of A7U3 for A7TP1) should be about —1.5 Vdc. (ECL logic levels are; on = —0.7 Vdc, off = —1.5 Vdc). If these inputs are correct A7U3 or A7VR3 is bad. If the voltage at A7TP1 is incorrect use an oscilloscope to check back to the RF inputs. The output of the Limiter Amplifiers (U1A pins 7 and 6, U1B pins 3 and 2) should be about 0.8 Vp-p.

Test 4. If the search control is working properly, compare the REF 20 MHz LIMITER/AMPL voltages and waveforms with the PHASE-SHIFTED 20 MHz LIMITER/AMPL voltages and waveforms. If one of the 20 MHz inputs is unplugged, the loop will be unlocked and this can be used for signal tracing through the phase detector and elliptic filter.

Figure 8-8.

SERVICE SHEET 5

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

A6 FM DRIVER ASSEMBLY

PRINCIPLES OF OPERATION

The FM Driver converts the dc error signal derived from the phase detector section to drive current for the YIG FM coil. In the FM mode, FM is added to the 20 MHz reference signal in the Modulation Section, routed through an amplifier in the RF Section, and is present on the YIG FM coil. If phase lock with the 20 MHz 2nd IF is lost, the search control turns on the Search Waveform Generator.

DC Amplifier and Output Driver

Transistors A6U1, A6Q1, 2, 3, and 4 function as an operational amplifier. The non-inverting input is A6U1 pin 4 and the inverting input is A6U1 pin 2. The phase detector error signal from the YIG loop phase detector is connected to the non-inverting input. The output of this amplifier drives the FM coil in the YIG oscillator. Current sense resistor A6R37 developes a voltage proportional to the FM coil current. This voltage is fed back to the amplifier input at A6U1 pin 2.

Search Waveform Generator

When the YIG is unlocked, the search control input (+3 Vdc) enables the search waveform oscillator A6U2B and A6U2C. Gate A6U2A acts as an inverter turning on A6Q5 and FET A6Q6. The output of the search waveform oscillator is a squarewave and is connected to the FET drain through gate A6U2D. Resistor A6R18 and capacitor A6C11 convert the squarewave to a sawtooth for driving the inverting input to the amplifier section. The fine tune winding of the YIG will then sweep until the quaerature phase detector (search control) goes to zero. FET A6Q6 will then be cutoff but capacitor A6C11 will hold its charge long enough for the loop to lock.

A7 YIG Loop Phase Detector Assembly
A9 20 MHz Filter Assembly
SERVICE SHEET 4

SERVICE SHEET 4 (Cont'd)

Quadrature Phase Detector. The quadrature phase detector circuit compares the 20 MHz 2nd IF with the 90° phase shifted 20 MHz reference to detect an unlocked condition. Two gates on A7U2 form an exclusive OR gate where the output is low only when the inputs are out of phase.

20 MHz Phase Detector. Phase Detector A7U4 is a balanced mixer type detector which compares the 20 MHz 2nd IF with the 20 MHz reference. The output of the detector passes through a low pass filter to produce a dc voltage proportional to the phase difference.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assembly or cables shown in the accompanying diagram. Troubleshoot the A7 YIG Loop Phase Detector Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Oscilloscope	HP 180C/1801A/1821A
10:1 Divider Probe	HP 10004
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A7 Assembly (+20 Vdc and —10 Vdc). Also check the +10 Vdc from the zener diode A7VR1 and the regulated —5 Vdc. If incorrect troubleshoot these circuits. If correct proceed to Test 2.

Test 2. If there is either no or low output at J2 of the A9 Assembly, check the input, cables, and —10 Vdc power supply input at A9C1. If no fault is found, replace the A9 Assembly.

Test 3. If the search control output was found faulty on Service Sheet 1, proceed as follows: With both RF inputs of the A7 Assembly driven from the 20 MHz reference, check pin 2 of A7U3 for about —1.2 Vdc. Pin 3 of A7U3 for A7TP1) should be about —1.5 Vdc. (ECL logic levels are; on = —0.7 Vdc, off = —1.5 Vdc). If these inputs are correct A7U3 or A7VR3 is bad. If the voltage at A7TP1 is incorrect use an oscilloscope to check back to the RF inputs. The output of the Limiter Amplifiers (U1A pins 7 and 6, U1B pins 3 and 2) should be about 0.8 Vp-p.

Test 4. If the search control is working properly, compare the REF 20 MHz LIMITER/AMPL voltages and waveforms with the PHASE-SHIFTED 20 MHz LIMITER/AMPL voltages and waveforms. If one of the 20 MHz inputs is unplugged, the loop will be unlocked and this can be used for signal tracing through the phase detector and elliptic filter.

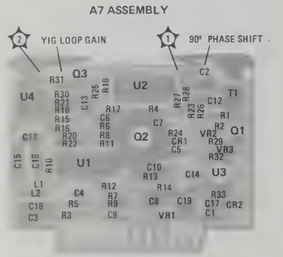


Figure 8-8. A7 Yig Loop Phase Detector Assembly Component and Test Point Locations

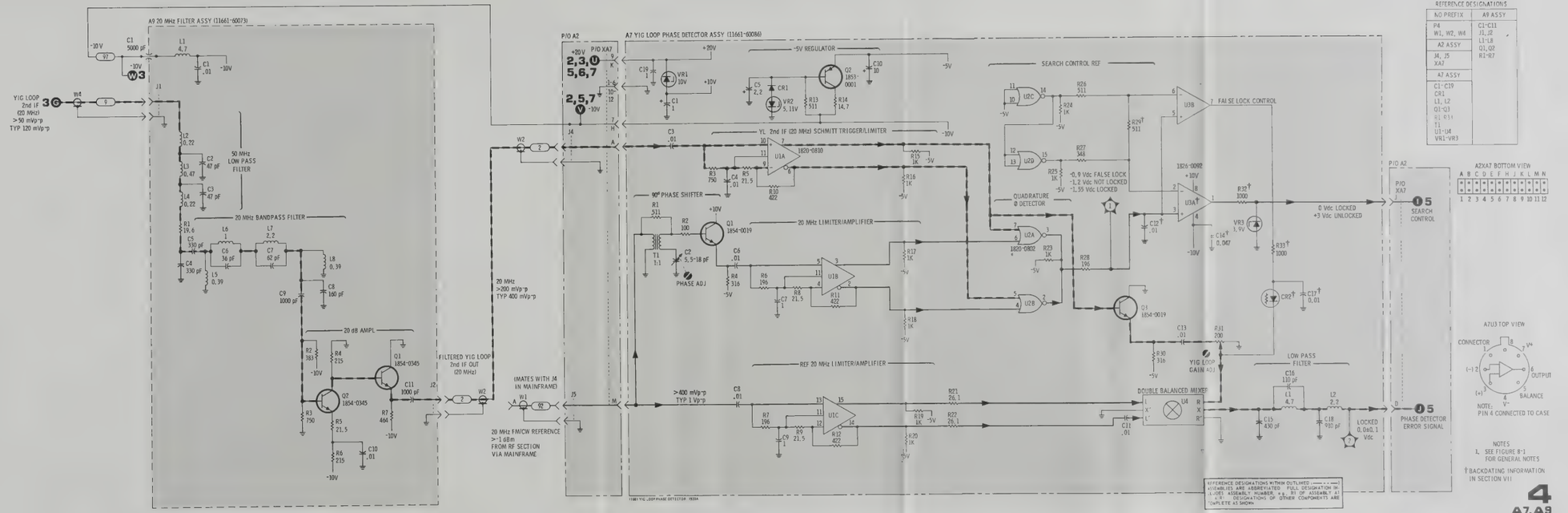


Figure 8-9. Yig Loop Phase Detector Section Schematic Diagram

SERVICE SHEET 5 (Cont'd)

A10 YIG Oscillator Assembly

The YIG Oscillator Assembly is non-repairable. The larger of two tuning coils is connected to the pretune circuit on the A5 Assembly. The smaller FM coil is connected to the A6 YIG FM Driver Assembly and is driven by the YIG phase detected error signal. The output of the YIG oscillator is therefore phase locked to the frequency digits 9 and 10 (100 MHz - 1200 MHz) and contains the FM if present.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the circuits using the test equipment and procedures given below.

Test Equipment	Model
Spectrum Analyzer	HP 8555A/8552B/140T
Oscilloscope	HP 180C/1801A/1821A
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A6 Assembly on the board. If any are missing, check for continuity of interconnections to the mainframe.

Test 2. To check the Search Waveform Generator proceed as follows. If the Search Control input is at a logic high, U2 pin 3 is low and A6Q5 will be turned ON. The output at U2 pin 11 will be a square wave, alternating between about 0 and 3.5 Vdc. The oscillator formed by A6U2B and A6U2C is ON at all times and has a period of about 5 milliseconds.

Test 3. Troubleshoot the amplifiers using the ramp waveform. With the search control active, use the oscilloscope to trace the ramp from U1 pin 2 (about 10 mV) to the output of the Output Driver. The signal amplitude at the base of A6Q3 should be about 10 Vp-p.

Test 4. If the tests on Service Sheet 1 shows that the YIG oscillator is far off frequency or has a low output signal amplitude, check the power supply voltages and interconnecting cables using the schematic diagram. If the inputs to the A10 YIG Oscillator Assembly are good, replace the entire assembly.

SERVICE SHEET 6

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

A3 SUM LOOP PHASE DETECTOR ASSEMBLY

PRINCIPLES OF OPERATION

The A3 Sum Loop Phase Detector compares the 30-20 MHz (1 Hz steps) signal from the mainframe and the 30-20 MHz Sum Loop 2nd IF signal and provides a phase error signal to the A4 assembly to accomplish phase lock.

Pulse Generators

The Sum Loop 2nd IF signal input is amplified by A3Q5. The output of A3Q5 drives the pulse forming circuit A3U3A to speed up the rise and fall time for the logic elements. Feedback inductor A3L2 allows gate A3U3A to operate more in its linear region and convert the small signal input to logic level pulses. A similar circuit is used for the 20 to 30 MHz input signal from the mainframe.

Digital Phase Detector and Low Pass Filters

The digital phase detector compares the phase relationship of two signals in the 20 to 30 MHz range and produces a dc error voltage proportional to the difference. Gates A3U3C and A3U2A are connected to form a flip-flop circuit. The output of gate A3U3C pin 8 is set to a logic high by the input signal. The 2nd IF flip-flop is reset by A3U2C only after both input signals have set their respective flip-flops. The duration of the logic high at the phase detector outputs therefore depends on the phase of the input signals. The dc level output of one lowpass filter will then be proportional to the phase difference of the input signals, while the other output is a constant low dc level. If in phase, both flip-flops reset immediately and both outputs will be a constant low dc level.

Active Filter/Integrator

The output circuit forms a differential amplifier. The two outputs of the phase detector are connected to the two inputs of this amplifier. Further filtering of the phase detector signal is accomplished by feedback resistor A3R21 and capacitor A3C19. If the loop is locked the amplifier output will be about +10 Vdc. If the 2nd IF is absent, for example, the output of the assembly will be about +20 Vdc.

A6 FM Driver Assembly
A10 YIG Oscillator Assembly
◀ SERVICE SHEET 5

SERVICE SHEET 5 (Cont'd)

A10 YIG Oscillator Assembly

The YIG Oscillator Assembly is non-repairable. The larger of two tuning coils is connected to the pretune circuit on the A5 Assembly. The smaller FM coil is connected to the A6 YIG FM Driver Assembly and is driven by the YIG phase detected error signal. The output of the YIG oscillator is therefore phase locked to the frequency digits 9 and 10 (100 MHz - 1200 MHz) and contains the FM if present.

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the circuits using the test equipment and procedures given below.

Test Equipment	Model
Spectrum Analyzer	HP 8555A/8552B/140T
Oscilloscope	HP 180C/1801A/1821A
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A6 Assembly on the board. If any are missing, check for continuity of interconnections to the mainframe.

Test 2. To check the Search Waveform Generator proceed as follows. If the Search Control input is at a logic high, U2 pin 3 is low and A6Q5 will be turned ON. The output at U2 pin 11 will be a square wave, alternating between about 0 and 3.5 Vdc. The oscillator formed by A6U2B and A6U2C is ON at all times and has a period of about 5 milliseconds.

Test 3. Troubleshoot the amplifiers using the ramp waveform. With the search control active, use the oscilloscope to trace the ramp from U1 pin 2 (about 10 mV) to the output of the Output Driver. The signal amplitude at the base of A6Q3 should be about 10 Vp-p.

Test 4. If the tests on Service Sheet 1 shows that the YIG oscillator is far off frequency or has a low output signal amplitude, check the power supply voltages and interconnecting cables using the schematic diagram. If the inputs to the A10 YIG Oscillator Assembly are good, replace the entire assembly.

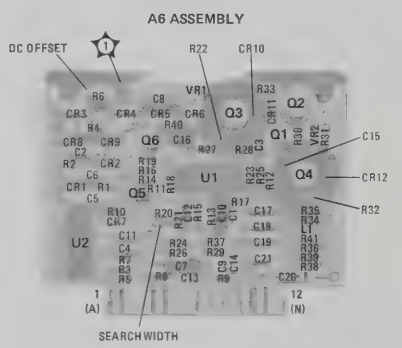


Figure 8-10. A6 FM Driver Assembly Component and Test Point Locations

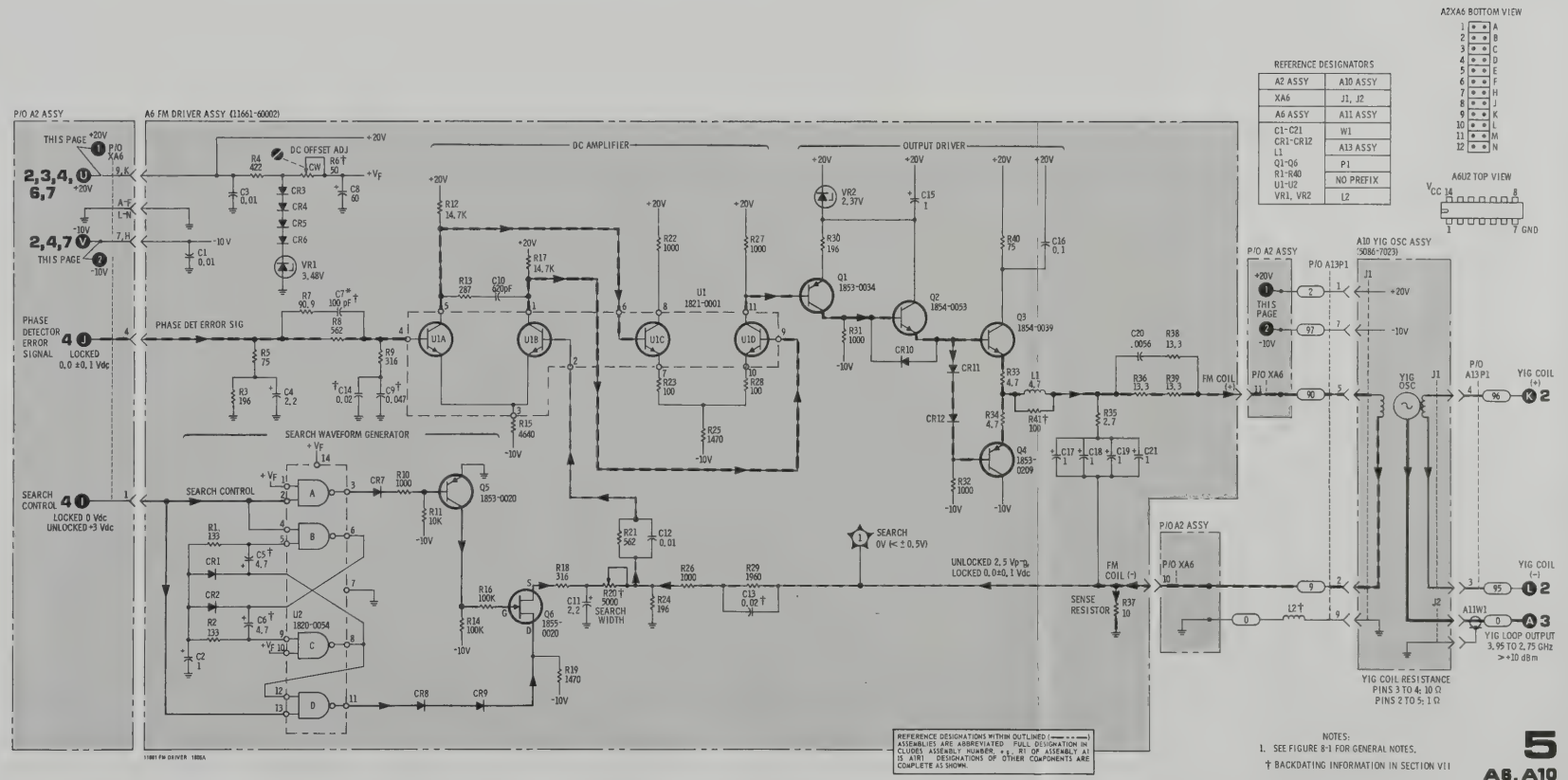


Figure 8-11. Yig Loop FM Driver/Oscillator Section Schematic Diagram

5
A6, A10

SERVICE SHEET 6 (Cont'd)

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the circuits using the test equipment and procedures given below.

Test Equipment	Model
Oscilloscope	HP 180C/1801A/1821A
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A3 Assembly on the board (+20, +5.25 and —40 Vdc). Also check the +5 Vdc filtered. If any voltages are incorrect, check continuity back to the mainframe. If correct proceed to test 2.

Test 2. Connect the two signal inputs to the 30-20 MHz input from the mainframe as described in Test 14 on Service Sheet 1. Connect oscilloscope probe first to U3C pin 10 and then to U1B pin 4 comparing the waveforms (30-20 MHz pulses). If either of these two signals are missing, check back to the common input with the oscilloscope probe to identify the problem.

Test 3. Move the oscilloscope probe first to U3C pin 8 and then to U1B pin 6. If pulses are missing from either point, use a digital voltmeter to locate the problem.

Test 4. Use a digital voltmeter to compare the two halves of the output circuit. If the inputs are balanced, similar points should have the same dc voltage. Note that A3TP1 is about +10 Vdc for phase lock.

SERVICE SHEET 7

NOTE

When a malfunction occurs, refer to Section VIII of the HP Model 8660-series mainframe Operating and Service Manual to begin troubleshooting (see System Troubleshooting Guide). Then, if that information does not isolate the problem to a definite instrument, refer to the Systems Troubleshooting information which preceeds Service Sheet 1 in the RF Section manual. This information may be used to isolate the defect to the Frequency Extension Module, another plug-in, or the mainframe. If the problem is in this module, refer to Service Sheet 1 for further troubleshooting information.

A4 SUM LOOP PRETUNE ASSEMBLY

PRINCIPLES OF OPERATION

The A4 Sum Loop Pretune Assembly converts the binary logic of the 10 MHz steps (digit 8) to a dc bias for coarse tuning of the VCO. This assembly includes a BCD to decimal decoder, a 4.05-3.95 GHz resistance ladder, and associated amplifiers. The pretune current is combined with the phase error signal from the A3 Assembly to produce the VCO control signal.

Logic Input

Inputs to XA4 pins N, M, L, and K are BCD code from the mainframe for digit 8 (10 MHz). Integrated circuit U1 converts the input from BCD to 10 line decimal. U1 also acts as a logic inverter so that only one line is ON (near ground) at any one time. The remaining lines will be above 3 Vdc. The digit selected will turn on one of the transistors Q1 through Q10. An adjustment for each transistor is provided for weighting the current for each digit.

Phase Error Signal

Transistor Q12 provides coupling of the phase error signal from the A3 Sum Loop Phase Detector Assembly. Potentiometer R6 provides an adjustment for controlling loop bandwidth at the low frequency end of the VCO range. Note that the higher the selected frequency, the lower the VCO tuning voltage. Transistor Q11 improves the high frequency response of the phase error signal. The voltage range of the tuning is from about −10 Vdc to about −16 Vdc.

Figu

A3 Sum Loop Phase Detector Assembly
SERVICE SHEET 6

SERVICE SHEET 6 (Cont'd)

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assemblies or cables shown on the accompanying diagram. Troubleshoot the circuits using the test equipment and procedures given below.

Test Equipment	Model
Oscilloscope	HP 180C/1801A/1821A
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A3 Assembly on the board (+20, +5.25 and −40 Vdc). Also check the +5 Vdc filtered. If any voltages are incorrect, check continuity back to the mainframe. If correct proceed to test 2.

Test 2. Connect the two signal inputs to the 30-20 MHz input from the mainframe as described in Test 14 on Service Sheet 1. Connect oscilloscope probe first to U3C pin 10 and then to U1B pin 4 comparing the waveforms (30-20 MHz pulses). If either of these two signals are missing, check back to the common input with the oscilloscope probe to identify the problem.

Test 3. Move the oscilloscope probe first to U3C pin 8 and then to U1B pin 6. If pulses are missing from either point, use a digital voltmeter to locate the problem.

Test 4. Use a digital voltmeter to compare the two halves of the output circuit. If the inputs are balanced, similar points should have the same dc voltage. Note that A3TP1 is about +10 Vdc for phase lock.

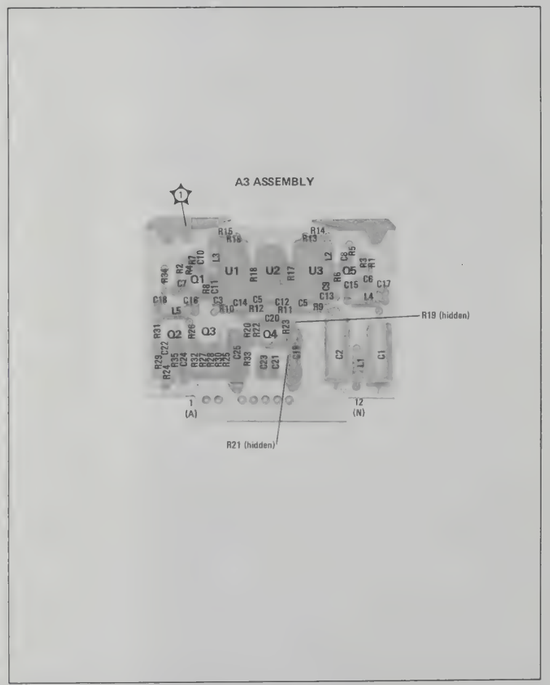


Figure 8-12. A3 Sum Loop Phase Detector Assembly Component Locations

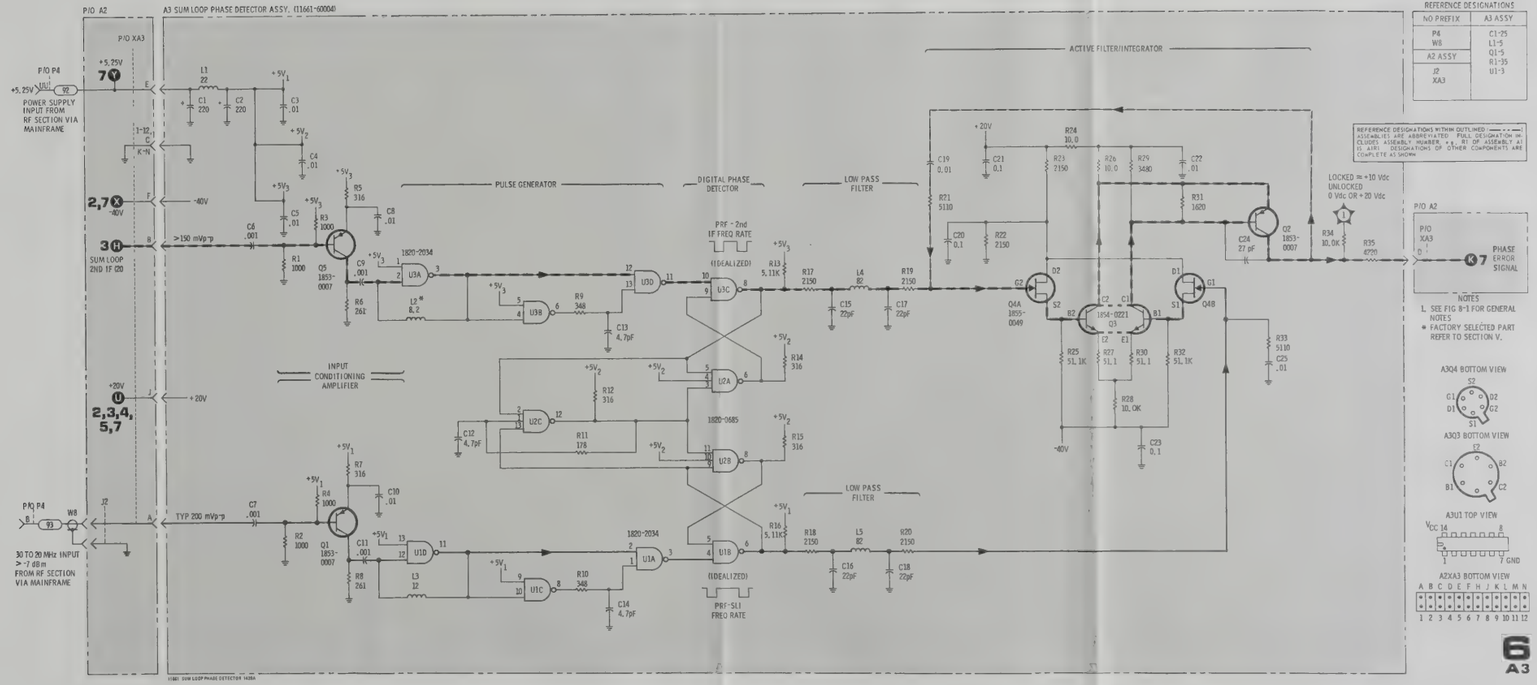


Figure 8-13. Sum Loop Phase Detector Section Schematic Diagram

SERVICE SHEET 7 (Cont'd)

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assembly or cables shown in the accompanying diagram. Troubleshoot the A4 Sum Loop Pretune Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A4 Assembly (+20V, +5V, —10V, and —40V). If incorrect check for continuity back to the mainframe. If correct proceed to test 2.

Test 2. If a fault lies with the pretune logic decoding, check the collector of transistors A4Q1 through A4Q10 while exercising the digit 8 tuning on the mainframe (0 through 9). Each line should pull up to about 0 Vdc when the matching number is selected. If there is no change in the output of the A4 Assembly (pin 1) for the entire range of tuning, probably A4U1 is bad.

Test 3. The output of the A4 Assembly (pin 1) should be at about —10 Vdc for 4.05 GHz and —26 Vdc for 3.95 GHz tuning of the VCO. If the voltages fall much outside of this range check A4Q11 and A4Q12 and associated components for short or open failures.

GENERAL REMOVAL AND DISASSEMBLY PROCEDURE (Cont'd)

c. Reconnect the mainframe line (Mains) power cable to the power outlet and set the mainframe line switch to ON.

Access To Internal Assemblies

a. For access to the circuit boards, remove the three screws securing the cover to the case. Circuit boards A3, A4, A5, A6, and A7 may be unplugged by simultaneously pulling up on both plastic arms associated with that board.

b. To open the Frequency Extension Module as shown in Figure 8-17 under Extension Module Internal View, remove the two screws on the side of the case marked "REMOVE FOR SERVICE" (shown in Figure 8-16).

CAUTION

Care must be exercised when removing plug-in boards with the module swung open. The printed circuit board guide bar is not rigid enough to use the extractor arm (black) on the open side without bending. To avoid damage, use a finger to lift the bottom edge of the board on the open side while using the color coded extractor arm on the far side.

Reassembly Procedure

Reassemble in reverse order of disassembly. Replace the two screws on the side of the case, replace the top cover after verifying all boards are in place, and then install in mainframe. Before replacing the instrument cover, verify that the two RF cables to J1 and J2 are properly installed as shown in Figure 2-2.

SERVICE SHEET 7 (Cont'd)

TROUBLESHOOTING

It is assumed that the troubleshooting information on Service Sheet 1 was used to isolate a circuit defect to the assembly or cables shown in the accompanying diagram. Troubleshoot the A4 Sum Loop Pretune Assembly by using the test equipment and procedures given below.

Test Equipment	Model
Digital Voltmeter	HP 34740A/34702A
Service Kit	HP 11672A

Test 1. Check the power supply inputs to the A4 Assembly (+20V, +5V, −10V, and −40V). If incorrect check for continuity back to the mainframe. If correct proceed to test 2.

Test 2. If a fault lies with the pretune logic decoding, check the collector of transistors A4Q1 through A4Q10 while exercising the digit 8 tuning on the mainframe (0 through 9). Each line should pull up to about 0 Vdc when the matching number is selected. If there is no change in the output of the A4 Assembly (pin 1) for the entire range of tuning, probably A4U1 is bad.

Test 3. The output of the A4 Assembly (pin 1) should be at about −10 Vdc for 4.05 GHz and −26 Vdc for 3.95 GHz tuning of the VCO. If the voltages fall much outside of this range check A4Q11 and A4Q12 and associated components for short or open failures.

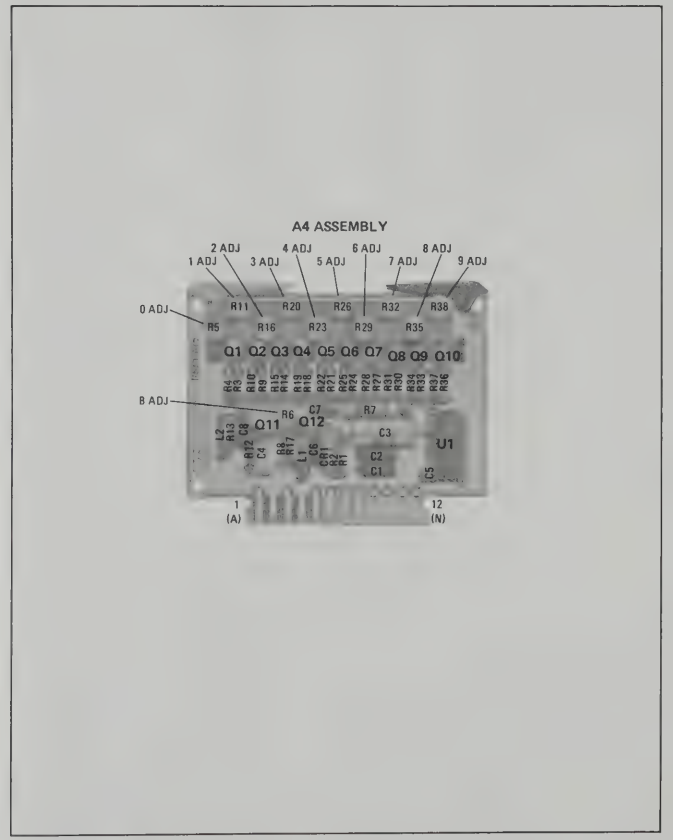


Figure 8-14. A4 Sum Loop Pretune Assembly Component Locations

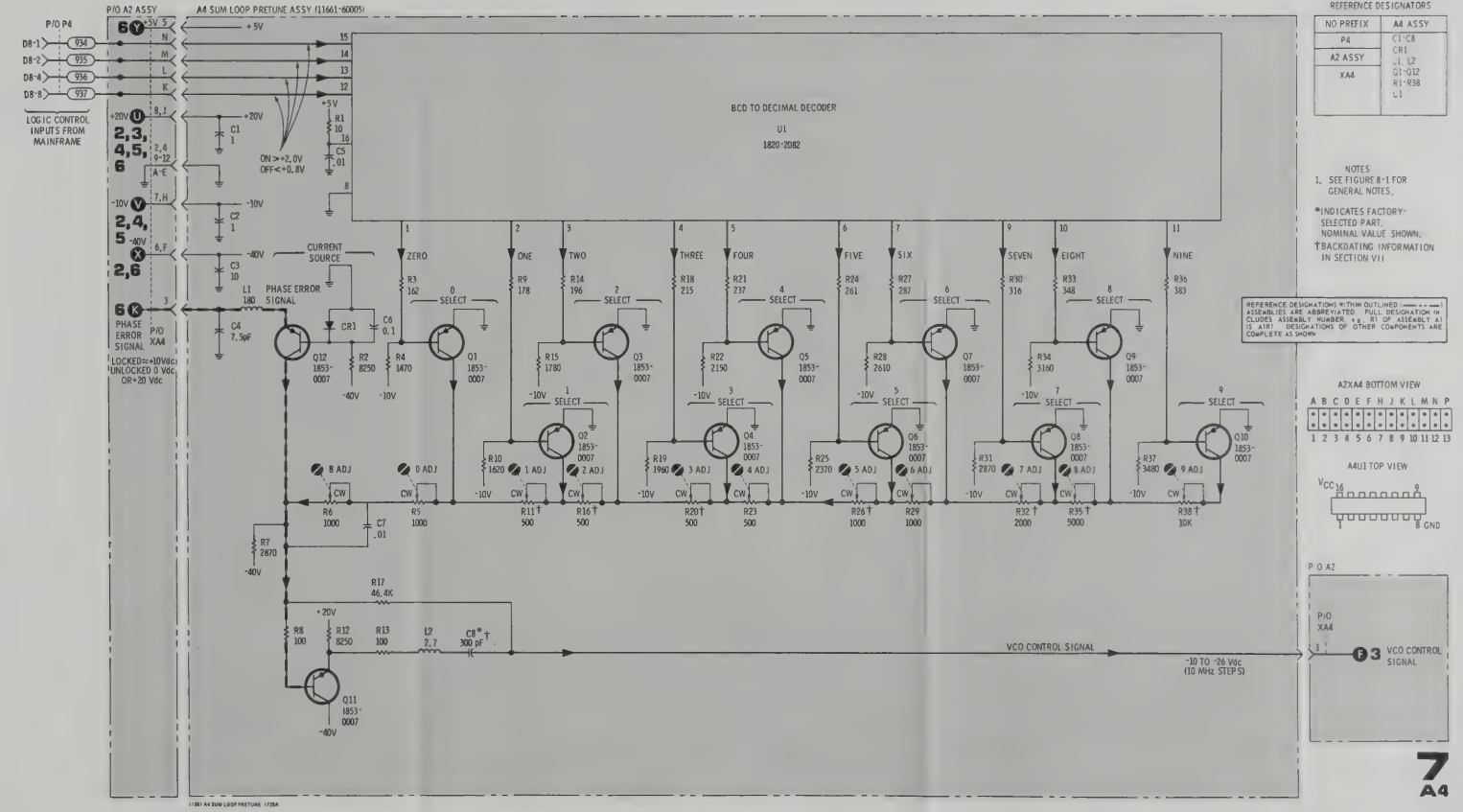


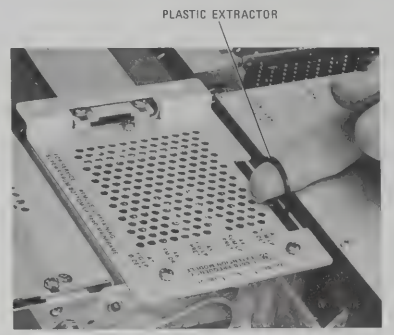
Figure 8-15. Sum Loop Pretune Section Schematic Diagram

Table 8-2. Assemblies, Chassis Mounted Parts, and Adjustment Locations (1 of 2)

Reference Designator	Service Sheet(s)	Figure(s)
A1 Assembly	3	8-6, 8-17
A1A1 Assembly	3	8-6, 8-17
A1A1C1 20 MHz Adj	3	8-6, 8-17
A1A2 Assembly	3	8-6, 8-17
A1A2R3 VCO Bias Adj	3	8-6, 8-17
A1A3 Assembly	3	8-6, 8-17
A1A4 Assembly	3	8-6, 8-17
A2 Assembly	2,3,4,5,6	8-17
A3 Assembly	6	8-12, 8-17
A4 Assembly	7	8-14, 8-17
A4R5 O Adj	7	8-14
A4R6 B Adj	7	8-14
A4R11 1 Adj	7	8-14
A4R16 2 Adj	7	8-14
A4R20 3 Adj	7	8-14
A4R23 4 Adj	7	8-14
A4R26 5 Adj	7	8-14
A4R29 6 Adj	7	8-14
A4R32 7 Adj	7	8-14
A4R35 8 Adj	7	8-14
A4R38 9 Adj	7	8-14
A5 Assembly	2	8-4, 8-17
A5R13 "1" Adj	2	8-4, 8-17
A5R15 "2" Adj	2	8-4, 8-17
A5R17 "4" Adj	2	8-4, 8-17
A5R19 "8" Adj	2	8-4, 8-17
A5R21 "10" Adj	2	8-4, 8-17
A5R29 OFFSET Adj	2	8-4, 8-17
A5R39 GAIN Adj	2	8-4, 8-17
A6 Assembly	5	8-10, 8-17
A6R6 DC OFFSET Adj	5	8-10, 8-17
A7 Assembly	4	8-8, 8-17
A7C2 Phase Adj	4	8-8, 8-17
A7R17 Phase Ref Adj	4	8-8, 8-17
A7R20 YIG Loop Gain Adj	4	8-8, 8-17

Table

Refe
A8 As A9 As A10 A A11 A A12 A A13 A A14 A
J1 J2 P4
W1, 2 W3 W4 W5-7 W8 W9-13



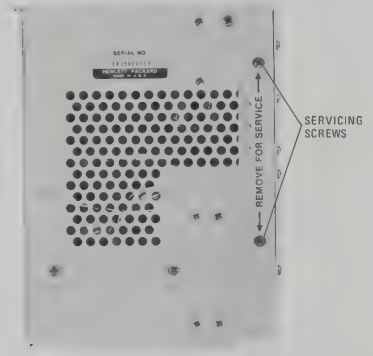


Figure 8-16. Location of Extractor and Servicing Screws

GENERAL REMOVAL AND DISASSEMBLY PROCEDURE

WARNING

To avoid contact with the line voltage, remove the line (Mains) power cable from the power outlet before removing or connecting cables to the Frequency Extension Module.

a. Remove the mainframe top cover by first removing the four Pozidriv screws; then slide the cover back and off the mainframe side rails.

b. If the Frequency Extension Module is to be removed from the mainframe, remove the bottom cover in a similar manner.

Model 11661B Module Removal

a. Remove the two cables connected to J1 and J2 as shown in Figure 2-2.

b. Remove the three securing screws shown in Figure 2-2 holding the Frequency Extension Module cover to the mainframe.

c. Remove the two bottom screws holding the Frequency Extension Module. These screws are accessible from the bottom of the mainframe.

d. Grip the plastic extractor shown in Figure 8-16 and lift straight up with a slight rotating action.

Interconnection of the Frequency Extension Module to the Mainframe for Troubleshooting Purposes

WARNING

With the mainframe top cover removed, power is supplied to the system during troubleshooting. Energy available at many points may, if contacted, result in personal injury.

a. Use extender cable HP 11672-60002 to connect J4 on the mainframe to P4 on the Frequency Extension Module.

b. Reconnect the RF connecting cables to J1 and J2 on the rear of the Frequency Extension Module. Refer to Figure 2-2 for cable color code.

NOTE

The interconnect cables and adaptors are found in the HP 11672A Service Kit. They may all be ordered in the kit or as individual pieces. Refer to the 11672A Operating Note or the Mainframe Manual for a pictorial cross reference.

GENERAL REMOVAL AND DISASSEMBLY PROCEDURE (Cont'd)

c. Reconnect the mainframe line (Mains) power cable to the power outlet and set the mainframe line switch to ON.

Access To Internal Assemblies

a. For access to the circuit boards, remove the three screws securing the cover to the case. Circuit boards A3, A4, A5, A6, and A7 may be unplugged by simultaneously pulling up on both plastic arms associated with that board.

b. To open the Frequency Extension Module as shown in Figure 8-17 under Extension Module Internal View, remove the two screws on the side of the case marked "REMOVE FOR SERVICE" (shown in Figure 8-16).

CAUTION

Care must be exercised when removing plug-in boards with the module swung open. The printed circuit board guide bar is not rigid enough to use the extractor arm (black) on the open side without bending. To avoid damage, use a finger to lift the bottom edge of the board on the open side while using the color coded extractor arm on the far side.

Reassembly Procedure

Reassemble in reverse order of disassembly. Replace the two screws on the side of the case, replace the top cover after verifying all boards are in place, and then install in mainframe. Before replacing the instrument cover, verify that the two RF cables to J1 and J2 are properly installed as shown in Figure 2-2.

A4 Sum Loop Pretune Assembly
SERVICE SHEET 7

Table 8-2. Assemblies, Chassis Mounted Parts, and Adjustment Locations (1 of 2)

Reference Designator	Service Sheet(s)	Figure(s)
A1 Assembly	3	8-6, 8-17
A1A1 Assembly	3	8-6, 8-17
A1A1C1 20 MHz Adj	3	8-6, 8-17
A1A2 Assembly	3	8-6, 8-17
A1A2R3 VCO Bias Adj	3	8-6, 8-17
A1A3 Assembly	3	8-6, 8-17
A1A4 Assembly	3	8-6, 8-17
A2 Assembly	2,3,4,5,6	8-17
A3 Assembly	6	8-12, 8-17
A4 Assembly	7	8-14, 8-17
A4R5 O Adj	7	8-14
A4R6 B Adj	7	8-14
A4R11 1 Adj	7	8-14
A4R16 2 Adj	7	8-14
A4R20 3 Adj	7	8-14
A4R23 4 Adj	7	8-14
A4R26 5 Adj	7	8-14
A4R29 6 Adj	7	8-14
A4R32 7 Adj	7	8-14
A4R35 8 Adj	7	8-14
A4R38 9 Adj	7	8-14
A5 Assembly	2	8-4, 8-17
A5R13 "1" Adj	2	8-4, 8-17
A5R15 "2" Adj	2	8-4, 8-17
A5R17 "4" Adj	2	8-4, 8-17
A5R19 "8" Adj	2	8-4, 8-17
A5R21 "10" Adj	2	8-4, 8-17
A5R29 OFFSET Adj	2	8-4, 8-17
A5R39 GAIN Adj	2	8-4, 8-17
A6 Assembly	5	8-10, 8-17
A6R6 DC OFFSET Adj	5	8-10, 8-17
A7 Assembly	4	8-8, 8-17
A7C2 Phase Adj	4	8-8, 8-17
A7R17 Phase Ref Adj	4	8-8, 8-17
A7R20 YIG Loop Gain Adj	4	8-8, 8-17

Table 8-2. Assemblies, Chassis Mounted Parts, and Adjustment Locations (2 of 2)

Reference Designator	Service Sheet(s)	Figure(s)
A8 Assembly	3	8-17
A9 Assembly	4	8-17
A10 Assembly	5	8-17
A11 Assembly	3	8-17
A12 Assembly	3	8-17
A13 Assembly	—	—
A14 Assembly	3	8-17
J1	3	2-2, 8-17
J2	3	2-2, 8-17
P4	2,3,4,5,6,7	8-17
W1, 2	4	8-17
W3	3	8-17
W4	3,4	8-17
W5-7	3	8-17
W8	6	8-17
W9-13	3	8-17

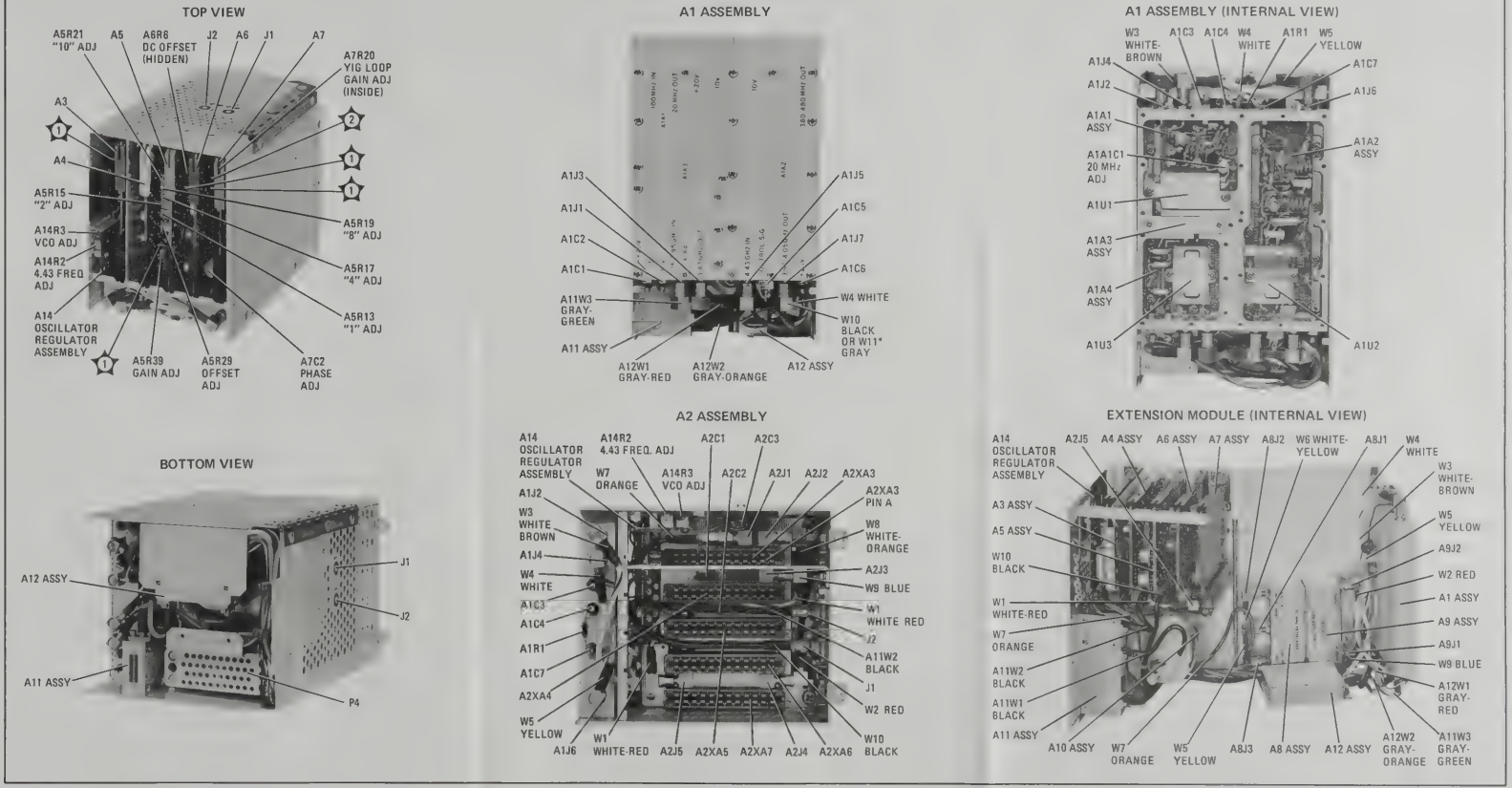


Figure 8-17. Assemblies, Chassis Mounted Parts, Adjustment Locations, and Test Point Locations

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COMPANY

ADDRESS

TECHNICAL CONTACT PERSON

PHONE NO. EXT.

MODEL NO. SERIAL NO.

MODEL NO. SERIAL NO.

P.O. NO. DATE

Accessories returned with unit

☐ NONE ☐ CABLE(S)

☐ POWER CABLE ☐ ADAPTER(S)

OTHER

over

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MODEL NO. SERIAL NO.

P.O. NO. DATE

Accessories returned with unit

☐ NONE ☐ CABLE(S)

☐ POWER CABLE ☐ ADAPTER(S)

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PHONE NO. EXT.

MODEL NO. SERIAL NO.

MODEL NO. SERIAL NO.

P.O. NO. DATE

Accessories returned with unit

☐ NONE ☐ CABLE(S)

☐ POWER CABLE ☐ ADAPTER(S)

OTHER

over

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TECHNICAL CONTACT PERSON

PHONE NO. EXT.

MODEL NO. SERIAL NO.

MODEL NO. SERIAL NO.

P.O. NO. DATE

Accessories returned with unit

☐ NONE ☐ CABLE(S)

☐ POWER CABLE ☐ ADAPTER(S)

OTHER

over

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Should one of your HP instruments need repair, the HP service organization is ready to serve you. However, you can help us serve you more effectively. When sending an instrument to HP for repair, please fill out this card and attach it to the product. Increased repair efficiency and reduced turn-around time should result.

COMPANY

ADDRESS

TECHNICAL CONTACT PERSON

PHONE NO. EXT.

MODEL NO. SERIAL NO.

MODEL NO. SERIAL NO.

P.O. NO. DATE

Accessories returned with unit

☐ NONE ☐ CABLE(S)

☐ POWER CABLE ☐ ADAPTER(S)

OTHER

over

HEWLETT PACKARD

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ADDRESS

TECHNICAL CONTACT PERSON

PHONE NO. EXT.

MODEL NO. SERIAL NO.

MODEL NO. SERIAL NO.

P.O. NO. DATE

Accessories returned with unit

☐ NONE ☐ CABLE(S)

☐ POWER CABLE ☐ ADAPTER(S)

OTHER

over

Service needed

☐ CALIBRATION ONLY

☐ REPAIR ☐ REPAIR & CAL

OTHER _____

_____

Observed symptoms/problems

FAILURE MODE IS:

☐ CONSTANT ☐ INTERMITTENT

SENSITIVE TO:

☐ COLD ☐ HEAT ☐ VIBRATION

FAILURE SYMPTOMS/SPECIAL CONTROL SETTINGS_____

_____

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If unit is part of system list model number(s) of other interconnected instruments. _____

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Service needed

☐ CALIBRATION ONLY

☐ REPAIR ☐ REPAIR & CAL

OTHER _____

_____

Observed symptoms/problems

FAILURE MODE IS:

☐ CONSTANT ☐ INTERMITTENT

SENSITIVE TO:

☐ COLD ☐ HEAT ☐ VIBRATION

FAILURE SYMPTOMS/SPECIAL CONTROL SETTINGS_____

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If unit is part of system list model number(s) of other interconnected instruments. _____

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Service needed

☐ CALIBRATION ONLY

☐ REPAIR ☐ REPAIR & CAL

OTHER _____

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Observed symptoms/problems

FAILURE MODE IS:

☐ CONSTANT ☐ INTERMITTENT

SENSITIVE TO:

☐ COLD ☐ HEAT ☐ VIBRATION

FAILURE SYMPTOMS/SPECIAL CONTROL SETTINGS_____

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If unit is part of system list model number(s) of other interconnected instruments. _____

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Service needed

☐ CALIBRATION ONLY

☐ REPAIR ☐ REPAIR & CAL

OTHER _____

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Observed symptoms/problems

FAILURE MODE IS:

☐ CONSTANT ☐ INTERMITTENT

SENSITIVE TO:

☐ COLD ☐ HEAT ☐ VIBRATION

FAILURE SYMPTOMS/SPECIAL CONTROL SETTINGS_____

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If unit is part of system list model number(s) of other interconnected instruments. _____

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Service needed

☐ CALIBRATION ONLY

☐ REPAIR ☐ REPAIR & CAL

OTHER _____

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Observed symptoms/problems

FAILURE MODE IS:

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SENSITIVE TO:

☐ COLD ☐ HEAT ☐ VIBRATION

FAILURE SYMPTOMS/SPECIAL CONTROL SETTINGS_____

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If unit is part of system list model number(s) of other interconnected instruments. _____

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Service needed

☐ CALIBRATION ONLY

☐ REPAIR ☐ REPAIR & CAL

OTHER _____

_____

Observed symptoms/problems

FAILURE MODE IS:

☐ CONSTANT ☐ INTERMITTENT

SENSITIVE TO:

☐ COLD ☐ HEAT ☐ VIBRATION

FAILURE SYMPTOMS/SPECIAL CONTROL SETTINGS_____

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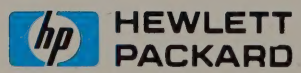
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