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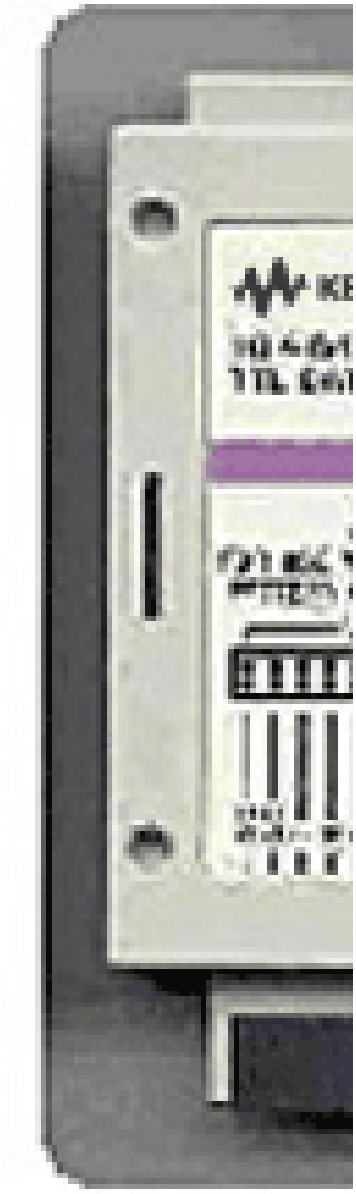
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E8141A LVDS Pattern Generator Data Pod

Obsolete

Overview and Features | **Post-Purchase Upgrades** | **Resources**

Notice to EU customers: This product is no longer available due to lack of compliance with EU RoHS directive 2011/65/EU. Keysight will continue service and support to the end of worldwide support life.

Data Pod Configuration

- Logic level support: LVDS

- Recommended lead set: Keysight E8142A
- Order at least one data pod for every 8 output channels
- Design in a dedicated header for pod connection or order one 8-channel lead set for each pod

Features

- Maximum clock: 300 MHz
- Skew: Typical less than 1 ns; worst case 2 ns
- 3-state enable: Positive true TTL; no connect=enabled
- Output type: 65LVDS389 (LVDS data lines) and 10H125 (TTL non-3-state channel 7)

The LVDS data pod supports Keysight Technologies 16720A and 1670G-004 pattern generators. Each pattern generator requires at least one clock pod, up to 4 data pods, and lead sets for each clock and data pod. For each E8141A data pod that you will be using, you will need one (1) E8142A lead set.

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- Recommended lead set: E8142A

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