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Expert team | Trust guarantee | 100% satisfaction

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Find the *Lauterbach LA-7708* at our website: ***Click HERE***

Products Overview

Linglin He

- **Products**
- Features
- Tool chain
- Support

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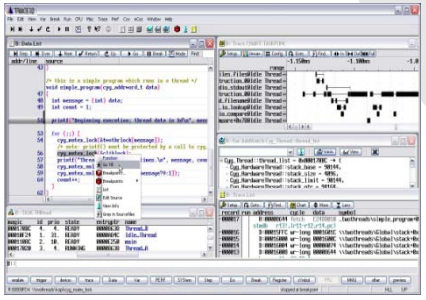
Lauterbach Products



In-Circuit Debuggers



In-Circuit Emulators



Front-ends for virtual prototypes

Logic Analyzer



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LAUTERBACH DEVELOPMENT TOOLS

In-Circuit Debuggers

Power-Debugger



PowerTrace



CombiProbe



Simulator

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Power Debugger

First Generation Debugger Module



PowerDebug USB-2 (LA-7708)

- Universal BDM debug and JTAG controller
- Compatible with all BDM and JTAG debuggers
- USB 2.0



PowerDebug Ethernet (LA-7705)

- Universal BDM debug and JTAG controller
- Compatible with all BDM and JTAG debuggers
- USB 2.0
- Ethernet 100MBit /10MBit
- Can be upgraded to PowerTrace

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Power Debugger

Second Generation Debugger Module



PowerDebug II (LA-7699)

- Universal BDM debug and JTAG controller
- Compatible with all BDM and JTAG debuggers
- USB 2.0
- Ethernet 1GBit / 100MBit / 10MBit
- Podbus Express Interface
- Debug controller with 1000 DMIPS / 500 MHz
- Expandable to PowerTrace II

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Power Debugger

Debug Cable



- Processor specific adaption
- Contains software license
- Supported Processor Families:

- ARM/XSCALE
- Power Architecture
- MIPS32/MIPS64
- Intel Atom™/x86
- 78K0R/RL78
- APS
- AVR32
- C166CBC
- CPU32
- ColdFire
- H8S/23x9
- M32R
- M-Core
- MCS08
- MSP430
- RX
- S12X
- SH
- TriCore
- V850
- VR
- XC2000/C166SV2
- XC800
- DSPs
- Softcores
- Configurable Cores
- Auxiliary Processors

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Power Debugger

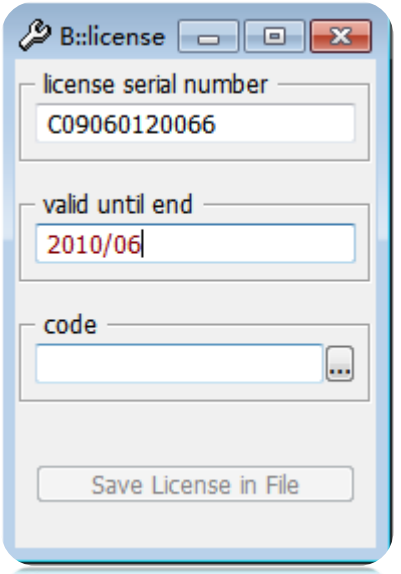
Debug Cable



S / N?

C090601202XX

Which version of Trace32 Can I use ?



SW maintenance Contract

What Can I debug ?



- MPC56XX ?
- ARM11 ?
- Cortex –A- R ?
- MIPS32 ?

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Power Trace

First Generation Trace Module



PowerTrace (LA-7690)

- Universal NEXUS / debug controller
- Compatible with all BDM and JTAG debuggers
- USB 2.0
- Ethernet 100MBit /10MBit
- 256MB / 512MB Trace Storage
- 300 MHz effective sample rate

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Power Trace

Second Generation Trace Module



PowerTrace II (LA-7692/ LA-7693 /LA-7694)

- Universal NEXUS / Debug Controller
- Compatible with all BDM and JTAG debuggers
- Needs PowerDebug II for Host Connection
- 1GB / 2GB / 4GB Trace Storage
- 600 MHz effective sample rate
- 17 logic analyzer channels

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Power Trace

Preprocessors / Nexus adapter



AutoFocus II

- Target voltage 1.8 .. 3.6 V
- 600 MHz effective sample rate
- Supports Embedded Trace Macrocell (ETM)
- Supports Program Trace Macrocell (PTM)
- ETM Mictor38 connector
- MIPI Samtec60 connector



ETM-HSSTP

- Support of High-Speed Serial Trace Port
- Compatible to Xilinx Aurora protocol
- Support of up to four differential lanes
- Maximum 6,25Gbit/s lane speed
- Up to 24 Giga CPU cycles

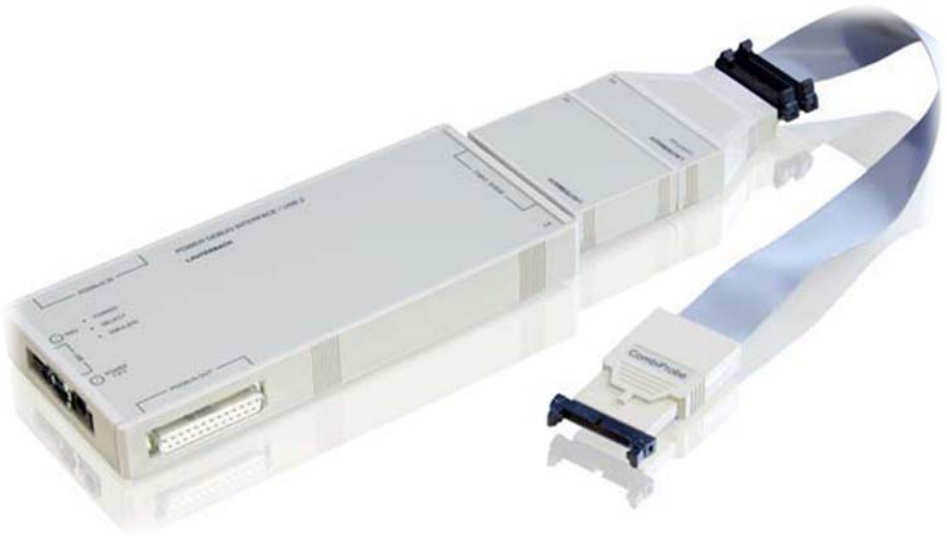


MPC55xx/MPC56xx NEXUS

- Support for NEXUS standard class 1 to 3+
- Support for VLE (Variable Length Encoding)
- Debugging and Tracing eTPUs

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CombiProbe



- Debug cable and 128-MB of trace memory
- Standard JTAG
- Serial Wire Debug Port from ARM
- cJTAG (IEEE P1149.7)
- Endless trace by real-time streaming (pipe mode)
- 4-Bit MIPI System Trace
- ITM over Serial Wire Output (Serial Wire Viewer) or 4-Bit TPIU
- 4-Bit ETMv3 in continuous mode
- Voltage range 0.3 V to 3.3 V

- Support for

  ARM/CORTEX, PIC32, X-GOLD110, X-GOLD102 ACTEL, ARM, ATMEL, CYPRESS, ENERGYMICRO, FREESCALE, FUJITSU, INFINEON, LUMINARYMICRO, MICROCHIP, MIPS, NXP, SAMSUNG, STM, TI, TOSHIBA

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In-Circuit Emulators

TRACE32-ICE for x86, 68k ...



Timing analyzer	▪ 32 channels ▪ 200 MHz sampling rate ▪ Trigger system
In-Circuit emulator	▪ From 8 to 32 Bit ▪ Option. port analyzer
Emulation memory	▪ Up to 16+16 Mbyte ▪ Dual-ported ▪ Data, break and flag
State analyzer	▪ 120 channels ▪ 32 KFrames trace ▪ Hypertrace
Emulation control unit	▪ Mapper ▪ Trigger unit ▪ Counter, VCO, pulse Gen.
System control unit	▪ Parallel, USB or Ethernet interface ▪ 50 MIPS 32-Bit controller ▪ 8..64 MByte memory

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In-Circuit Emulators

TRACE32-FIRE for MCS12, C167, H8S ...



FIRE system controller

- Printer, USB or PODBUS interface
- 40 MIPS 32-Bit controller
- 16 MByte RAM

FIRE port analyzer

- Up to 1024 channels
- 32 External channels
- 100 MHz sampling rate
- Transient recording

FIRE emulation controller

- Mapper
- Dual-port controll
- VCO, Pulse Gen., etc.

FIRE emulation memory

- Up to 16+16 Mbyte
- 8 ns SYNCH, ZBT
- 10 ns ASYNCH

Emulation adapter

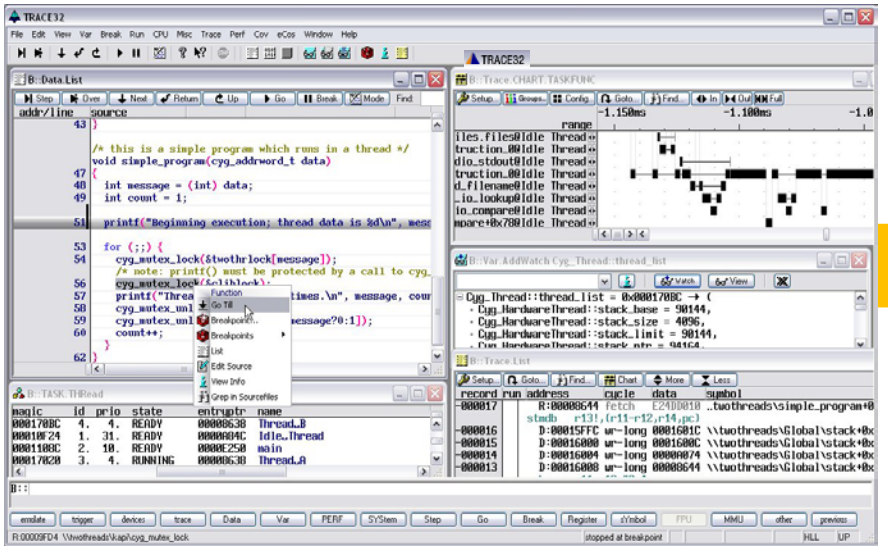
- Family module
- Target adapter

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Front-ends for virtual prototypes

To debug software before target hardware/microprocessor is available

TRACE32-PowerView



Virtual prototype/core simulator

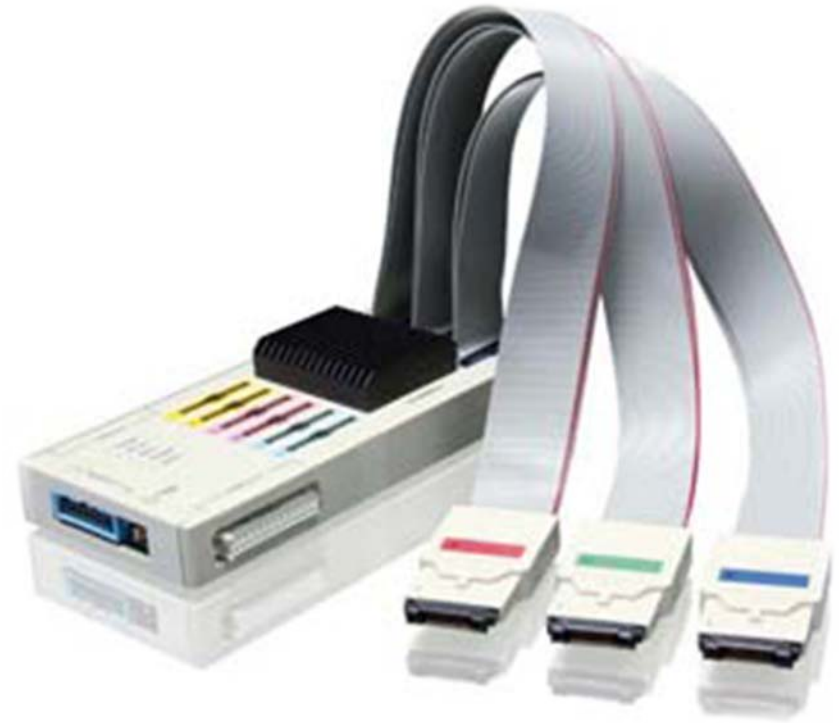
API

- VAST for PPC, ARM, StarCore, OAK, TeakLite, V850
- Virtio for ARM
- TI Simulator for C55x, C54x, C6200, C6400, C6700
- GDI for TriCore
- MDI for MIPS, ZSP
- more to follow

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Logic Analyzer

First Generation PowerIntegrator



PowerIntegrator (LA-7940)

- 200(400) MHz State, 500 MHz Timing
- Transient Recording
- 48 Bit Time-Stamp
- 204 Channels
- 512 Kframes
- Requires PODBUS-Interface

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Logic Analyzer

Second Generation PowerIntegrator



PowerIntegrator II

- 200(400) MHz State, 500 MHz Timing
- Transient Recording
- 48 Bit Time-Stamp
- 102 Channels
- 64 / 128 / 256 Mframes
- Requires PODBUS Express Interface

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Products Overview

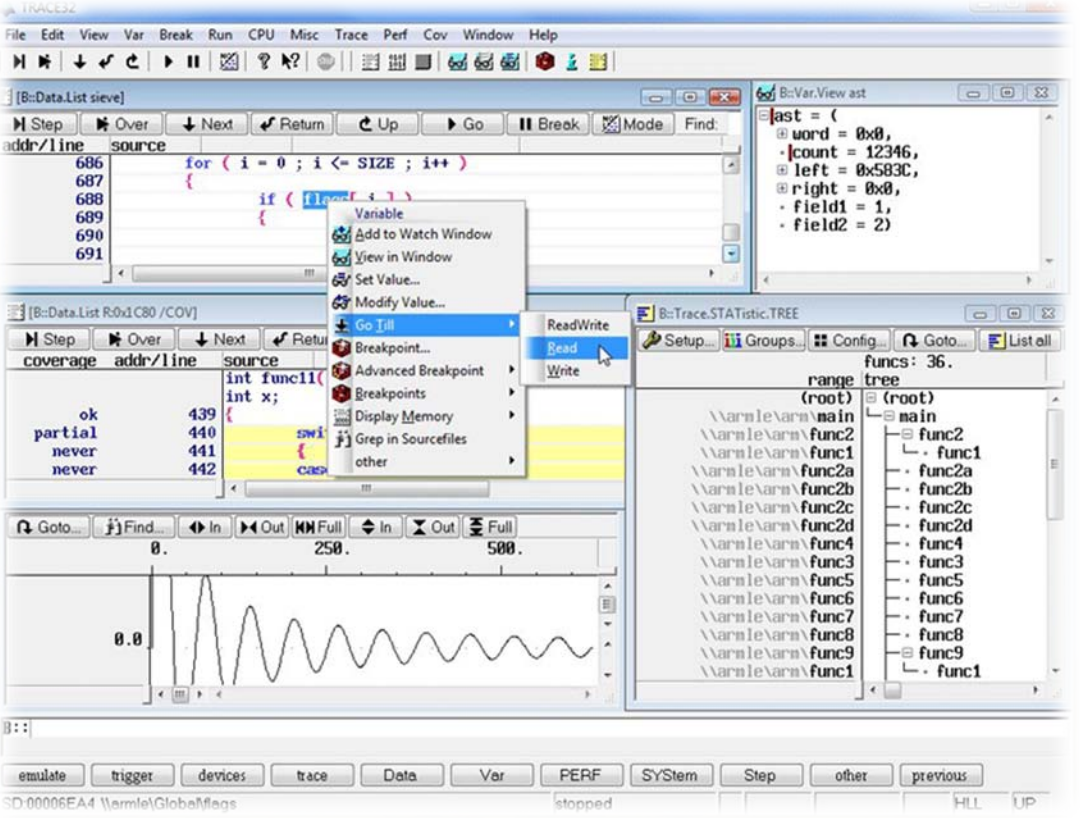
- Products
- **Features**
- Tool chain
- Support

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Universal User Interface

PowerView – the uniform GUI for all products

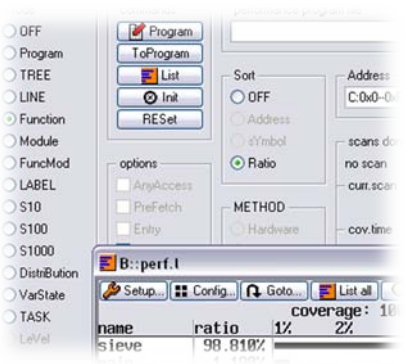
- ASM Debugger/HLL Debugger
- FLASH Programmer
- CTS- Context Tracking System
- STATISTIC and PERFORMANCE Analyzer
- COVERAGE Analyzer
- RTOS Debugger
- INT Tools Integration
- Front-End to TRACE32 Instruction Set Simulator
- Front-End to Third-Party Core Simulators/Virtual
- Prototypes Front-End to Third-Party Target Servers

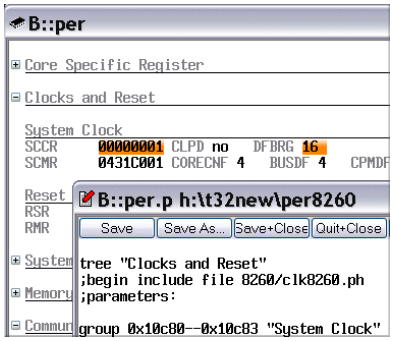


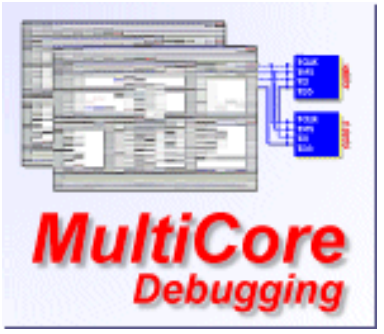
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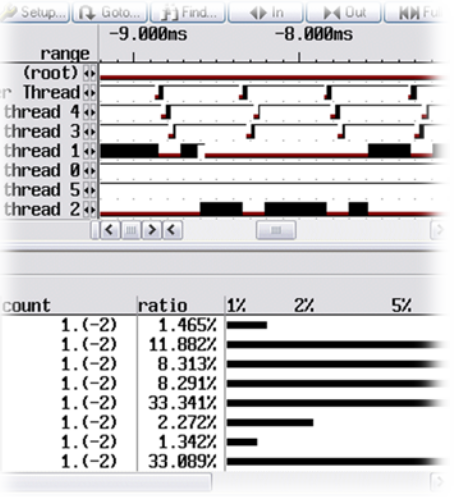
Debugger Features

- Multi-core Debugging
- Peripheral Browser
- NOR FLASH Programming
- NAND FLASH Programming
- HLL Debugger
- Sample-based Profiling
- MMU Support
- OS-aware Debugging
- VM-aware Debugging









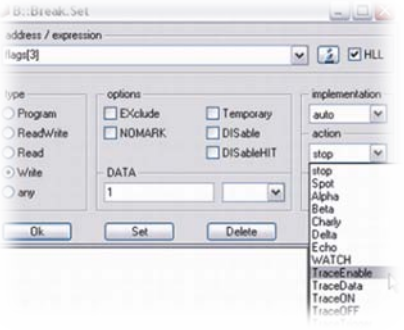


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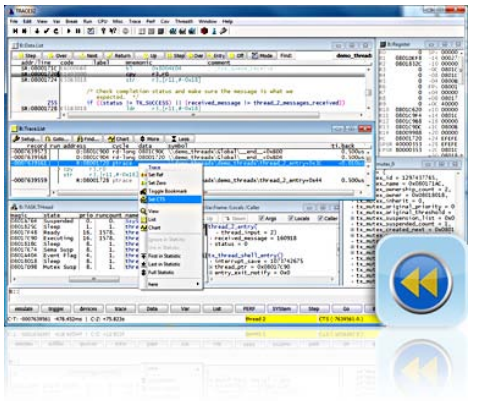
Trace Features

- Trace Sinks
- Filter and Trigger
- Trace-based Debugging
- Cache Analysis
- Trace-based Profiling
- Trace-based Code Coverage
- Energy Profiling





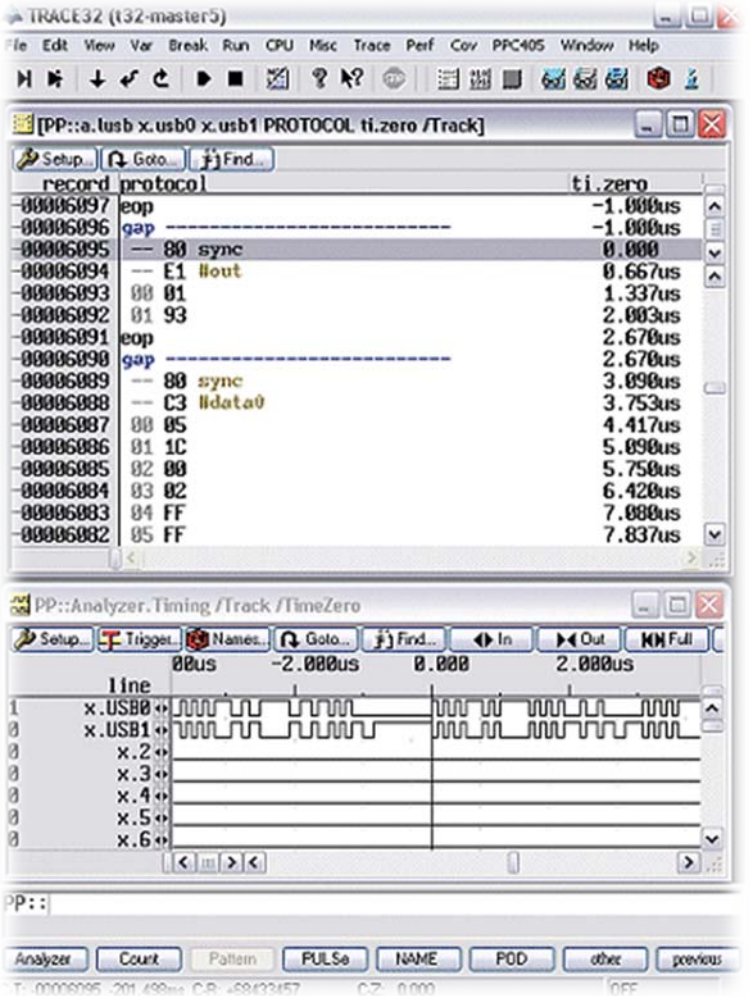




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Logic Analyzer Features

- PowerProbe - Logic and Protocol Analyzer
- PowerIntegrator - Logic and Bus Analyzer
- Protocol Analysis
  - CAN Support
  - FlexRay Support
  - LIN Support
  - SPI Support
  - USB Support
  - I2C Support
  - JTAG Support
  - ASYNC Support
  - PCI Support
  - DRAM Support
  - User Specific Protocols



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Products Overview

- Products
- Features
- **Tool chain**
- Support

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LAUTERBACH DEVELOPMENT TOOLS

Support Host Operating System

- MACINTOSH
  - LINUX/PPC
  - MAC OS-X/X86
- PC
  - NOVELL-UNIXWARE
  - WINDOWS XP
  - WINDOWS 7
  - LINUX
- SPARC
  - SUNOS 4.1.1
  - SOLARIS 2.3
  - SOLARIS 2.X
- VAX-STATION















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Supported Compilers

TRACE32 supports all standard compiler formats for the following programming languages

- C
- C++
- JAVA
- ADA
- ASM
- PASCAL
- PLM

















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Supported NOR / NAND / Serial Controller

























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Supported Target Operating Systems























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Supported Tool Integration















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Products Overview

- Products
- Features
- Tool chain
- **Support**

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Warranty

- 12 months software maintenance
  - Software updates for the first year are included in the price
- 36 months hardware free repairing



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Training

- Training documents on help system
- 3 levels of courses
  - Online Courses
  - Courses at Academy in Suzhou
    - PowerDebug
    - PowerTrace
    - LINUX Debugging
  - On site Courses & Consulting
    - Customized
    - Specific consulting



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Technical Support Contacts

- A free lifetime (typical 5 - 10 years) technical support for contract goods by telephone or e-mail
- Telephone & E-mail
  - Tel: + 86 512 62658030
  - E-mail: support_cn@lauterbach.com

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Thank you!

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Questions?

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