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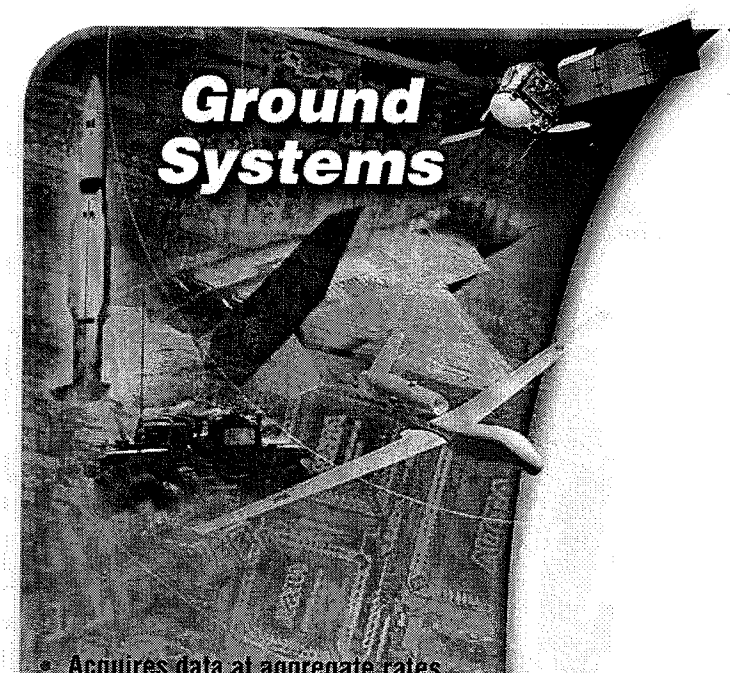
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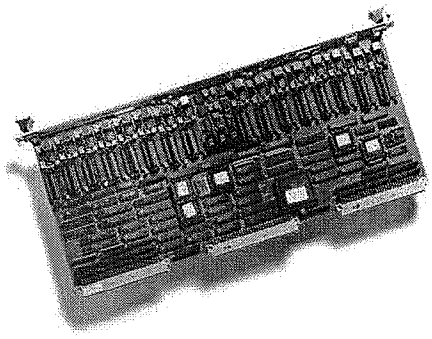
Find the *Lockheed Martin ADC572* at our website: ***Click HERE***



Analog Input

ADC572/ADC573

- Acquires data at aggregate rates of 5.3 Msps
- 12-bit resolution analog-to-digital conversion (binary and 2's complement)
- Self-calibration procedure initiated on module setup
- Simultaneous Sample and Hold (acquire data from all channels simultaneously optionally including other Analog Input modules)
- Individual sample acquisition rates per channel (depending on source)



Acquire and digitize analog data for real-time applications.

The Analog Input module simultaneously acquires up to 16 channels of analog input data based on an internal or external sample clock at rates up to 333 kHz for each channel. Each channel can be independently set up for one of four input voltage ranges. Digital data is tagged by user-definable tags for insertion onto the MUXbus II. The system's data flow architecture allows modules to be added as necessary for an application (limited only by the number of chassis in the system).

L3 communications
Telemetry–West

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ADC572/ADC573 Specifications

Analog Inputs

Channels per Module 16
Input Voltage Ranges: (jumper selectable per channel)*
ADC572 . . . . . .0–5V, 0–10V, ± 5V, ± 10V
ADC573 . . . . . .0–15V, 0–30V, ± 15V, ± 30V
Resolution . . . . . .12 bits
Sample and Hold . . . . . .Simultaneous for all channels
Sample Rate (per Channel) . . .333 kHz max.
Common Mode Rejection . . . .70 dB min.
Overvoltage Protection . . . . . .± 75 max.
Impedance . . . . . .10 GΩ typ.
Bias Current . . . . . .4 nA typ. @ +25°C
Amplifier Slew Rate . . . . . .4V/µs min. (6V/µ typ.)
Amplifier Settling Time . . . . . .10 µs typ. (to 0.01% ± 10V step)
Calibration . . . . . .On Setup or Cal command
* Contact factory for other range requirements.

Sample Clock

Source . . . . . .Internal or external (jumper selectable)
Maximum Rate . . . . . .333 kHz
Minimum Internal Rate . . . . . .0.596 Hz
Internal Clock Resolution . . . .Based on 10 MHz divider
Sample Edge . . . . . .Rising
Pulse Width . . . . . .40 ns min.
Voltage . . . . . .TTL or RS-422 (jumper selectable)
Impedance . . . . . .10 GΩ typ. (low impedance terminator optional)

MUXbus II Output

Burst Rate . . . . . .8 MHz max.
Continuous Rate . . . . . .5.33 MHz max.
Format:
Analog Unipolar Channel . . . .12-bit binary, zero filled to 32 bits
Analog Bipolar Channel . . . . .12-bit, 2's complement with sign extended to 32 bits
Output FIFO . . . . . .8 K words
Sequencer Memory . . . . . .16 K bytes

Functions

Analog-to-Digital
Conversion . . . . . .Digitize data for transmission, storage, digital processing
Simultaneous Sampling . . . . . .Daisy chain external sample clocks of more than one module to obtain simultaneous sampling of > 16 channels

Program Setup

Keyboard and Mouse . . . . . .Fill-in-the-blank OSF/Motif™ displays aided by list-pick selections
ASCII Text File . . . . . .User-created description

General Requirements

Chassis Requirement . . . . . .1 9U slot
Maximum per Chassis 15
Maximum per System 112
Power (DC) . . . . . .5V @ 3.5 A max.
Rear Panel . . . . . .2 slots
Connector Type . . . . . .1 DB-37 (1 DB-25 for external control)
Environment . . . . . .See Base System Cjassosdata sheet
Module Size . . . . . .365mm (9U) x 160mm
Diagnostic Display . . . . . .4 status LEDs

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