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Find the *Measurement Computing PCI-DDA08/12* at our website: ***Click HERE***

SPECIFICATIONS

PCI-DDA08/12
PCI-DDA04/12
PCI-DDA02/12

Analog Output Boards



Revision 2, August, 2001

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Typical for 25°C unless otherwise specified.

Power Consumption

+5V Operating	
PCI-DDA08/12	1.6A typical, 2.6A max
PCI-DDA04/12	1.5A typical, 2.4A max
PCI-DDA02/12	1.4A typical, 2.2A max
+12V	
PCI-DDA08/12	24 mA typical, 48 mA max
PCI-DDA04/12	12 mA typical, 24 mA max
PCI-DDA02/12	6 mA typical, 12 mA max
–12V	
PCI-DDA08/12	16 mA typical, 25 mA max
PCI-DDA04/12	8 mA typical, 12 mA max
PCI-DDA02/12	4 mA typical, 6 mA max

Analog Output

D/A converter type	AD7837B
Resolution	12 bits
Number of channels	
PCI-DDA08/12	8
PCI-DDA04/12	4
PCI-DDA02/12	2
Output Ranges	±10V, ±5V, ±2.5V, 0 to 10V, 0 to 5V, 0 to 2.5V. Each channel is independently programmable.
Data transfer	Programmed I/O.
Offset error (calibrated)	±(300 µV +¼LSB)
Gain error (calibrated)	±(300 µV +¼LSB)
Differential nonlinearity	±1 LSB max
Integral nonlinearity	±1 LSB max
Monotonicity	12 bits
D/A Gain drift	±2 ppm/°C
D/A Offset drift	±5 µV/°C
Throughput	PC dependent
Settling time (20V step to ±½ LSB)	6 µs typ, 10 µs max
Slew Rate	5V/µs
Current Drive	±5 mA
Output short-circuit duration	25 mA indefinite
Output coupling	DC
Output impedance	0.1 Ohms max
Miscellaneous	Double buffered output latches Update DACs individually or simultaneously (software-selectable) On power-up and reset, all DAC's are cleared to 0 volts, ±210mV

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Digital Input / Output

Digital Type (main connector)	82C55 mode 0 emulation
Output:	74S244
Input:	74LS373
Configuration	4 banks of 8, 4 banks of 4; programmable by bank as input or output
Number of channels	48 I/O
Output High	2.4 volts min @ –15mA
Output Low	0.5 volts max @ 64 mA
Input High	2.0 volts min, 7 volts absolute max
Input Low	0.8 volts max, –0.5 volts absolute min
Power-up / reset state	Input mode (high impedance)

Environmental

Operating temperature range	0 to 70°C
Storage temperature range	–40 to 100°C
Humidity	0 to 90% non-condensing

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