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SPECIFICATIONS

CIO-DAC02/16

Dual 16-bit Analog Outputs



**MEASUREMENT
COMPUTING™**

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POWER CONSUMPTION

| | |
|--------------|------------------------------|
| + 5V Supply | 400 mA Typical / 675 mA Max. |
| + 12V Supply | None |
| - 12V Supply | None |

ANALOG OUTPUTS

| | |
|-------------------------------|--|
| Outputs | 2 |
| Resolution | 16 Bits (1 part in 65536) |
| Type | AD660BN |
| Output Ranges: | $\pm 10V$, $\pm 5V$, $\pm 2.5V$, 0 to 10V, 0 to 5V, 0 to 2.5V |
| Latches | Double-Buffered |
| Update | Individual or Simultaneous (Jumper Select) |
| Power Up/Reset | DAC Output forced to 0 Volts |
| Offset Error | Adjustable to 0 |
| Gain Error | Adjustable to 0 |
| Differential Linearity | ± 1 LSB Max. |
| Integral Non linearity | ± 1 LSB Max. |
| Monotonicity | Guaranteed to 15 bits over temp range |
| Gain Drift | ± 15 ppm/C Max |
| Bipolar Offset Drift | ± 5 ppm/C Max |
| Unipolar Offset Drift | ± 3 ppm/C Max |
| Slew Rate | 2.8V/ μ s Typical |
| Settling Time: | |
| 20 Volt Step to 0.0008% | 12 μ s typical, 19 μ s max |
| 10 Volt Step to 0.0008% | 6 μ s typical, 9 μ s max |
| Current Drive | ± 5 mA Min |
| Output Resistance (Op27) | 0.1 Ohm Max |
| Output Short Circuit Duration | 40 mA Min Continuous |

ENVIRONMENTAL

| | |
|-----------------------|-------------------------|
| Operating Temperature | 0 to 70 Deg. C |
| Storage Temperature | -40 to +100 Deg. C |
| Humidity | 0 to 90% Non-condensing |

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