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SPECIFICATIONS

CIO-DAC08/16
CIO-DAC16/16

Analog Outputs



Revision 6, April, 2001

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Power Consumption

+5V supply	
CIO-DAC16/16	1.8 A typical, 2.25 A max
CIO-DAC08/16	1.3 A typical, 1.7 A max

Analog Output

Resolution	16 bits
Number of channels	
CIO-DAC16/16	16 Voltage Outputs
CIO-DAC08/16	8 Voltage Outputs
D/A type	AD660BN
Voltage Ranges	±5V, ±10V, 0 to 5V, 0 to 10V, jumper-selectable
Offset error	Adjustable to zero
Gain error	Adjustable to zero
Differential nonlinearity	±1 LSB max
Integral nonlinearity	±1 LSB max
Monotonicity	Guaranteed monotonic to 15 bits over temperature
Gain drift (DAC)	±15 ppm/°C max
Bipolar offset drift (DAC)	±5 ppm/°C max
Unipolar offset drift (DAC)	±3 ppm/°C max
Throughput	System dependent
Slew Rate	2.8V/µs Typical
Settling time (20V step to .0008%	12 µs typ, 19 µs max
Settling time (10V step to .0008%	6 µs typ, 9 µs max
Current Drive	±5 mA min.
Output resistance (OP-27)	0.1 ohm max.
Output short-circuit duration	40 mA min. Continuous
Miscellaneous	Double-buffered output latches Update DACs individually or all DACs simultaneously (jumper-selectable) Power up and reset, all DAC's cleared to 0 volts (jumper selects bipolar or unipolar zero)

Environmental

Operating temperature range	0 to 70°C
Storage temperature range	–40 to 100°C
Humidity	0 to 90% non-condensing

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