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Find the *Mercury Computer Systems RACE MCJ6* at our website: *Click HERE*



Computer Systems, Inc.
MERCURY

MCV6™ Multicomputer System Module

RACEway Ready

Complete Development Environment

Heterogeneous Compute Environment

Support of Industry Standards

Flexible Interconnections

Scalable I/O Bandwidth

Configuration Expandability

The MCV6 is a 6U VME module designed for embedded use in chassis and workstations. As the first member of Mercury's RACE® family of products, the MCV6 offers true scalability, configurability, and real-time determinism in an open architecture. System developers can configure the optimal solution without restructuring their applications to fit a rigid architecture.

Highlights

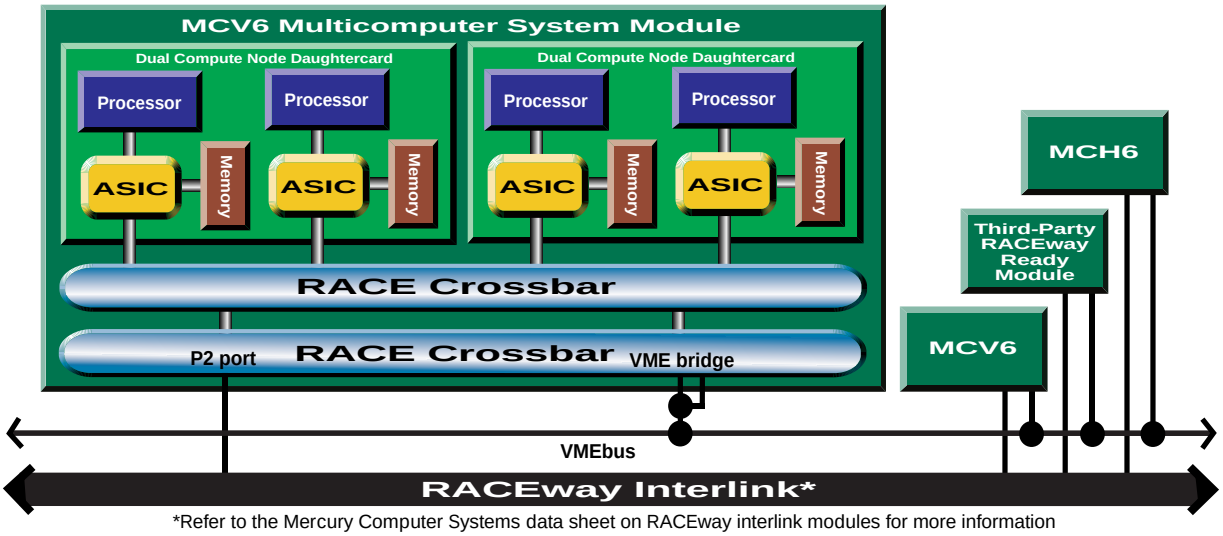
- Development environment includes real-time multicomputer operating system, optional Parallel Application System (PAS™) API, and extensive multicomputer development tools.
- Four Intel i860 compute nodes (CNs) with a distributed/shared memory architecture provide up to 320 MFLOPS of processing power per module (a CN consists of a processor, memory, and an ASIC).
- Bandwidth automatically increases as CNs are added to the system (essential for true scalability).
- Support for major interconnection topologies (including buses, rings, large crossbar, dataflow, and meshes) allows system architecture to be tailored to application requirements.
- Direct I/O and processor interconnect through RACEway allows up to 480 MBs/s of concurrent communication per MCV6 module.
- Memory configurations include 2, 4, 8, 16, 32, and 64 MBs per CN. CNs and memory are modular to allow for system expansion.
- Transparent memory buffering allows all memory accesses to be at peak rates (160 MBs/s) regardless of external I/O rate, minimizing memory contention between the processor and I/O.
- MCV6 utilizes VME64 and supports VSB expansion.
- Dedicated performance metering registers provide essential performance monitoring and debugging information.
- Heterogeneous compatibility with MCH6™ and MCH9™ modules allows system solutions with the i860, PowerPC, and SHARC CNs.
- Available as a component module or as part of an integrated chassis-level system.

RACEway Interlink Standard

RACEway Interlink is the ANSI/VITA 5-1994 standard. The current standards document is entitled ANSI/VITA 5-1994 American National Standard for RACEway Interlink, July 31, 1995.

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*Refer to the Mercury Computer Systems data sheet on RACEway interlink modules for more information

Commercial Environmental Specifications, Standard Models

Temperature (C):	
Storage	-40° to 85°
Operating*	-20° to 50°
Vibration (operating):	
Sweep (Mil-5400)	Curve IIa & IIb (2.5 G max)
Random (Mil-810)	3 Grms
Shock (operating)	15G, 11ms half sine (Mil-810)
Humidity (%)**	10 to 90
Altitude (ft.)	10,000

* As measured at inlet air with an airflow of 16 cfm/slot.

** Relative, non-condensing.

i860 Compute Node Performance

Floating-point, peak performance:	
Per CN, single precision	80 MFLOPS
Per CN, double precision	60 MFLOPS
8-module (32-CN)	2.5 GFLOPS
16-module (64-CN)	5.0 GFLOPS
Peak integer performance	40 MIPS/CN
Clock cycle time	25ns

Transfer Rates (MBs/s)

DRAM access	160 peak, 157 sustained
VME memory access (master and slave):	
D32 single transfers	13 sustained
D32 block transfers	35 sustained
D64 block transfers	70 sustained
DMA controller	160 peak, 150 sustained (writes)
Crossbar switch (per port)	160 peak, 150 sustained (writes)
Crossbar switch (per module)	480 peak aggregate (non-broadcast)

Electrical/Mechanical

Input voltage	4.75 to 5.25Vdc
Power	28 watts maximum @ 5Vdc
Dimensions:	
MCV6 motherboard	6.3 X 9.2 inches
i860 daughtercard	4.3 X 5.45 inches
Slot-to-slot spacing	.80 inches, standard
Weight	1.6 lbs. with 4 CNs of 16MB

Other System Specifications

Configuration:	
Number of CNs per slot	One to four*
Memory (MBs/CN)	2, 4, 8, 16, 32, 64*
DMA controllers	One per CN
Max. number of CNs	80 per 21-slot chassis
Max. number of modules	20 per 21-slot chassis
I/O daughtercards	Up to one per module**
MTBF	82,270 hrs.***
VME compliance	VITA Rev. C.1 IEEE 1014-87 Rev. C.3
VME64	VITA Rev. D
RACEway Interlink	ANSI/VITA 5-1994

*Maximum of two CNs per slot, when configured with 32 or 64 MBs of memory.

**Occupies one processor daughtercard location.

*** MCV6 with four CNs, 4 MBs per CN.

Computer Systems, Inc.
MERCURY
The Ultimate Performance Machine

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