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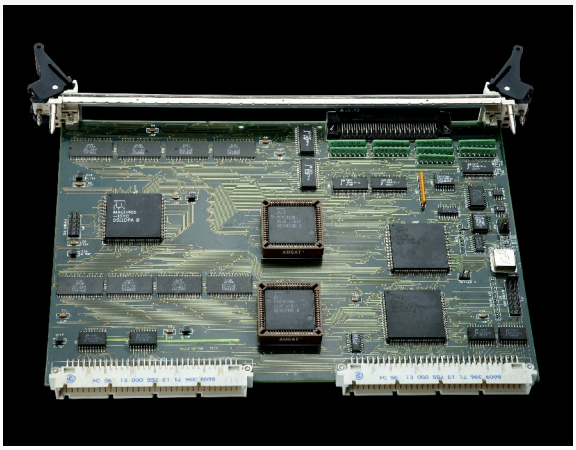
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9531 FPDP SRAM Module



Specifications:

Memory: 8 Mbytes SRAM

FPDP Transfer Rate: 60 Mbytes/sec

VME Transfer Rate: 40 Mbytes/sec

VME Specifications

Meets VME Specifications Revision C.1 IEEE Std. 1014-1987

A32:D32 BLT Slave

Address Modifier Code 09, or 0B HEX.

16M consecutive byte locations, base address configurable within 4G Extended I/O space.

Board size: 6U

Power Requirements

+5 Volts @ 400mA

Environmental

Operating Temperature: -20 to 85 °C

Storage Temperature: -40 to 125 °C

Shock: 25g, 11ms on all axis

The 9531 FPDP SRAM Module offers the following features:

The user may access the 8 Mbytes of SRAM through either the VMEbus or FPDP interface. The user configures the unit via software, defining the number of FPDP transfers per frame, time between SYNC pulses, and the FPDP clock speed. The user may also set periodic PIO1 and PIO2 pulses.

When used with a D/A module the data is transmitted out the FPDP. The user may configure the unit for a single shot waveform or continuous waveform. When used with an A/D module the data is received from the FPDP interface.

Conformal coating is standard on all units.

Conduction-cooled version available.

Merlin Embedded
151 Sawgrass Corners Dr., Suite 105
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