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Find the *Curtiss-Wright / VMETRO / Micro Memory MM-6260AD/16M* at our website: ***Click HERE***

MM-6260AD

128 MBYTE, HIGH-DENSITY, VMEbus DRAM MEMORY MODULE

A super-fast, high capacity memory module for VMEbus systems, the MM-6260AD provides up to 128 Mbytes of DRAM and 32 MBytes/sec data rates. Extensive use of surface mount technology allows the module to occupy a single 6U card slot.

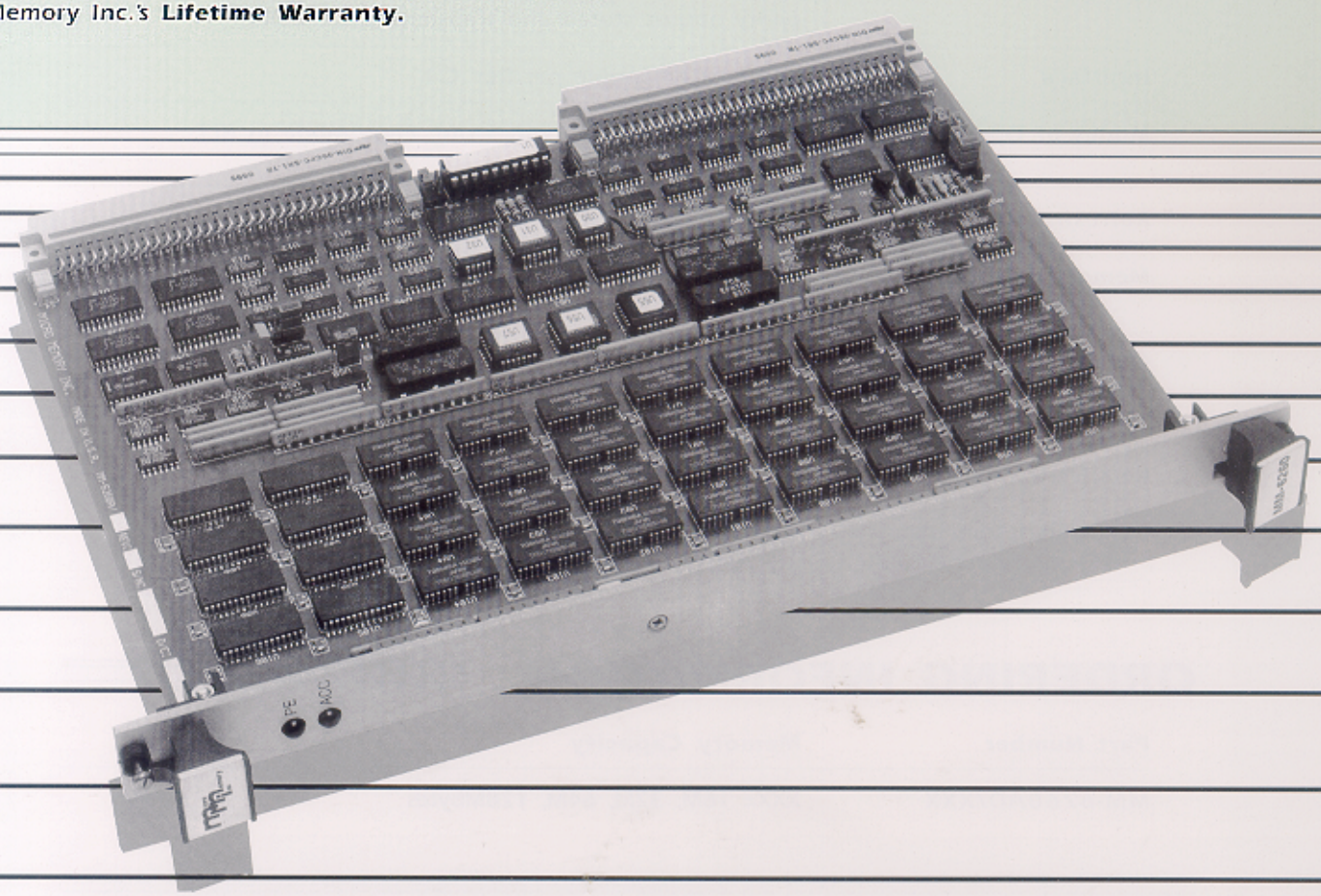
The MM-6260AD supports A32/A24, D32/D16/D8. Module selection is on 1 Mbyte boundaries, with switch-selectable upper and lower limits. Address modifier codes are programmed into a socketed PAL, which allows A32 or A24 masters to access memory when residing on the same bus. The MM-6260AD employes Block Transfer (BLT) and Page Modes.

Parity generation and checking are provided for each byte. Parity status is stored and transmitted via BERR*. Asserting BERR* inhibits the DTACK* signal during memory read operations.

Reliability is ensured by burn-in and running memory diagnostics that check operation for 48 hours while temperature-cycling boards between 0 and 60°C. Also, reliability is backed up by Micro Memory Inc.'s **Lifetime Warranty.**

FEATURES;

- Capacity: 16M, 32M, 64M, 128M bytes.
- Cycle/Access Time (nsec):
  Block Transfer-BLT, Page Mode
  Read: 120/90, Write: 120/50
  Non Block Transfer, CACHE Hits Read: 175/135, Write: 175/70
- VMEbus-compatible per Rev C.1 A32/A24, D32/D16/D8 (UAT)
- Block Transfer (BLT) 256 bytes sequential access, expandable to 256 Kbytes (jumper-selectable)
- Parity generation and checking on each byte
- Module selection on 1 Mbyte boundaries, with switch-selectable lower and upper limits
- Lifetime Warranty on parts and labor



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MM-6260AD SPECIFICATIONS

Parameter	Specification
Capacity	16M, 32M, 64M, 128M bytes
Cycle/Access Time (nsec)	Read: 120/90, Write: 120/50 (BLT, Page Mode) Read: 175/135, Write: 175/70 (CACHE Hits, No BLT)
Address	A32/A24 address bits
Data In/Data Out	D32/D16/D8
Sequential Access	Block Transfer within 256 byte boundary (BLT), expandable to 256 Kbytes
Address Modifiers	6-bit, decoded by socketed IFL with the following codes: A24: 39, 3A, 3D and 3E A32: 09, 0A, 0D and 0E A24 with BLT: 3B, 3F A32 with BLT: 0B, 0F
Parity	Byte-wide parity generation and checking; parity output stored and transmitted via BERR*
Interface	VMEbus-compatible per Rev C.1
Modes of Operation	Read, write, read-modify-write, Page Mode in BLT, and refresh
Refresh	Hidden refresh, 200 nsec every 15µsec
Memory Selection	1 Mbyte boundaries, switch-selectable for the upper and lower limits
Operating Temperature	0 to +60˚C
Storage Temperature	-40 to +85˚C
Relative Humidity	Up to 95% without condensation
Power Requirements: +5V (Fully-populated)	Operate 2.8 Amp.

ORDERING INFORMATION

Part Number	Memory Capacity
MM-6260AD/XXX	XXX= 16M, 32M, 64M, 128Mbytes

Specifications are subject to change without notice. Micro Memory Inc. reserves the right to make changes to improve reliability, function, or design. Micro Memory Inc. does not assume any liability arising from application of this product.

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