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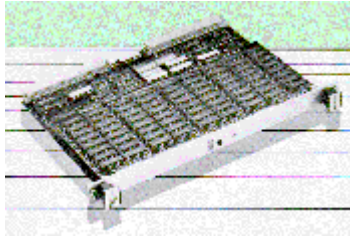
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## MM-6230 DATA SHEET

### A High Speed, 16MB, VMEbus Compatible DRAM Memory Module



#### FEATURES:

- **Capacity:** 4M, 8M, 12M, 16MB
- **Cycle Time:** 240 nsec
- **Read/Write Access:** 175 nsec/50 nsec (Fast Write)
- **VMEbus-compatible** with A32/A24, D32/D16/D8 (UAT)
- **Block mode transfers** for sequential access (BLT)
- **Parity** generation and checking for error detection on each byte
- **Module Selection** on 64KB boundaries, switch-selectable upper and lower limits in each of the 16MB banks
- **Six-bit address modifiers** support user and supervisory modes

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#### DESCRIPTION:

The MM-6230D main board is a high density memory module that provides 4M, 8M, 12M, or 16MB. The memory module is completely compatible with the VMEbus and supports A32/A24, D32/D16/D8, byte, word, long word, unaligned bus transfer (UAT) and Fast-Write cycles. It also supports sequential access block mode transfers (BLT) of 256 bytes.

Module selection is on 64KB boundaries, whose upper and lower limits are switch-selectable. To simplify reprogramming, address modifiers are programmed and decoded in a socketed IFL.

Parity generation and checking are provided for each byte. Parity status is stored and transmitted via BERR\* signal. If the BERR\* signal is asserted, the DTACK\* signal is inhibited during memory read operations.

Reliability is ensured by burn-in and running memory diagnostics that check operation for 48 hours while temperature-cycling boards from 0 to 60°C.

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#### SPECIFICATIONS:

##### Capacity

4M, 8M, 12M, 16MB with parity

**Cycle Time**

240 nsec for Read or Write

**Access Time**

Read = 175 nsec, Write = 50 nsec (Fast Write)

**Address**

A32/A24 address bits

**Data-in/Data-out**

D32/D16/D8 bits (UAT)

**Sequential Access**

Block mode transfers within a 256 byte boundary (BLT)

**Address Modifiers**

6 bits, decoded by a socketed IFL to support user and supervisory modes

**Parity**

Byte-wide parity generation and checking; parity output stored and transmitted via BERR\*

**Interface**

VMEbus-compatible (48 mA drivers)

**Modes of Operation**

Read, Write, Read-modify-Write, and refresh

**Refresh**

Hidden refresh, 250 nsec cycle every 15 usec

**Memory Selection**

Switch-selectable upper and lower limits for 64KB boundaries

**Operating Temperature**

0 to 60°C

**Storage Temperature**

-40 to +85°C

**Relative Humidity**

Up to 95% without condensation

**Power Requirements: +5V (Fully-populated)**

Standby 2.7A

Operate 2.9A



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