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Find the *Curtiss-Wright / VMETRO / Micro Memory MM-6710C* at our website: *Click HERE*

MM-6710C

8 MBYTE, FLASH MEMORY, 8 MBYTE CMOS STATIC RAM OR EPROM MEMORY MODULE FOR VMEbus

A high-speed, static memory module for VMEbus applications, the MM-6710C accommodates either CMOS static RAM, flash memory, or EPROM devices/ The MM-6710C has two independent blocks, each of which accept DIP memory devices of the same type and configuration. Blocks can be any combination of CMOS static, flash memory, or EPROM devices.

The MM-6710C can accept either 256Kx8 or 512Kx8 flash memory chips, which provides up to 8 Mbytes of memory for each block. Capacity for CMOS static memory or EPROM is 8 Mbytes for each block.

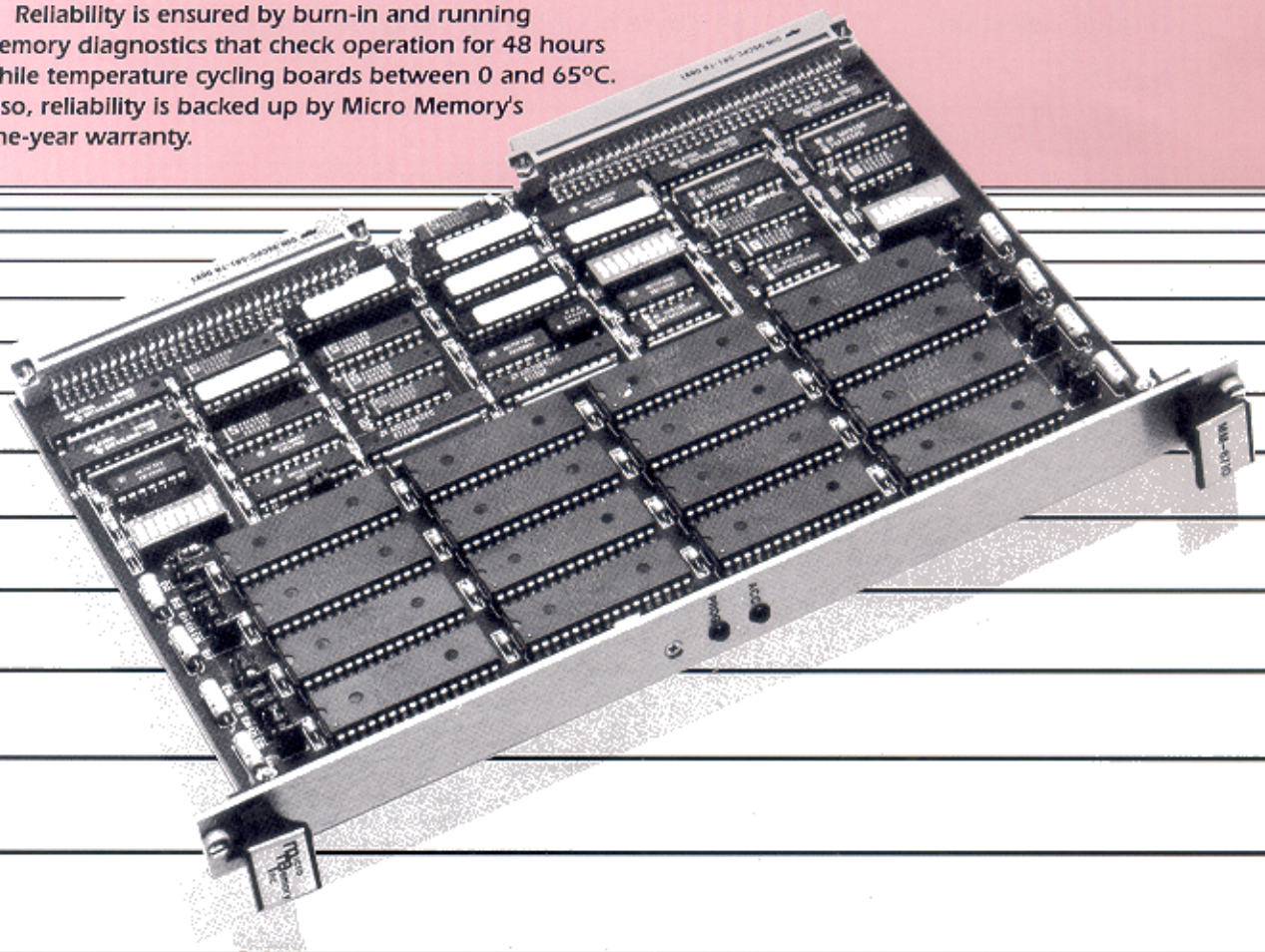
This memory module includes a flash memory software driver example. This software is provided on a floppy diskette along with a sample listing in"C" code. This driver enables the user to erase and program the flash memory.

The MM-6710C is completely compatible with VMEbus specification Rev C.1 with block transfer (BLT) of 256 bytes for each burst and unaligned data transfer (UAT).

Reliability is ensured by burn-in and running memory diagnostics that check operation for 48 hours while temperature cycling boards between 0 and 65°C. Also, reliability is backed up by Micro Memory's one-year warranty.

FEATURES:

- Capacity
  CMOS Static Ram: 1M, 2M, 4M, 8M bytes
  Flash Memory 2, 4, 8 bytes
- Two independent memory blocks allow mixing of CMOS static RAM, flash memory and EPROM of same configuration
- Cycle/Access Time:
  CMOS Static RAM: 150/100 nsec
  Flash Memory: 200/150 nsec
- Includes flash memory software driver example
- Compatible with VMEbus Rev C.1 A32/A24 D32/D16/D8 (UAT)
- Block transfer of 256 bytes for each burst
- Module selection of 64 Kbyte boundaries, switch-selectable
- **One Year Warranty**



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MM-6710C SPECIFICATIONS

Parameter	Specification
Capacity (Two Block Total)	CMOS Static RAM or EPROM: 1M, 2M, 4M, 8M bytes Flash Memory: 2M, 4M, 8M bytes
Cycle/Access Time	CMOS Static RAM: 150/100 nsec Flash Memory: 200/150 nsec
Memory Partition	Two independent blocks of eight 32-pin sockets for either flash memory, CMOS static RAM, or EPROM of the same type
Address	32 bits plus a 6-bit memory address modifier
Data In/Data Out	8, 16, 24, 32 bits (UAT) and block transfer (BLT) of 256 bytes for each burst
Modes of Operation	Read, write, read-modify-write
Module Selection	64 Kbyte boundaries, switch-selectable
Interface	VMEbus Rev C.1
Operating Temperature	0 to +65°C
Storage Temperature	−40 to +85°C
Relative Humidity	95% without condensation
Programming Power Requirement	+12V (on-board switch-controlled)
Operating Power Requirements:	Standby / Operate
+5V (Fully Populated)	1.6A / 1.8A
+12V (Programming)	— / 5mA

ORDERING INFORMATION

Part Number	Memory Capacity
MM-6710C/XX	No Memory Devices: XX=0
MM-6710F/XX	Flash Memory: XX=2M, 4M, 8M bytes
MM-6710C/XX	CMOS Static RAM: XX= 2M, 4M, 8M bytes

NOTE: Flash memory is always in Block A, unless otherwise specified.

Specifications are subject to change without notice. Micro Memory Inc. reserves the right to make changes to improve reliability, function, or design. Micro Memory Inc. does not assume any liability arising from applications of this product.
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