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Find the *Curtiss-Wright / VMETRO / Micro Memory MM-6704* at our website: ***Click HERE***

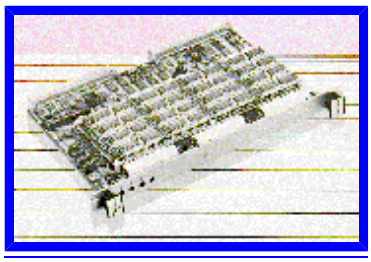
MM-6704 Data Sheet

A VMEbus Compatible, Non-volatile, High Speed Static Memory

FEATURES:

- **Capacity:** 512K to 4M bytes (expandable to 16 Mbytes)
- **Cycle Time:**
  150 nsec sustained rate (guaranteed over 25 Mbytes transfer rate for read or write cycles without refresh delays)
- **VMEbus compatible** with A32/A24/A16, D32/D16/D8 (UAT) per Revision C.1
- **Block transfer** for sequential access (BLT)
- **Module selection** on 64 Kbytes boundaries, jumper-selectable for starting and ending address
- **Four independent memory blocks**
  (each having independent, adjustable DTACK) allow mixing of RAMs and EPROMs of the same size
- **Three on-board,**
  redundant, fused batteries with battery charger, LED indicators and CSR for status reporting
- **Data Retention:** 2 years with Lithium batteries; 4 weeks with rechargeable NiCad batteries
- **On-board calendar/clock** counts secs, mins, hrs, days, date
- **One-year warranty**

DESCRIPTION:



The MM-6704 is a high speed, high density static CMOS non- volatile memory module. To maximize density, support devices are surface mounted. Memory devices are 128 Kbytes DIPs to provide the flexibility to mix static CMOS and EPROMs. The board occupies a single card slot. Built in to the board is future expansion to 16 Mbytes using 1 Mbit CMOS chips.

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The board is designed to be totally compatible with VMEbus specification REV. C1 with block transfer (BLT) of up to 256 bytes in each burst and unaligned data transfers (UAT).

CMOS and EPROM can be installed at the same time and a different DTACK* signal generated for each without penalizing either one. Write-protect switches allow disabling the write operation for selected portions of the CMOS RAM, which then appear as read-only memory.

SPECIFICATIONS:

Capacity
512K, 1M, 2M, 4M bytes (expandable to 16Mbytes with 1 Mbit CMOS chips)

Cycle/Access Time
150 nsec (25 Mbytes guaranteed transfer rate)

Address
32 bits + 6-bit Address Modifiers

Data-in/Data-out
8, 16, 32 bits (UAT)

Sequential Access
Block Transfer of 256 bytes (BLT)

Modes of Operation
Read, Write, Read-Modify-Write, Write-Protect

Module Selection
64 Kbytes, jumper-selectable by setting the start and ending addresses

Real Time Clock (DS12161)
Provides accurate time and date and occupies one location in the short I/O space (A16)

On-Board Batteries
3 redundant, high-temperature fused batteries; each battery has its own charger, voltage detector and LED display

Battery Types
NiCad (rechargeable) 100mA/hr, data retention 4 weeks (powered down)
Lithium (non-rechargeable) 1500 mA/hr, data retention 2 years (powered down)

Power Requirements
+5V supply @ 1.8A (fully populated with CMOS RAM)

Operating Temperature
0 to +60 degrees (Celsius)

Storage Temperature
-40 to +85 degrees (Celsius)

Relative Humidity

Up to 95% without condensation

ORDERING INFORMATION:

MM-6704CC/XXX (Rechargeable Ni-Cad batteries)

XXX = 512K, 1M, 2M, 4M bytes

MM-6704CN/XXX (Non-rechargeable Lithium batteries)

XXX = 512K, 1M, 2M, 4M bytes

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