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1GB 3U CompactPCI DRAM Memory Buffer

FEATURES:

- **Capacity:** 128MB, 256MB, 512MB and 1 GB
- **Transfer Rate:** Up to 132MB/s
- **cPCI DMA Initiator** for Bus Mastering
- **DMA Initiator and Target** capabilities necessary to maximize read and write transfers
- **Supports cPCI interrupts**
- **Multiple RAMDisk drivers** included for various Operating Systems
- **Compatible** with cPCI Local Bus Specification Rev. 2.2
- **Lifetime Warranty**

DESCRIPTION:

The MM-5100D is a 32-bit/33MHz 3U CompactPCI DRAM memory buffer capable of burst mode accesses at the full bandwidth of 32-bit/33MHz cPCI with transfer rates of up to 132 MB/s.

The MM-5100D is commonly used in telecommunications and industrial control applications. In many CompactPCI applications, the maximum speeds on the memory read cycles can only be achieved by using Push/Push technology. This is accomplished with our on-board DMA initiator. Various drivers are included (Windows NT, VxWorks, QNX) so that it can also be used as a high speed RamDisk. The card is commonly used as a large buffer in system applications involving high speed data acquisition.

Due to the cPCI bios, memory reads can tend to be significantly slower than writes in certain applications. The MM-5100D overcomes this obstacle because it is capable of being both a target cPCI memory and a cPCI DMA initiator. This feature allows you to set the DMA so the card will perform writes instead of reads to maximize throughput. The configuration space header of the MM-5100D returns a class code of 05h, indicating that it is a memory controller, and a sub-class code of 00h, indicating that it is a RAM

module.

Reliability is ensured by burn-in and running memory diagnostics that check operations for 48 hours while temperature-cycling boards from 0° to 70°C. Quality is guaranteed by Micro Memory's **Lifetime Warranty** .

SPECIFICATIONS:

Capacity

128MB, 256MB, 512MB, 1GB

Compatibility

cPCI Local Bus Specification Rev. 2.2

Transfer Rate: Up to 132 MB/s

Bus Mastering

DMA Initiator and Target capabilities

Interrupts

Supports cPCI Interrupts

Refresh

Fifteen refresh cycles occur every 225 microseconds, each requiring 240 nsec

Interface

cPCI bus compatible Rev 2.2

Operating Temperature

0° to 70°C

Storage Temperature

-40° to +85°C

Relative Humidity

Up to 95% without condensation

Power Requirements

+5V (fully populated) @ 2.4 Amps (operational)



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