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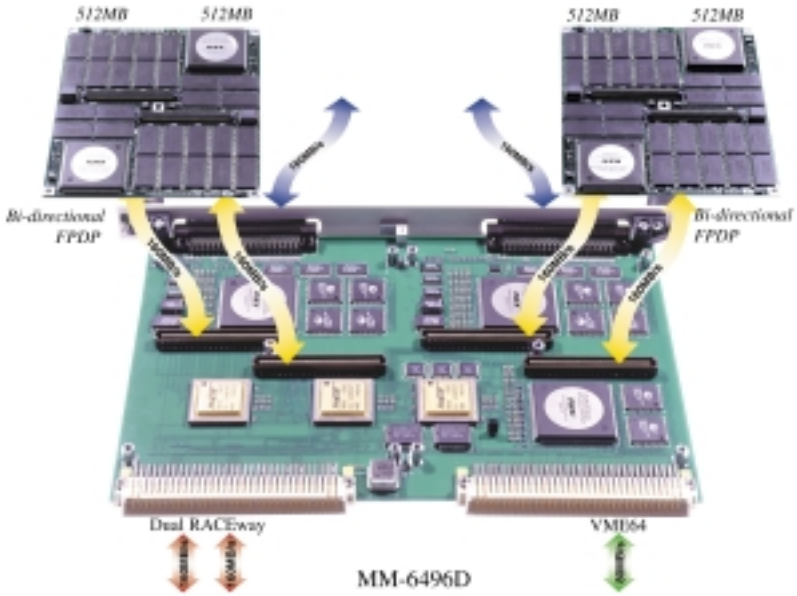
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Find the *Curtiss-Wright / VMETRO / Micro Memory MM-6496* at our website: ***Click HERE***

2GB Dual FPDP, Dual RACEway VME64 Memory Buffer with Direct FPDP to RACEway Interface

MM-6496D



FEATURES

- **Nine individual ports**
- **Direct FPDP to RACEway interface**
- **Full chain RACEway DMA Master to FPDP**
- **Capacity: 512MB, 1GB and 2GB (future expansion to 4 GB)**

- **Data Transfer Rates up to:**
  - **RACEwayA: 160MB/s**
  - **RACEwayB: 160MB/s**
  - **FPDP1: 160MB/s**
  - **FPDP2: 160MB/s**
  - **VME/VME64: 40/80MB/s**

The MM-6496D is a high-speed, nine port, single slot VME memory buffer that provides up to 2GB of DRAM for FPDP, VME and RACEway systems. Data from two independent FPDP ports and two independent RACEway ports can be directly transmitted or received from any one of four "non-busy" RACEway ports at 160MB/s with a combined data rate of 640MB/s.

In addition to having two FPDP ports and a VME port, the card has two RACEway ports on P2 and four RACEway ports on the face of the board. Each of these four ports has an individual memory bank with up to 512MB of DRAM. Each memory bank can be accessed simultaneously, but only through its dedicated RACEway port.

The MM-6496D has a direct FPDP to RACEway interface, 160MB/s to 160MB/s, with a full chain DMA. Because the interface is built into an FPGA there is no hardware integration required to configure the FPDP/RACEway connection. Loading a simple driver is the only software installation required to achieve the fastest possible FPDP to RACEway solution.

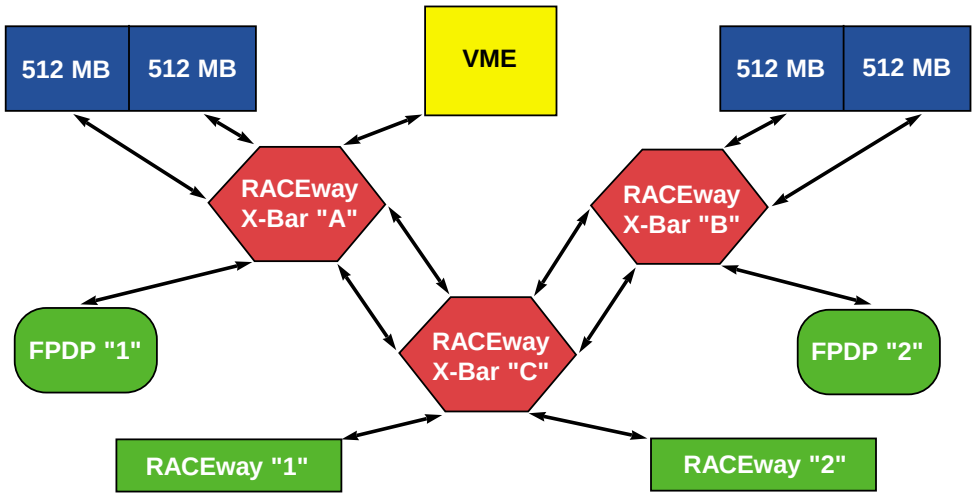
The two bi-directional FPDP ports can be configured as either two input ports, two output ports, or one input and one output port. Data can be transmitted or received from each FPDP port simultaneouly and the FPDP ports conform to FPDP/TM (Transmit Master), FPDP/RM (Receive Master), FPDP/R (receive) through jumper selection.

The FPDP ports are controlled by a full chain DMA in the RACEway interface and can be initialized by a host processor on either the VME bus or over the RACEway.

Users who desire access through FPDP and VME but do not require RACEway can still access each of the four memory banks through their dedicated RACEway ports without utilizing MCOS.

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MM-6496D



SPECIFICATIONS

Parameter	Specifications
Capacity	512MB, 1GB and 2GB (future expansion to 4GB)
Data Transfer Rate	VME: 40 MB/s VME64: 80 MB/s RACEway: 160 MB/s FPDP: 160 MB/s
Address	32-bits for VMEbus and RACEway
Data In/ Data Out	VMEbus: 8, 16, 32, 64 bits (UAT) multiplexed RACEway: 32 bits multiplexed FPDP: 32 bits
Sequential Access	Block Transfer (BLT) of 256 bytes (MBLT) 2 KB, and RACEway 2KB
CSR Selection	CSR selection is jumper selectable on any 256 byte boundary in the VME A16 address space. Module selection is software selectable on 1 MB boundaries (max 256MB)*
Interface	Compatible with VMEbus ANSI/VITA 1-1994, RACEway ANSI VITA 5-1994, and FPDP rev.1.7.
Memory Selection	CSR is jumper selectable on any 256 byte boundary in the VME A16 address space. The CSR controls the VME "window" onto RACEway fabric. The "window" is software selectable from one (1) to 256MB*
Relative Humidity	Up to 95% without condensation
Warranty	One year parts and service warranty
Power Requirements	TBD
Operating Temperature	0° C to +60° C
Storage Temperature	-40° C to +85° C

ORDERING INFORMATION

Part Number	Memory Capacity
MM-6496D/XXX	XXX=512M, 1G and 2G

* Module selection resolution must be greater than or equal to the window size: e.g. 64MB window requires module selection on 64MB boundary. e.e.g. 0,64,128, 192...

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