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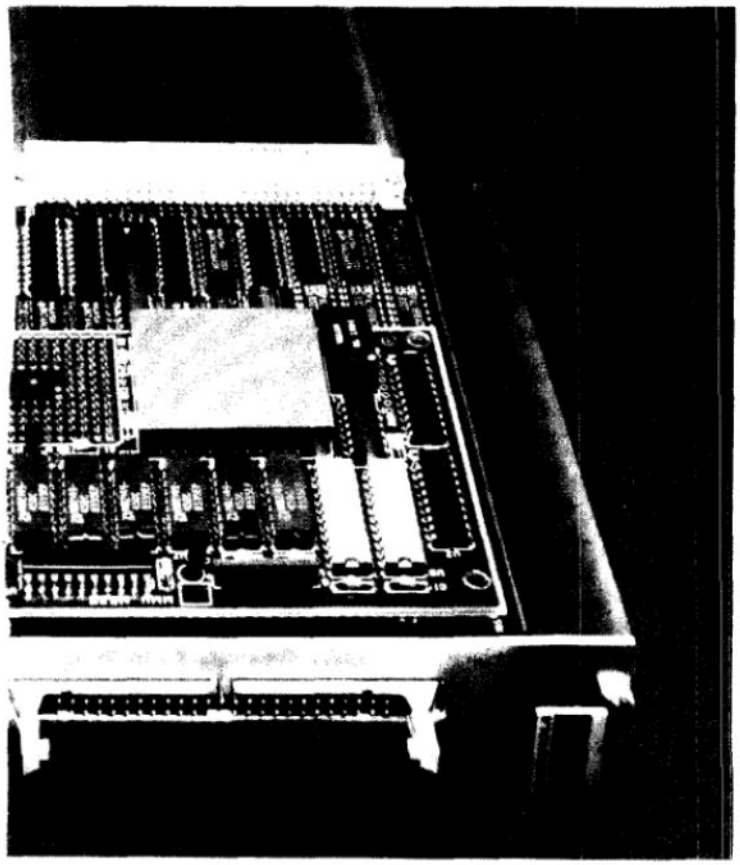
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ROM devices, 16 bi-directional parallel I/C lines, three off-board-accessible programmable 16-bit counter/timers, hexadecimal LED status display, and seven interrupt levels with bus arbitration. Designed to serve as a system controller (slot 1). **MVME101**

VMEbus Monoboard 8 MHz MC68000 Microcomputer with one serial port, includes 8 sockets for 2K to 32Kbyte RAM/ROM/EPROM devices, I/O Channel interface, three off-board-accessible programmable 16-bit counter/timers, run/halt/fail LED status lights, and seven interrupt levels with bus arbitration. Designed to serve as a system controller (slot 1). **MVME110-1**

MC68010 Monoboard Microcomputer with 512 Kbytes zero-wait-state RAM, battery backed up clock/calendar, four RAM/ROM sockets, socket for MC68881, SCSIbus interface **MVME117**

VMEbus Processor Module with 10 MHz MC68010 CPU, MC68451 Memory Management Unit 4Kb zero wait state CACHE Memory, 128Kb dual-ported Dynamic RAM with byte parity, one serial port, three 16-bit timers, and two sockets for 2K × 8 to 64K × 8 byte EPROM (user provided). MVME025, MVME050, or other VMEbus System Controller required. **MVME120**

Same as MVME120 except 512Kb Dynamic RAM. **MVME121**

VMEbus Processor Module with 12.5 MHz MC68010 CPU, 128Kb of dual-ported Dynamic RAM, three 16-bit timers, and two sockets for 2K × 8 to 64K × 8 bytes EPROM (user provided). MVME025, MVME050, or other VMEbus System Controller required. **MVME122**

Same as MVME122 except 4Kb zero wait state CACHE memory and 512Kb dual-port Dynamic RAM. **MVME123**

VMEbus Monoboard 12.5 MHz MC68020 Microcomputer with socket for MC68881 Floating Point Coprocessor (user provided), socket for Demand Page Virtual Memory Module (user provided), two 28-pin sockets for up to 16Kb ROM/EPROM Memory (user provided), MVMX32bus private memory bus interface, dual multiprotocol serial I/O ports, and a programmable timer module. **MVME130**

Same as MVME130 except Hardware Memory Management Module included. **MVME131**

VMEbus 512Kb Dynamic RAM Memory Module featuring byte parity checking and user selectable memory map assignment. **MVME202**

VMEbus 1Mb Dynamic Memory Module with MVMX32bus interface, dual-ported, interleaved, byte parity and error checking. Designed to be used with MVME130/131. **MVME204**

512 Kbytes Dynamic Memory Module featuring 32-bit data and address bus. **MVME205**

256Kbyte CMOS Static RAM Module with battery back-up. **MVME215**

Same as MVME202 except 1Mb Dynamic RAM. **MVME222-1**

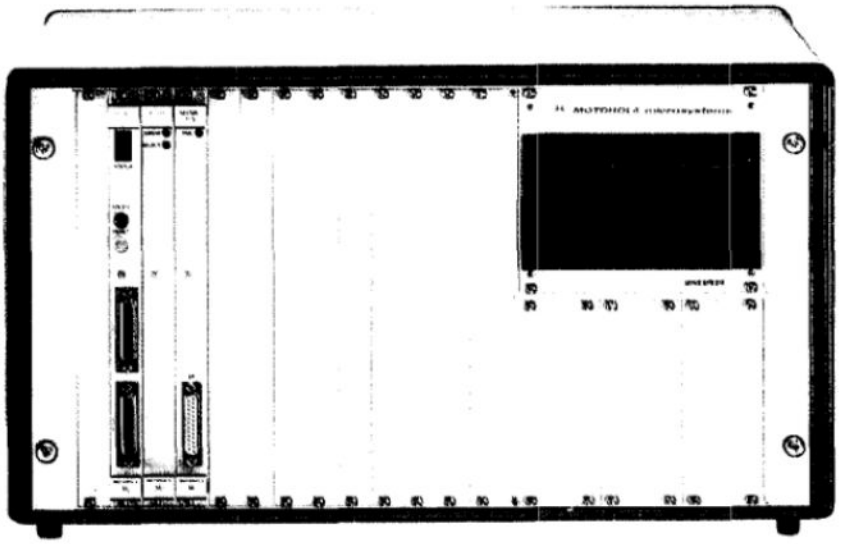
Same as MVME202 except 2Mb Dynamic RAM. **MVME222-2**

Same as MVME205 but with 2 Mbytes RAM. **MVME225**

RAM/ROM Memory Modules

VMEbus Static RAM/ROM Module for added memory mapped program (up to 1Mb); provides

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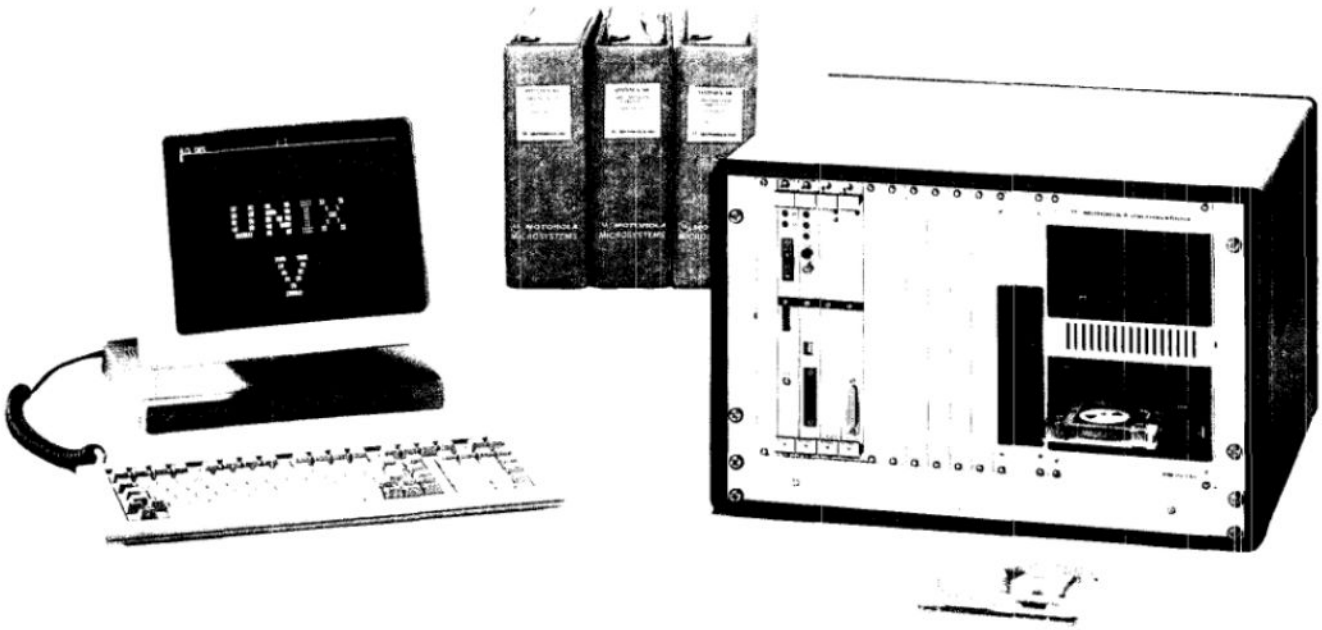


VME 319 Modular System

The MVME319 system is a highly modular, ready-to-use open system for VMEbus system development. A set of three cards is mounted on the 9-slot VMEbus backplane, leaving 6 slots free for the user. VERSAdos is resident on the Winchester disk. When additional VMEbus modules are added by the user, VERSAdos can be reconfigured using the SYSGEN utility. User programs can be generated using a terminal-independent screen editor and structured macro-assembler for MC68000, MC68008, MC68010. Optional High Level Languages available are Pascal and FORTRAN.

VMEbus Modular System with SYSTEM V/68 (UNIX)

The MVME121 System is a powerful 3-user UNIX environment for software development and hardware integration.
Fast execution speeds are achieved using an MC68010 microprocessor, large onboard memory and cache. The intelligent mass-storage controller allows the operating system to make enhanced use of system resources.
The SYSTEM V/68 Operating System is the standard M68000 implementation of UNIX System V, jointly developed by Motorola and A. T. & T.
The hardware is configured using standard components for easy customization by the user to adapt it to specific applications.



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