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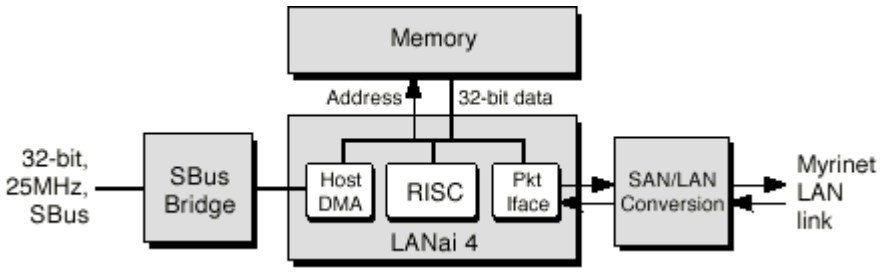
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Find the *Myricom M2F-SBus32B* at our website: *Click HERE*

# M2F-SBus32C

*Myrinet-LAN/SBus interface*




*Block Diagram*

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## Specifications

**SBus Interface:** 32-bit, 25MHz, master and slave, fully compliant with SBus specifications (IEEE Std 1496), supports all SBus burst-transfer modes. Sustained, large-block, DMA performance on an UltraSPARC (25MHz SBus) is 66 MB/s to system memory and 44 MB/s from system memory.

**Interface processor:** LANai-4 RISC operating at 1.5x the SBus clock rate (37.5MHz on a 25MHz SBus).

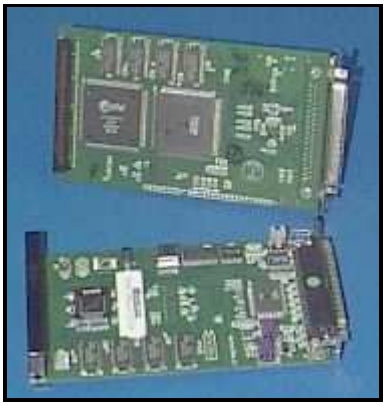
**Interface memory:** 1MB (256Kx4B). The interface memory operates at 3x the SBus clock rate. With a 25MHz SBus clock, 300 MB/s memory bandwidth is available to support the Myrinet link, the host DMA, and the RISC processor.

**Myrinet-LAN port:** 1.28+1.28 Gb/s or 0.64+0.64 Gb/s data rates selected automatically by the cable type. LVDS or PECL signal levels selected automatically (LVDS only at the 1.28+1.28 Gb/s data rate).

**Physical characteristics:** Single-width SBus board, ~5 Watts.

**Myricom-supported software:** Both GM and MyriAPI software, which can be downloaded from the Software & Customer Support page are supported under Solaris on UltraSPARCs. Only the MyriAPI software is availablue under SunOS.

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*M2F-SBus32C (two boards, to show both sides).*

*Last updated: 1 July 1999*

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