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Find the *Nallatech BenBlue-II* at our website: *Click HERE*

Dual Virtex-II Module
BenBLUE™-II



Format
Single width DIME-II

FPGA onboard
Virtex-II

Onboard FPGA options
2V4000-4, 2V4000-5, 2V4000-6, 2V6000-4, 2V6000-5, 2V6000-6, 2V8000-4, 2V8000-5

Memory Onboard
8 Mbytes ZBT SRAM

Overview

The BenBLUE-II is the ideal solution for systems requiring high performance density and digital I/O. Combining two FPGAs, digital I/O and SRAM, the module provides processing and memory expansion capability for Nallatech cPCI, VME, PCI and PC/104+ COTS FPGA computing systems. The BenBLUE-II provides the maximum available logic density using Virtex-II technology, closely coupled to high-speed SRAM. The provision of the high-bandwidth digital I/O header offers a means off-module data communication to sensors and other systems. This functionality is combined in a proven COTS solution, designed for ease of use, low risk and staighforward system integration/in-field deployment.

Key Features

- Expansion module for Nallatech cPCI, VME, PCI and PC/104+ COTS FPGA computing systems
- Two on-board Xilinx Virtex-II FPGAs
- FPGA device sizes available: 2V4000, 2V6000 or 2V8000
- Up to 210k logic cells of programmable logic per module
- Up to 336 Block RAMs and embedded multipliers per module
- 8 MBytes of ZBT SRAM in two independent banks
- ZBT SRAM interfacing IP Core
- SRAM Data Rate of 133MHz
- Maximum memory bandwidth of 1 GBytes/second
- 80-way digital I/O header for LVTTL or LVDS communication
- Supported in Nallatech FUSE software
- Compatible with Nallatech DIMEtalk
- Multiple design-entry flows supported
- Supports industry-standard synthesis and implementation tools
- Includes open-source example designs

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