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Find the *Nallatech BenDATA-II* at our website: ***Click HERE***

Virtex-II Pro, SRAM and DDR SDRAM Module
BenDATA™-II



Format
Single width DIME-II

FPGA onboard
Virtex-II Pro

Memory Type
ZBT SRAM, DDR SDRAM

Memory Onboard
16 Mbytes ZBT SRAM, 256 MBytes DDR SDRAM

Overview

For processing applications requiring the combination of different memory types and high-density FPGA, the BenDATA-II is the perfect solution. Combining a Virtex-II Pro FPGA, 2 banks of SRAM and 2 banks of DDR SDRAM, the module provides processing and memory expansion capability for Nallatech cPCI, VME, PCI and PC/104+ COTS FPGA computing systems. The combination of high-density DDR SDRAM and rapid, random access ZBT SRAM offers maximum flexibility for application implementation. This functionality is combined in a proven COTS solution, designed for ease of use, low risk and staighforward system integration/in-field deployment.

Key Features

- Expansion module for Nallatech cPCI, VME, PCI and PC/104+ COTS FPGA computing systems
- On-board Xilinx Virtex-II Pro FPGA
- FPGA device sizes available: 2VP70 or 2VP100
- Up to 100k logic cells of programmable logic per module
- Up to 444 Block RAMs and 444 embedded multipliers per module
- 16 MBytes of ZBT SRAM in two independent banks
- ZBT SRAM interfacing IP Core
- SRAM Data Rate of 166MHz
- Maximum SRAM bandwidth of 2.66 GBytes/second
- 256 MBytes of DDR SDRAM in two independent banks
- DDR SDRAM interfacing IP Core
- DDR SDRAM Data Rate of 400MHz
- Maximum DDR SDRAM bandwidth of 3.2 GBytes/second
- 8 external 2.5 Gbit/s RocketIO links
- Supported in Nallatech FUSE software
- Compatible with Nallatech DIMEtalk
- Multiple design-entry flows supported

- Supports industry-standard synthesis and implementation tools
- Includes open-source example designs

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