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Find the *Nallatech Benera* at our website: *Click HERE*

The Benera™
Compact PCI DIME-II™ Motherboard

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Company

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The Reconfigurable Computing Platform taking FPGA Systems to TeraOPS Performance. The Nallatech DIME-II module standard has been developed to provide ultra high performance support for the very latest range of Xilinx Virtex FPGAs. Designed with flexibility in mind, DIME-II has wide I/O communications buses for highly parallel interconnectivity. Along with the Benera motherboard, Nallatech also offer a range of DIME-II modules.

The Benera Compact PCI DIME-II motherboard has been designed for high performance applications, utilising the latest FPGAs from Xilinx along with the flexibility and scalability of DIME-II. Features include a Xilinx Virtex FPGA and a Virtex-E FPGA, along with support for up to four DIME-II modules. The board is supplied with PCI and board control firmware, which is contained in the on-board Virtex FPGA. The larger Virtex-E FPGA is available entirely for users' own designs. Flexible clocking options and the plug'n'play ability of The Benera makes it an ideal platform for electronic systems in many application areas, including DSP, communications, real-time systems and image processing.

Software & Configuration
The Benera comes supplied with Nallatech's PCI control core providing an interface between the host and the board, abstracting this functionality and allowing users to be up and running quickly. The Nallatech plug'n'play Multiple Configuration software allows rapid configuration of both the on-board and DIME-II module Virtex FPGAs. As a result, users can have their design configured and running in a matter of minutes.

Product Highlights

- Highly parallel architecture
- Over 400 Module to Motherboard User definable I/O
- CPCI for easy integration into existing systems
- On-board User Virtex-E FPGA with 1M-2M system gates (Virtex 1000E/2000E)
- Virtex-II support for User FPGA
- Four DIME-II module slots
- 3 Programmable Clock Domains
- 16 Mbytes Flash RAM for standalone operation
- Dynamic Reconfiguration of FPGAs, either locally or remotely via TCP/IP
- Plug & Play FPGA configuration via JTAG or SelectMap interface
- Multiple Operating System Support: Windows 9x/NT/2000, Linux & VxWorks

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PCI Functionality

- 64 bit/33 MHz PCI Bus
- PCI Master DMA Engine capable of up to 266Mbytes/sec burst transfers
- Two separate FIFO interfaces between the PCI master DMA engine and the Virtex

Specification

- FG680 Virtex Control FPGA
- FG860 Virtex-E User FPGA
- 16 MByte Flash RAM
- 3 Programmable Clock sources

- Supports 3.3V and 5V PCI
- High Speed PnP FPGA reconfiguration from software
- Upgradeable Firmware over TCP/IP
- Status LED Control
- 4 DIME-II Module slots
- Direct connections to User FPGA via backplane connectors

The Benera kit contents
The Benera is supplied as standard with the following:

- Control FPGA pre-loaded with PCI Firmware
- Windows 95/98/NT/2000 drivers
- Linux Drivers
- High Speed Plug'n'Play Virtex configuration software
- Software utility to read/write data to FPGA
- C/C++ library to interface/transfer data/control Benera through software
- Multiple Example Designs

Download Information Sheet(456Kb)

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