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PXI Express

NI PXIe-8101/8102 User Manual

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Electromagnetic Compatibility Information

This hardware has been tested and found to comply with the applicable regulatory requirements and limits for electromagnetic compatibility (EMC) as indicated in the hardware's Declaration of Conformity (DoC)[1]. These requirements and limits are designed to provide reasonable protection against harmful interference when the hardware is operated in the intended electromagnetic environment. In special cases, for example when either highly sensitive or noisy hardware is being used in close proximity, additional mitigation measures may have to be employed to minimize the potential for electromagnetic interference.

While this hardware is compliant with the applicable regulatory EMC requirements, there is no guarantee that interference will not occur in a particular installation. To minimize the potential for the hardware to cause interference to radio and television reception or to experience unacceptable performance degradation, install and use this hardware in strict accordance with the instructions in the hardware documentation and the DoC[1].

If this hardware does cause interference with licensed radio communications services or other nearby electronics, which can be determined by turning the hardware off and on, you are encouraged to try to correct the interference by one or more of the following measures:

- Reorient the antenna of the receiver (the device suffering interference).
- Relocate the transmitter (the device generating interference) with respect to the receiver.
- Plug the transmitter into a different outlet so that the transmitter and the receiver are on different branch circuits.

Some hardware may require the use of a metal, shielded enclosure (windowless version) to meet the EMC requirements for special EMC environments such as, for marine use or in heavy industrial areas. Refer to the hardware's user documentation and the DoC[1] for product installation requirements.

When the hardware is connected to a test object or to test leads, the system may become more sensitive to disturbances or may cause interference in the local electromagnetic environment.

Operation of this hardware in a residential area is likely to cause harmful interference. Users are required to correct the interference at their own expense or cease operation of the hardware.

Changes or modifications not expressly approved by National Instruments could void the user's right to operate the hardware under the local regulatory rules.

[1] The Declaration of Conformity (DoC) contains important EMC compliance information and instructions for the user or installer. To obtain the DoC for this product, visit `ni.com/certification`, search by model number or product line, and click the appropriate link in the Certification column.

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About This Manual

This manual contains detailed instructions for installing and configuring the National Instruments PXIe-8101/8102 embedded computer kit.

How to Use the Documentation Set

Begin by reading the *NI PXIe-8101/8102 Installation Guide*, a brief quick-start guide that describes how to install and get started with your controller.

This manual, the *NI PXIe-8101/8102 User Manual*, contains more details about changing the installation or configuration from the defaults and using the hardware.

Conventions

The following conventions appear in this manual:

» The » symbol leads you through nested menu items and dialog box options to a final action. The sequence **File»Page Setup»Options** directs you to pull down the **File** menu, select the **Page Setup** item, and select **Options** from the last dialog box.



This icon denotes a tip, which alerts you to advisory information.



This icon denotes a note, which alerts you to important information.



This icon denotes a caution, which advises you of precautions to take to avoid injury, data loss, or a system crash.

bold Bold text denotes items that you must select or click in the software, such as menu items and dialog box options. Bold text also denotes parameter names.

italic Italic text denotes variables, emphasis, a cross-reference, or an introduction to a key concept. Italic text also denotes text that is a placeholder for a word or value that you must supply.

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`monospace`	Text in this font denotes text or characters that you should enter from the keyboard, sections of code, programming examples, and syntax examples. This font is also used for the proper names of disk drives, paths, directories, programs, subprograms, subroutines, device names, functions, operations, variables, filenames, and extensions.
`monospace bold`	Bold text in this font denotes the messages and responses that the computer automatically prints to the screen. This font also emphasizes lines of code that are different from the other examples.

Related Documentation

The following documents contain information you may find helpful as you read this manual:

- *PICMG EXP.0 R1.0 CompactPCI Express Specification*, PCI Industrial Computers Manufacturers Group
- IEEE Standard P1284.1-1997 *(C/MM) Standard for Information Technology for Transport Independent Printer/System Interface*
- *PCI Express Base Specification*, Revision 2.0, PCI Special Interest Group
- *PXI-5 PXI Express Hardware Specification*, Revision 1.0, PXI Systems Alliance
- *PXI-6 PXI Express Software Specification*, Revision 1.0, PXI Systems Alliance
- *Serialized IRQ Support for PCI Systems Specification*, Revision 6.0, Compaq Computer et al.

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1 Introduction

This chapter provides overview information for PXI Express and the NI PXIe-8101/8102 embedded controller.

Benefits of PXI Express

The PXI (PCI eXtensions for Instrumentation) industry standard, an open specification governed by the PXI Systems Alliance (PXISA), has quickly gained adoption and grown in prevalence in test, measurement, and control systems since its release in 1998. One of the key elements driving the rapid adoption of PXI is its use of PCI in the communication backplane. As the commercial PC industry has improved the available bus bandwidth by evolving PCI to PCI Express, PXI is now able to meet even more application needs by integrating PCI Express into the PXI standard. By taking advantage of PCI Express technology in the backplane, PXI Express increases the maximum PXI bandwidth from up to 132 MB/s to up to 6 GB/s for a more than 45x improvement in bandwidth.

PXI Express maximizes both hardware and software compatibility with PXI modules. PXI Express hybrid slots deliver both PCI and PCI Express signaling to accept devices that use PXI communication and triggering or the newer PXI Express standard. Software compatibility is maintained because PCI Express uses the same OS and driver model as PCI, resulting in complete software compatibility among PCI-based systems, for example PXI, and PCI Express-based systems such as PXI Express.

PXI Express, like PXI, leverages from the CompactPCI specification to define a rugged, modular form factor that offers superior mechanical integrity and easy installation and removal of hardware components. PXI Express products offer higher and more carefully defined levels of environmental performance required by the shock, vibration, temperature, and humidity extremes of industrial environments. Mandatory environmental testing and active cooling is added to the CompactPCI mechanical specification to ease system integration and ensure multivendor interoperability.

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The demanding timing and synchronization requirements of instrumentation systems are met by the integrated features of PXI Express. Not only are the trigger bus, 10 MHz system reference clock, and star trigger bus available in PXI retained by PXI Express, but new timing and synchronization features that include a 100 MHz differential system reference clock for the synchronization of multiple modules and three differential star trigger buses for the distribution of precise clock and trigger signals have been added. Differential timing and synchronization signals provide PXI Express systems with increased noise immunity and the ability to transmit clock signals at higher frequencies.

NI PXIe-8101/8102

Description

The NI PXIe-8101/8102 PXI Express/CompactPCI Express embedded computer is a high performance PXI Express/CompactPCI Express-compatible system controller. The NI PXIe-8101/8102 controller integrates standard I/O features in a single unit by using state-of-the-art packaging. Combining an NI PXIe-8101/8102 embedded controller with a PXI Express-compatible chassis, such as the PXIe-1062Q, results in a fully PC-compatible computer in a compact, rugged package.

The NI PXIe-8101 has an Intel® Celeron 575 processor (2.00 GHz single core processor) with all the standard I/O and a 80 GB (or larger) hard drive. The NI PXIe-8102 has an Intel® Celeron Dual-Core T3100 processor (1.9 GHz) with all the standard I/O and a 80 GB (or larger) hard drive.

The standard I/O on each module includes DVI-I (Digital Video Interface Integrated Analog/Digital) video, one RS-232 serial port, a parallel port, two Hi-Speed USB ports, Gigabit Ethernet, a reset button, and a PXI trigger.

Functional Overview

This section contains functional descriptions of each major logic block on the NI PXIe-8101/8102 embedded computer.

NI PXIe-8101/8102 Functional Description

The NI PXIe-8101/8102 is a modular PC in a PXI Express 3U-size form factor. Figure 1-1 is a functional block diagram of the NI PXIe-8101/8102. Following the diagram is a description of each logic block shown.

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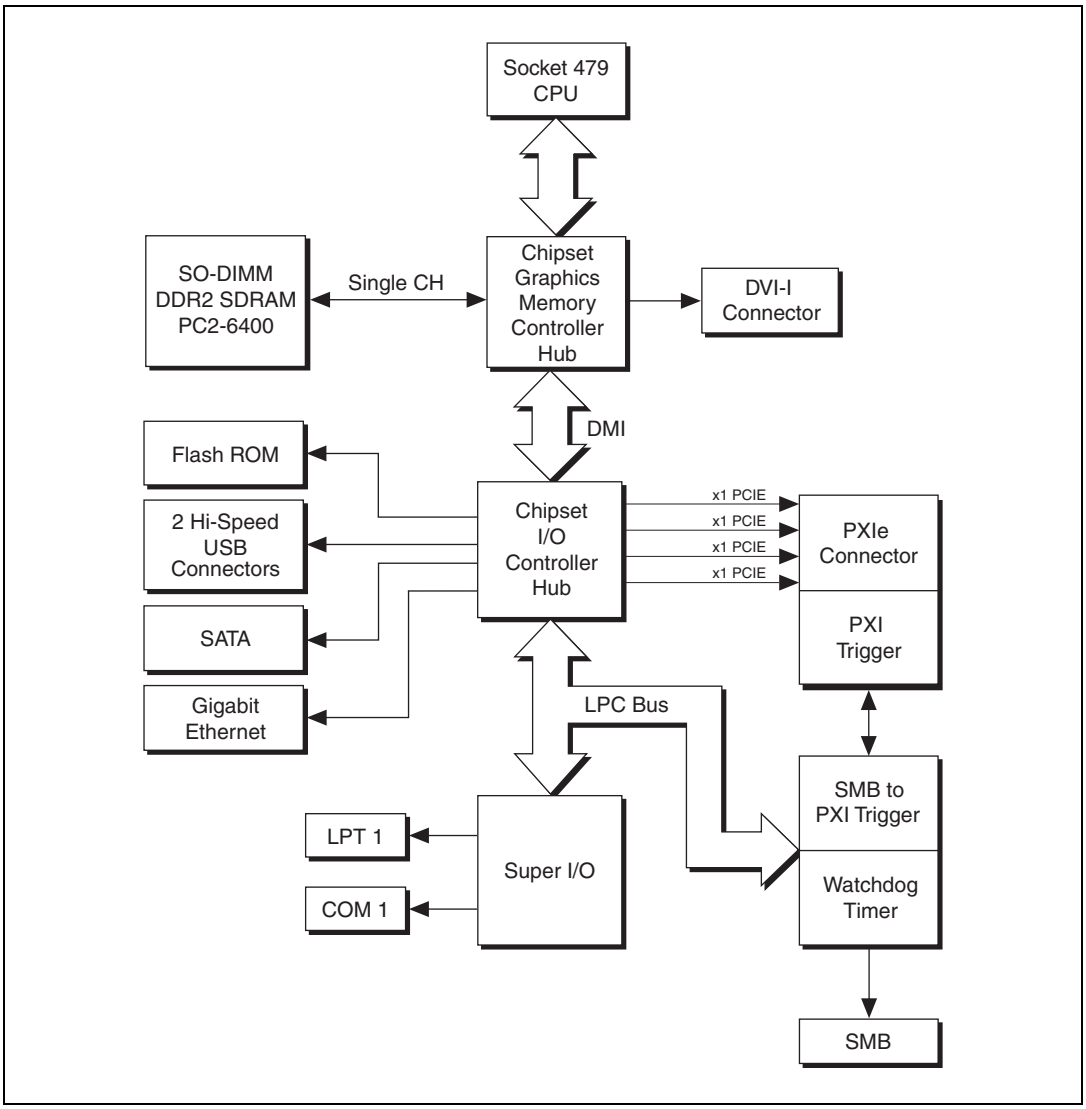


Figure 1-1. NI PXIe-8101/8102 Block Diagram

The NI PXIe-8101/8102 consists of the following logic blocks on the CPU module. The CPU module has the following logic blocks:

- *Socket 479 CPU* is the socket definition for the Intel® Celeron® 575 and Intel® Dual-Core T3100 processors.
- The *SO-DIMM* block consists of one 64-bit DDR2 SDRAM socket that can hold up to 4 GB.

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- The *Mobile Intel GM45 Express Chipset* (Graphics and Memory Controller Hub) connects to the CPU, DDR2 SDRAM, and DVI-I video.
- The *SMB to PXI Trigger* provides a routable connection of the PXI triggers to/from the SMB on the front panel.
- The *Watchdog Timer* block consists of a watchdog timer that can reset the controller or generate a trigger.
- The *Chipset ICH9M* (I/O Controller Hub) connects to the PCI, USB, SATA, LPC buses, and Ethernet.
- The *USB Connectors* are connected to the ICH9M chipset.
- The *PXI Express Connector* connects the NI PXIe-8101/8102 to the PXI Express/CompactPCI Express backplane.
- The *Super I/O* block represents the other peripherals supplied by the NI PXIe-8101/8102. The NI PXIe-8101/8102 has one serial port, and an ECP/EPP parallel port.
- The *Gigabit Ethernet* connects to either 10 Mbit, 100 Mbit, or 1,000 Mbit Ethernet interfaces.
- The *SATA* block connects a Serial ATA hard drive to the ICH9M.

National Instruments Software

National Instruments has developed several software tools you can use with the NI PXIe-8101/8102.

National Instruments' hardware and software work together to help you make the most of your PXI Express system. The LabVIEW, Measurement Studio, and LabWindows™/CVI™ application development environments combine with leading hardware drivers such as NI-DAQmx to provide exceptional control of NI hardware. Instrument drivers are available at `ni.com/idnet` to simplify communication with instruments over a variety of buses.

LabVIEW is a powerful and easy-to-use graphical programming environment you can use to acquire data from thousands of different instruments including USB, VXI, serial, PLCs, and plug-in boards. LabVIEW helps you convert acquired data into meaningful results using powerful data analysis routines. Add-on tools provide additional specialized functionality. For more information visit `ni.com/labview` and `ni.com/toolkits`.

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If you prefer to use Microsoft's Visual Basic, Visual C++, and Visual Studio .NET for the core of your application, Measurement Studio adds tools for Measurement and Automation to each language. For more information visit `ni.com/mstudio`.

LabWindows/CVI is an interactive ANSI C programming environment designed for building virtual instrument applications. LabWindows/CVI delivers a drag-and-drop editor for building user interfaces, a complete ANSI C environment for building your test program logic, and a collection of automated code generation tools, as well as utilities for building automated test systems, monitoring applications, or laboratory experiments. For more information visit `ni.com/lwcvi`.

NI-DAQmx provides an extensive library of functions that you can call from your application development environment or interactive environment such as NI Signal Express. These functions provide an intuitive API for National Instruments' multifunction DAQ products. Features available include analog input (A/D conversion), buffered data acquisition (high-speed A/D conversion), analog output (D/A conversion), waveform generation, digital I/O, counter/timer operations, SCXI signal conditioning, RTSI or PXI synchronization, self-calibration, messaging, and acquiring data to extended memory. For more information visit `ni.com/daq`.

National Instruments' Modular Instruments use specialized drivers suited to each product's specialization. Express VIs provide customized, interactive programming of instruments in a single interface and soft front panels provide an interface for testing the functionality of each instrument with no programming required. NI Switches, DMMs, High-Speed DIO, High-Speed Digitizers, and Sources each have customized drivers for high-end modular instrumentation systems. RF applications leverage two drivers, NI-RFSG and NI-RFSA and Dynamic Signal Acquisition is available through NI-DAQmx. For more information visit `ni.com/modularinstruments`.

You can expand the timing and triggering functionality of your PXI system with PXI Timing and Synchronization products. These products provide precision clock sources, custom routing of triggers for multi-chassis synchronization, clock sharing, and more and are programmed with NI-Sync. For more information visit `ni.com/pxi`.

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NI-VISA is the National Instruments implementation of the VISA specification. VISA is a uniform API for communicating and controlling USB, Serial, PXI, VXI, and various other types of instruments. This API aids in the creation of portable applications and instrument drivers. For information on writing your own PXI instrument driver with NI-VISA, refer to the *NI-VISA Getting Started Manual* and the `readme.txt` file in the NI-VISA directory. For more information visit `ni.com/visa`.

With LabVIEW for Linux and support for over two hundred devices on Linux with the NI-DAQmx driver, you can now create Virtual Instruments based on the Linux OS. Instrument control in Linux has been improved by the NI-VISA driver for Linux and NI Modular Instruments are partially supported. For more information visit `ni.com/linux`.

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2

Installation and Configuration

This chapter contains information about installing and configuring your NI PXIe-8101/8102 controller.

Installing the NI PXIe-8101/8102

This section contains general installation instructions for the NI PXIe-8101/8102. Consult your PXI Express chassis user manual for specific instructions and warnings.

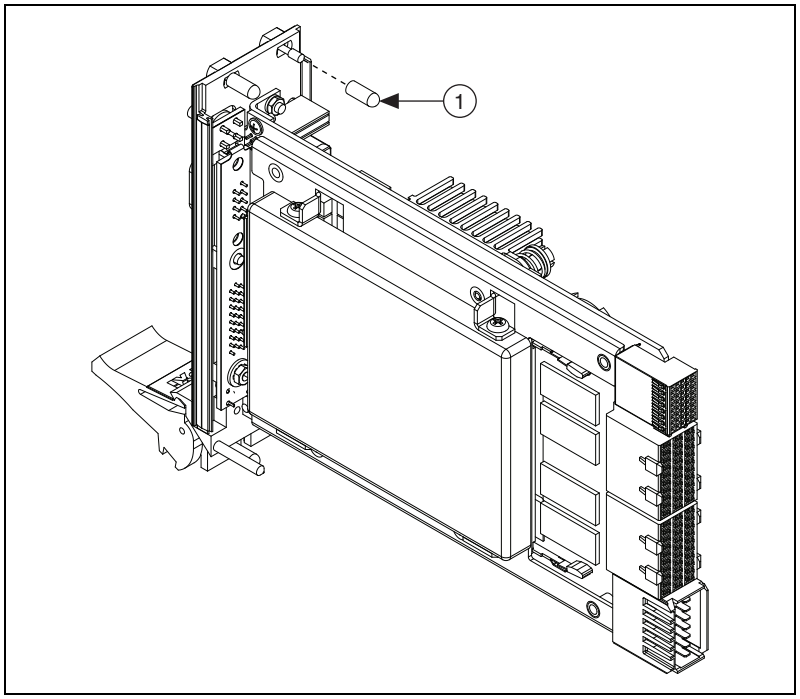
1. Plug in your chassis before installing the NI PXIe-8101/8102. The power cord grounds the chassis and protects it from electrical damage while you install the module. Make sure the power switch is turned off.

Caution To protect both yourself and the chassis from electrical hazards, leave the chassis powered off until you finish installing the NI PXIe-8101/8102 module.

2. Remove any filler panels blocking access to the system controller slot (slot 1) in the chassis.
3. Touch the metal part of the case to discharge any static electricity that might be on your clothes or body.

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4. Remove the protective plastic covers from the four bracket-retaining screws as shown in Figure 2-1.



1 Protective Screw Cap (4X)

Figure 2-1. Removing Protective Screw Caps

5. Make sure the injector/ejector handle is in its downward position. Align the NI PXIe-8101/8102 with the card guides on the top and bottom of the system controller slot.

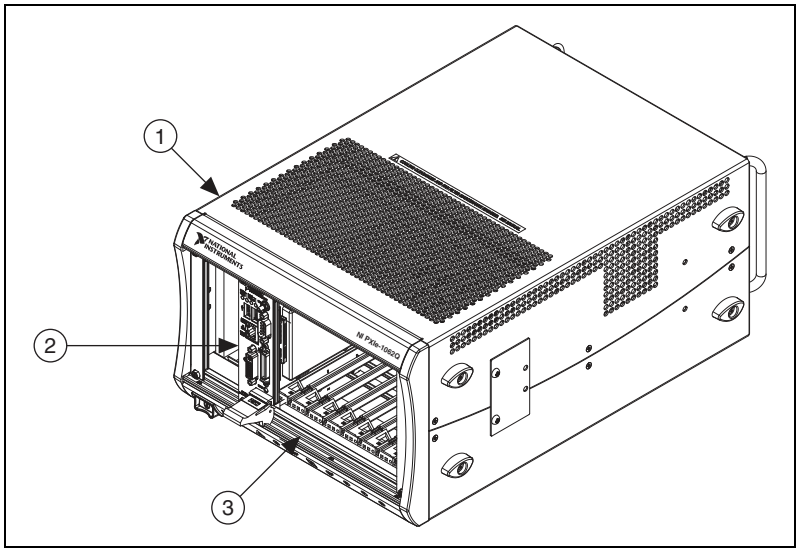
Caution Do *not* raise the injector/ejector handle as you insert the NI PXIe-8101/8102. The module will not insert properly unless the handle is in its downward position so that it does not interfere with the injector rail on the chassis.

6. Hold the handle as you slowly slide the module into the chassis until the handle catches on the injector/ejector rail.
7. Raise the injector/ejector handle until the module firmly seats into the backplane receptacle connectors. The front panel of the NI PXIe-8101/8102 should be even with the front panel of the chassis.
8. Tighten the four bracket-retaining screws on the top and bottom of the front panel to secure the NI PXIe-8101/8102 to the chassis.

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9. Check the installation.
10. Connect the keyboard and mouse to the appropriate connectors. If you are using a PS/2 keyboard and a PS/2 mouse, a Y-splitter adapter is available to connect both to a single USB connector. Refer to Figure 4-1, *Y-Splitter Cable*.
11. Connect the DVI-I monitor video cable to the DVI-I connector, or use the DVI-to-VGA adapter included with your controller to connect a VGA monitor to the DVI-I connector.
12. Connect devices to ports as required by your system configuration.
13. Power on the display device.
14. Power on the chassis.
15. Verify that the controller boots. If the controller does not boot, refer to the *What if the NI PXIe-8101/8102 does not boot?* section of Chapter 5, *Troubleshooting*.

Figure 2-2 shows an NI PXIe-8101 installed in the system controller slot of a National Instruments NI PXIe-1062 chassis. An NI PXIe-8102 would appear the same installed in the chassis.



1	NI PXIe-1062 Chassis	3	Injector/Ejector Rail
2	NI PXIe-8101 Controller		

Figure 2-2. NI PXIe-8101 Controller Installed in a PXI Express Chassis

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How to Remove the Controller from the PXI Express Chassis

The NI PXIe-8101/8102 controller is designed for easy handling. To remove the unit from the PXI Express chassis, complete the following steps.

1. Power off the chassis.
2. Remove any cables that may be attached to the controller front panel.
3. Unscrew the 4 bracket-retaining screws in the front panel. Refer to Figure 2-1 for the location of these screws.
4. Press the injector/ejector handle down.
5. Slide the unit out of the chassis.

Note If the PXIe chassis Inhibit Mode Selector Switch is not in the Default position, any attempt to shut down the NI PXIe-8101/8102 through the push button reset or using Windows will result in the controller Power OK LED blinking. The user will be required to use the Remote Inhibit pin on the Remote Inhibit and Voltage Monitoring Connector to turn off the chassis. Refer to the PXIe chassis user manual for details on the functionality of the Remote Inhibit and Voltage Monitoring controls.

BIOS Setup

You can change the NI PXIe-8101/8102 configuration settings in the BIOS setup program. The BIOS is the low-level interface between the hardware and operating system software that configures and tests your hardware when you boot the system. The BIOS setup program includes menus for configuring settings and enabling NI PXIe-8101/8102 controller features.

Most users do not need to use the BIOS setup program, as the NI PXIe-8101/8102 controller ships with default settings that work well for most configurations.

Caution Changing BIOS settings may lead to incorrect controller behavior and possibly an unbootable controller. If this happens, follow the instructions for restoring default settings in the *System CMOS* section. In general, do *not* change a setting unless you are absolutely certain what it does.

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Accessing BIOS Setup Utility

Complete the following steps to start the BIOS setup program.

1. Power on or reboot your NI PXIe-8101/8102 controller.
2. When the message **<DEL> = BIOS Setup Menu** appears, press the <Del> key. The setup program loads after a short delay.

   The **Main** menu is displayed when you first enter the BIOS setup program.

Use the following keys to navigate through the BIOS setup program:

- **Left Arrow, Right Arrow**—Use these keys to move between the different setup menus. If you are in a submenu, these keys have no effect, and you must press <Esc> to leave the submenu first. (To use the arrows on the numeric keypad, you must turn off Num Lock.)
- **Up Arrow, Down Arrow**—Use these keys to move between the options within a setup menu. (To use the arrows on the numeric keypad, you must turn off Num Lock.)
- **<Enter>**—Use this key either to enter a submenu or display all available settings for a highlighted configuration option.
- **<Esc>**—Use this key to return to the parent menu of a submenu. At the top-level menus, this key serves as a shortcut to the **Exit** menu.
- **<+> and <–>**—Use these keys to cycle between all available settings for a selected configuration option.
- **<Tab>**—Use this key to select time and date fields.
- **<F9>**—Use this key to load the optimal default values for BIOS configuration settings. The optimal default values are the same as the shipping configuration default values.

Main Setup Menu

The most commonly accessed and modified BIOS settings are in the **Main** setup menu. The **Main** setup menu reports the following configuration information:

- **BIOS Version and Build Date**—These values indicate the version of the NI PXIe-8101/8102 controller BIOS and the date on which the BIOS was built.
- **Processor Type, Speed, and Number of Cores**—These values indicate the type of processor used in the NI PXIe-8101/8102 controller, the speed of the processor, and the number of processor cores.

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- **System Memory**—This value indicates the size of system RAM detected by the BIOS.
- **Chassis Link Configuration**—This value displays the PXI Express link configuration, either **2-Link** or **4-Link**. If **2-Link** is displayed, the NI PXIe-8101/8102 detected a 2-link PXI Express backplane and is operating with two x1 PCI Express links. If **4-Link** is displayed, the NI PXIe-8101/8102 detected a 4-link PXI Express backplane and is operating with four x1 PCI Express links.

The **Main** setup menu also includes the following settings:

- **System Time**—This setting controls the time of day, which is stored in a battery-backed real-time clock. Most operating systems also include a way to change this setting. Use <+> and <–> in conjunction with <Enter> and <Tab> to change these values.
- **System Date**—This setting controls the date, which is stored in a battery-backed real-time clock. Most operating systems also include a way to change this setting. Use <+> and <–> in conjunction with <Enter> and <Tab> to change these values.

Advanced Setup Menu

This menu contains BIOS settings that normally do not require modification. If you have specific problems such as unbootable disks or resource conflicts, you may need to examine these settings.

Caution Changing settings in this menu may result in an unstable or unbootable controller. If this happens, follow the procedures outlined in the *System CMOS* section to restore BIOS settings to their factory defaults.

The **Advanced** setup menu includes the following settings and submenus:

- **SATA Configuration**—Use this setting to access the **SATA Configuration** submenu. Refer to the *SATA Configuration Submenu* section for more information.
- **CPU Configuration** (NI PXIe-8102 only)—Use this setting to access the **CPU Configuration** submenu. Refer to the *CPU Configuration Submenu (NI PXIe-8102 Only)* section for more information.
- **Video Configuration**—Use this setting to access the **Video Configuration** submenu. Refer to the *Video Configuration Submenu* section for more information.

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- **PCI Express Configuration**—Use this setting to access the **PCI Express Configuration** submenu. Refer to the *PCI Express Configuration Submenu* section for more information.
- **Power/Reset Configuration**—Use this setting to access the **Power/Reset Configuration** submenu. Refer to the *Power/Reset Configuration Submenu* section for more information.
- **USB Configuration**—Use this setting to access the **USB Configuration** submenu. Refer to the *USB Configuration Submenu* section for more information.
- **Serial/Parallel Port Configuration**—Use this setting to access the **Serial/Parallel Port Configuration** submenu. Refer to the *Serial/Parallel Port Configuration Submenu* section for more information.
- **Trigger Router Configuration**—Use this setting to access the **Trigger Router Configuration** submenu. Refer to the *Trigger Router Configuration Submenu* section for more information.

SATA Configuration Submenu

Use this submenu to apply alternate settings to the hard disk drive (HDD) interfaces. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **SATA Controller**—This setting specifies whether or not the onboard SATA controller is enabled or disabled. The default value is **Enabled**.
  - **SATA Mode Selection**—This setting determines whether AHCI mode is enabled or disabled for the SATA port. Some operating systems, such as Windows 2000, do not support AHCI mode. You can use this setting to disable AHCI mode so that non-compatible OSes function correctly. The default value is **AHCI**.
- **Serial ATA Port 0**—This item displays the onboard SATA drive detected in the system.

CPU Configuration Submenu (NI PXIe-8102 Only)

Use this submenu to apply alternate settings to the CPU. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **Core Multi-Processing Controller**—This setting specifies whether or not the second core of the processor is enabled or disabled. The default value is **Enabled**.

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- **Processor Type, Speed, and Number of Cores**—These values indicate the type of processor used in the NI PXIe-8101/8102 controller, the speed of the processor, and the number of processor cores.

Video Configuration Submenu

Use this submenu to apply alternate settings to the video configuration. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **Primary Display**—This setting specifies which video adapter the BIOS should use as the primary adapter if more than one is present. To use an external video adapter as the primary graphics adapter, choose **Add-in PCI Video**. The default value is **Onboard Video**.
- **Total Graphics Memory**—This setting specifies the amount of system memory to allocate as graphics memory for use by the onboard video device. The default value is **256 MB**.
- **DDC Routing**—This setting determines how the monitor DDC is routed. Use this setting to select whether or not the DDC is routed for an analog monitor or a DVI monitor. In order to use a DVI monitor, this setting must be set to **DVI**. An analog monitor, however, will function with this option set to either **Analog** or **DVI**. The DDC communication path is only enabled when set to **Analog** for an analog monitor, so certain advanced features of your analog monitor may only be enabled when routing DDC to **Analog**. The default setting is **DVI**.

PCI Express Configuration Submenu

Use this submenu to apply alternate settings to the PCI Express configuration. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **PCIe High Priority Port**—This setting specifies whether or not one of the PCI Express ports will receive priority when arbitrating data bandwidth. The default value for this setting is **Disabled**.
- **Select Port**—This setting determines which port receives priority when arbitrating data bandwidth if the **PCIe High Priority Port** setting is **Enabled**. To give priority to one of the four ports routed to a PXIe chassis, select **Root Port [1-4]**. The default value for this setting is **Root Port 1**.

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Power/Reset Configuration Submenu

Use this submenu to apply alternate settings to the power and reset configuration. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **Restore on AC Power Loss**—This setting specifies how the controller should behave after an AC power loss event occurs. The controller can be configured to **Stay Off** or **Turn On**. The default is **Stay Off**.
- **Resume on PCIe WAKE#**—This setting determines whether the system can wake from Hibernate via the PCI Express WAKE# signal. This feature must be enabled to wake the system from a device in a PXI Express slot. The default value for this setting is **Disabled**.
- **Power Button Off Behavior**—This setting specifies the system behavior after pressing the power button when the system is in a running state. **Normal** allows the operating system to control the power off behavior. **Force Off** causes an immediate system shutdown. **Disable** completely disables the power button. The default value for this setting is **Normal**.

Caution Setting **Power Button Off Behavior** to **Disabled** is intended for advanced use only when the system power will be controlled through an alternate mechanism. For example, when the PXIe Chassis Inhibit Switch is set to **Manual**, the Inhibit signal can control the system power instead. Refer to an appropriate PXIe chassis User Manual for more information on these options. Setting **Power Button Off Behavior** to **Disabled** under other circumstances may result in situations where the system can only power down safely by disconnecting AC power directly.

USB Configuration Submenu

Use this submenu to apply alternate configurations to the USB ports. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **USB Devices**—This item lists the total number of devices detected in the system, categorized by device type.
- **Legacy USB Support**—This setting specifies whether or not legacy USB support is enabled. Legacy USB support refers to the ability to use a USB keyboard and mouse during system boot or in a legacy operating system such as DOS. The default value is **Enabled**.

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Note Certain real-time applications may require you to disable this setting to reduce loop time jitter. When the controller is configured to boot LabVIEW RT, legacy USB support is automatically disabled.

- **Device Reset Delay**—This setting specifies the number of seconds the Power-On Self Test will wait for a USB mass storage device to start. The default is **20** seconds.

In addition, the following option is available for each detected device if a USB mass storage device is present:

- **Emulation Type**—This setting specifies how the BIOS will present the USB mass storage device to the system. This option can be used to present a USB mass storage device as a floppy, Zip, hard disk, or CD-ROM drive. The default is **Auto**, which allows the BIOS to treat small USB flash disk drives as floppy drives and larger USB flash disk drives as hard disk drives.

Serial/Parallel Port Configuration Submenu

Use this submenu to apply alternate configurations to the serial and parallel ports. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **Serial Port 0 Configuration**—Use this setting to access the **Serial Port 0 Configuration** submenu. Refer to the *Serial Port 0 Configuration Submenu* section for more information.
- **Parallel Port Configuration**—Use this setting to access the **Parallel Port Configuration** submenu. Refer to the *Parallel Port Configuration Submenu* section for more information.

Serial Port 0 Configuration Submenu

- **Serial Port**—This setting enables or disables the onboard serial port. The default value is **Enabled**.
- **Device Settings**—This item displays the current base address and interrupt request level (IRQ) information for the onboard serial port.
- **Change Settings**—This setting changes the base address and interrupt request level (IRQ) information for the onboard serial port. The default value is **Auto**.

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Parallel Port Configuration Submenu

- **Parallel Port**—This setting enables or disables the onboard parallel port. The default value is **Enabled**.
- **Device Settings**—This item displays the current base address and interrupt request level (IRQ) information for the onboard parallel port.
- **Device Mode**—This settings enables alternate modes of operation for the parallel port. Usually the default setting works for all applications. The default is **STD Printer Mode**.
- **Change Settings**—This setting changes the base address and interrupt request level (IRQ) information for the onboard parallel port. The default value is **Auto**. Note that the options available vary based upon the **Device Mode** selected for the parallel port.

Trigger Router Configuration Submenu

Use this submenu to program the front panel trigger router configuration. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration possible.

- **Route Front Panel Trigger**—This setting specifies how the front panel SMB trigger should be routed. The front panel trigger can be routed to **System Reset**, allowing an external trigger to reset the system. The default value is **Disabled**.

LabVIEW RT Options Setup Menu

Use this menu to configure boot options for LabVIEW RT if it is installed on the controller. If you are not using LabVIEW RT, you should leave these settings at default.

Note The settings below override the behavior of the switches on SW1. Refer to the *LabVIEW RT Configuration Switches* section for more information. To use the settings from the switches, select **Use Switch Setting** for each option.

- **Boot Configuration**—This setting selects whether the controller should boot LabVIEW RT, LabVIEW RT Safe Mode, or an installed OS such as Windows XP. The default is **Use Switch Setting**.
- **Reset IP Address**—If the controller is deployed to a different subnet from which it was originally configured, or if the current IP address is invalid, use this switch to reset the IP address and other TCP/IP settings to their factory defaults during LabVIEW RT startup. The default is **Use Switch Setting**.

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Note By default, the target will automatically attempt to connect to the network using DHCP. If the target is unable to initiate a DHCP connection, the target connects to the network with a link-local IP address or `169.254.x.x`.

- **Disable Startup VI**—If the controller becomes inaccessible because of a startup VI, this switch can prevent VIs from automatically running at startup. The default is **Use Switch Setting**.

Boot Setup Menu

Use this menu to configure settings related to the boot process and boot device priority.

- **Boot Settings Configuration**—Use this setting to access the **Boot Settings Configuration** submenu. Refer to the *Boot Settings Configuration Submenu* section for more information.
- **SCSI Drive Boot**—This setting specifies whether or not boot support is enabled for legacy mass storage devices, such as SCSI drives. When enabled, legacy mass storage controllers with boot support will be displayed in the **Boot Option Priorities** menu. The default value is **Enabled**.
- **PXE Network Boot**—This setting specifies whether or not the PXE network boot agent is enabled. When enabled, the Intel Boot Agent will be displayed in the **Boot Option Priorities** menu, allowing you to boot from a PXE server on the local subnet. Note that the Intel Boot Agent device is named **IBA GE Slot 00c9 v1310** in the **Boot Option Priorities** menu. The system must be restarted for this setting to take effect. The default value is **Disabled**.
- **Boot Option Priorities**—These settings specify the order in which the BIOS checks for bootable devices, including the local hard disk drive, removable devices such as USB flash disk drives or USB CD-ROM drives, or the PXE network boot agent. The BIOS will first attempt to boot from the device associated with **1st Boot Device**, followed by **2nd Boot Device**, and **3rd Boot Device**. If multiple boot devices are not present, the BIOS setup utility will not display all of these configuration options. To select a boot device, press <Enter> on the desired configuration option and select a boot device from the resulting menu. You can also disable certain boot devices by selecting **Disabled**.

Note Only one device of a given type will be shown in this list. If more than one device of the same type exists, use the *Device BBS Priorities* submenus to re-order the priority of devices of the same type.

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The following submenus will be displayed if one or more bootable devices of the corresponding type is present:

- **Hard Drive BBS Priorities**—Use this setting to access the **Hard Drive BBS Priorities** submenu to re-order or disable bootable hard drive devices. Refer to the *Hard Drive BBS Priorities Submenu* section for more information.
- **CD/DVD ROM Drive BBS Priorities**—Use this setting to access the **CD/DVD ROM Drive BBS Priorities** submenu to re-order or disable bootable CD/DVD ROM drive devices. Refer to the *CD/DVD ROM Drive BBS Priorities Submenu* section for more information.
- **Floppy Drive BBS Priorities**—Use this setting to access the **Floppy Drive BBS Priorities** submenu to re-order or disable bootable floppy drive devices. Refer to the *Floppy Drive BBS Priorities Submenu* section for more information.
- **Network Device BBS Priorities**—Use this setting to access the **Network Device BBS Priorities** submenu to re-order or disable bootable network devices. Refer to the *Network Device BBS Priorities Submenu* section for more information.

Boot Settings Configuration Submenu

Use this submenu to apply alternate configurations to boot settings. Normally, you do not need to modify these settings, as the factory default settings provide the most compatible and optimal configuration.

- **Quiet Boot**—This setting enables or disables the splash screen during the boot process. The default value is **Enabled**.
- **Setup Prompt Timeout**—This setting specifies the number of seconds the system will wait for a BIOS Setup menu keypress (the <Del> key). The default value is **1** second.
- **Bootup NumLock State**—This setting specifies the power-on state of the keyboard NumLock setting. The default value is **On**.

Hard Drive BBS Priorities Submenu

- **Boot Option #1, Boot Option #2, Boot Option #3**—These settings specify the boot priority of hard drive devices. The highest priority device is displayed on the main **Boot Option Priorities** list. Optionally, each device can also be **Disabled** if the device should never be used as a boot device.

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CD/DVD ROM Drive BBS Priorities Submenu

- **Boot Option #1, Boot Option #2, Boot Option #3**—These settings specify the boot priority of CD/DVD ROM drive devices. The highest priority device is displayed on the main **Boot Option Priorities** list. Optionally, each device can also be **Disabled** if the device should never be used as a boot device.

Floppy Drive BBS Priorities Submenu

- **Boot Option #1, Boot Option #2, Boot Option #3**—These settings specify the boot priority of floppy drive devices. The highest priority device is displayed on the main **Boot Option Priorities** list. Optionally, each device can also be **Disabled** if the device should never be used as a boot device.

Network Device BBS Priorities Submenu

- **Boot Option #1, Boot Option #2, Boot Option #3**—These settings specify the boot priority of network devices. The highest priority device is displayed on the main **Boot Option Priorities** list. Optionally, each device can also be **Disabled** if the device should never be used as a boot device.

Security Menu

Use this menu to enable BIOS security options.

- **Setup Administrator Password**—This setting specifies a password that must be entered to access the BIOS Setup Utility. If only the Administrator's password is set, then this only limits access to the BIOS setup program and is only asked for when entering the BIOS setup program. By default, no password is specified.
- **User Password**—This setting specifies a password that must be entered to access the BIOS Setup Utility or to boot the system. If only the User's password is set, then this is a power on password and must be entered to boot or enter the BIOS setup program. In the BIOS setup program, the User will have Administrator rights. By default, no password is specified.

Save & Exit Menu

The **Save & Exit** setup menu includes all available options for exiting, saving, and loading the BIOS default configuration. As an alternative to this screen, press <F9> to load optimal BIOS default settings and <F10> to save changes and exit setup.

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The **Exit** setup menu includes the following settings:

- **Save Changes and Reset**—Any changes made to BIOS settings are stored in NVRAM. The setup program then exits and reboots the controller. The <F10> key can also be used to select this option.
- **Discard Changes and Reset**—Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller. The <Esc> key can also be used to select this option.
- **Save Changes**—Changes made to BIOS settings during this session are committed to NVRAM. The setup program remains active, allowing further changes.
- **Discard Changes**—Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The BIOS setup continues to be active.
- **Restore Factory Defaults**—This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting. Notice that any nondefault settings such as boot order, passwords, and so on, are also restored to their factory defaults. The <F9> key can also be used to select this option.
- **Save As User Defaults**—This option saves a copy of the current BIOS settings as the User Defaults. This option is useful for preserving custom BIOS setup configurations.
- **Restore User Defaults**—This option restores all BIOS settings to the user defaults. This option is useful for restoring previously preserved custom BIOS setup configurations.
- **Boot Override**—This option lists all possible bootable devices and allows the user to override the **Boot Option Priorities** list for the current boot. If no changes have been made to the BIOS setup options, the system will continue booting to the selected device without first rebooting. If BIOS setup options have been changed and saved, a reboot will be required and the boot override selection will not be valid.

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System CMOS

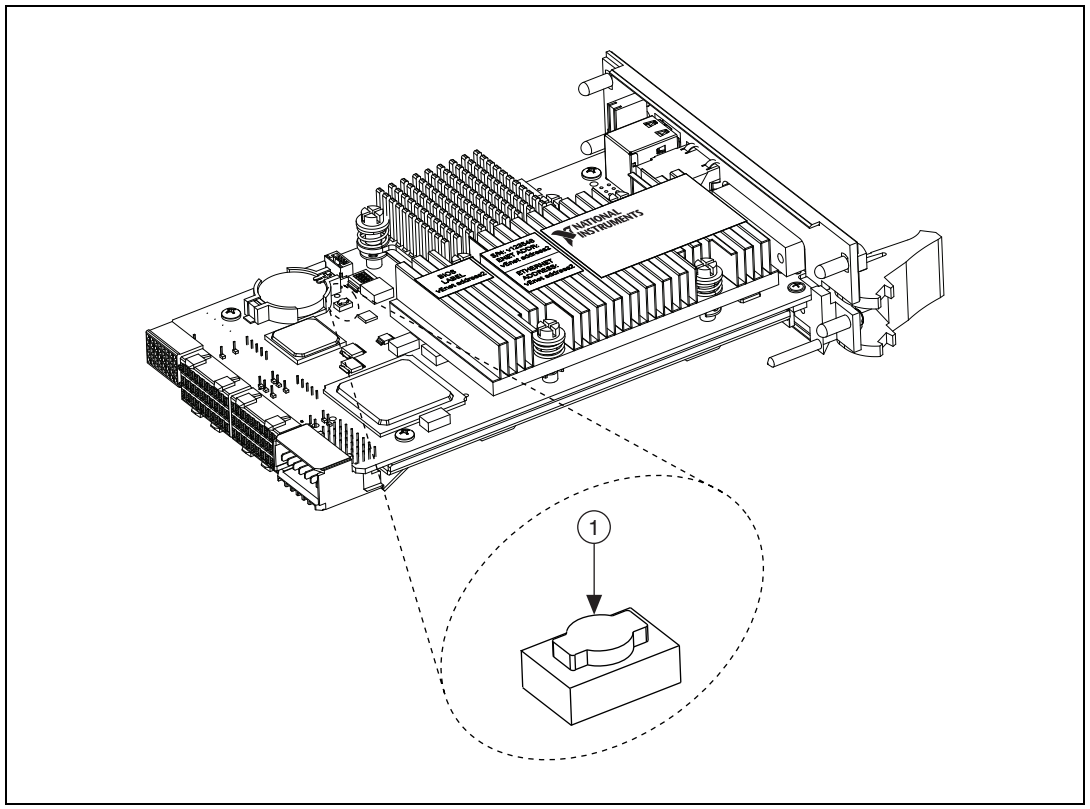
The NI PXIe-8101/8102 contains a backed-up memory used to store BIOS configuration information.

Complete the following steps to clear the CMOS contents:

1. Power off the chassis.
2. Remove the controller from the chassis.
3. Press and hold down push-button switch SW2 for 2 to 3 seconds. The switch location is shown in Figure 2-3.
4. Reinstall the controller in the chassis.

Note After reinstalling the controller in the chassis the system may power up automatically since the CMOS option for **Restore on AC Power Loss** has just been cleared. Subsequent removal and reinstallation of the controller will not result in this behavior unless CMOS is cleared again.

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1	Push-Button Switch SW2

Figure 2-3. Clearing the CMOS Contents

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LabVIEW RT Installation

This section explains software installation and switch configuration for LabVIEW RT on your PXI controller.

LabVIEW RT Software Installation

The following section describes the necessary steps to get your PXI embedded controller setup to run LabVIEW Real-Time. In this section you will configure the boot mode of the controller, verify or change IP settings, and install LabVIEW Real-Time software.

Complete the following steps to install the LabVIEW RT software.

1. Boot the NI PXI embedded controller into the real-time operating system. Refer to the *LabVIEW RT Configuration Switches* section or the *LabVIEW RT Options Setup Menu* section in this manual to configure the controller for booting into LabVIEW RT.

   The PXI controller will automatically boot into LabVIEW RT Safe Mode when no software is installed. LabVIEW RT Safe Mode loads with the basic real-time operating system and will automatically attempt to connect to the network using DHCP. If DHCP is not available, it will then connect to the network with a link-local IP address.



Tip You can connect a monitor to the desktop PC to display startup messages such as the IP address and MAC address.

2. Open Measurement & Automation Explorer (MAX) on another computer in the same subnet and expand the **Remote Systems** branch. MAX lists the PXI controller as the model name of the controller followed by the MAC address (for example, **NI-PXIe-8101 00802f108562**).



Note The other computer must have LabVIEW, LabVIEW RT, and any desired drivers installed.



Tip Record the PXI controller MAC address, located on the side of the controller, for use in identifying the controller. The label also can be removed and placed on the front of the controller for easier access.

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3. Click on the appropriate PXI controller entry to access the **Network Settings** tab in the right pane view.
4. (Optional) Enter a name for the RT target in the **Name** text box.
5. (Optional) Set the network configuration options of the RT target in the **IP Settings** section and click the **Apply** button.

   For information about configuring network settings, refer to the *Configuring Network Settings* book, accessible by browsing to **MAX Remote Systems Help»LabVIEW Real-Time Target Configuration»Configuring Network Settings** from the **Contents** tab of *MAX Help*.

Note When any IP or identification settings are changed, you will be prompted to reboot the controller for the changes to take effect. Click **Yes** to automatically reboot the RT target. You may also reboot the controller by right-clicking on the target name under **Remote Systems** and selecting **Reboot**.

After rebooting the PXI controller it will appear in the **Remote Systems** category with the assigned name.

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Figure 2-4 shows the RT Series PXI target, **PXIe-8101**, configured to automatically obtain an IP address from a DHCP server, as indicated in the **IP Settings** section of the figure. The NI PXIe-8102 would appear similar in MAX.

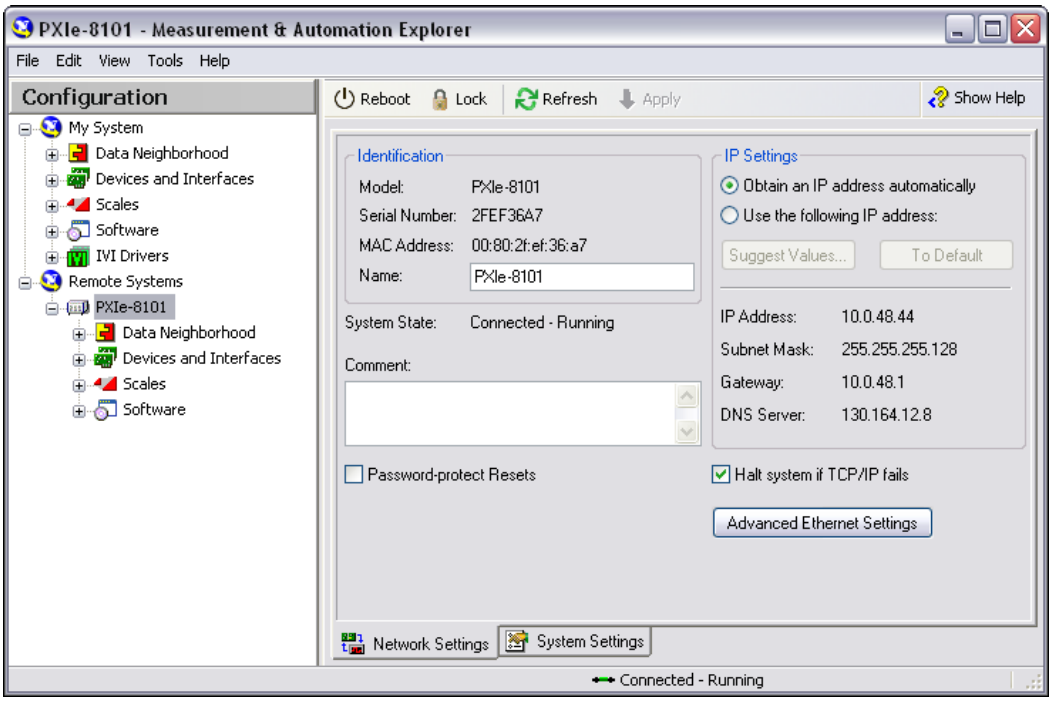


Figure 2-4. Configuring RT Target Network Settings

6. Expand the PXI controller view in the **Remote Systems** branch and select **Software**.
7. Click the **Add/Remove Software** button in the toolbar to launch the LabVIEW Real-Time Software Wizard.
8. Install the LabVIEW Real-Time software and device drivers that you require on the RT target. Refer to the NI Web site at `ni.com/info` and enter the Info Code `etspc` for the latest information about supported software.

After installation of the software the controller will automatically reboot and you will now be able to program it using LabVIEW Real-Time.

Note Refer to the *RT Getting Started Guide* available on your host computer for more information about setting up your RT target.

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LabVIEW RT Configuration Switches

Use the LabVIEW RT configuration switches to configure LabVIEW RT if it is installed on the controller. If you are not using LabVIEW RT, these switches should remain in the OFF position. The controller reads these switches only after a system reset.

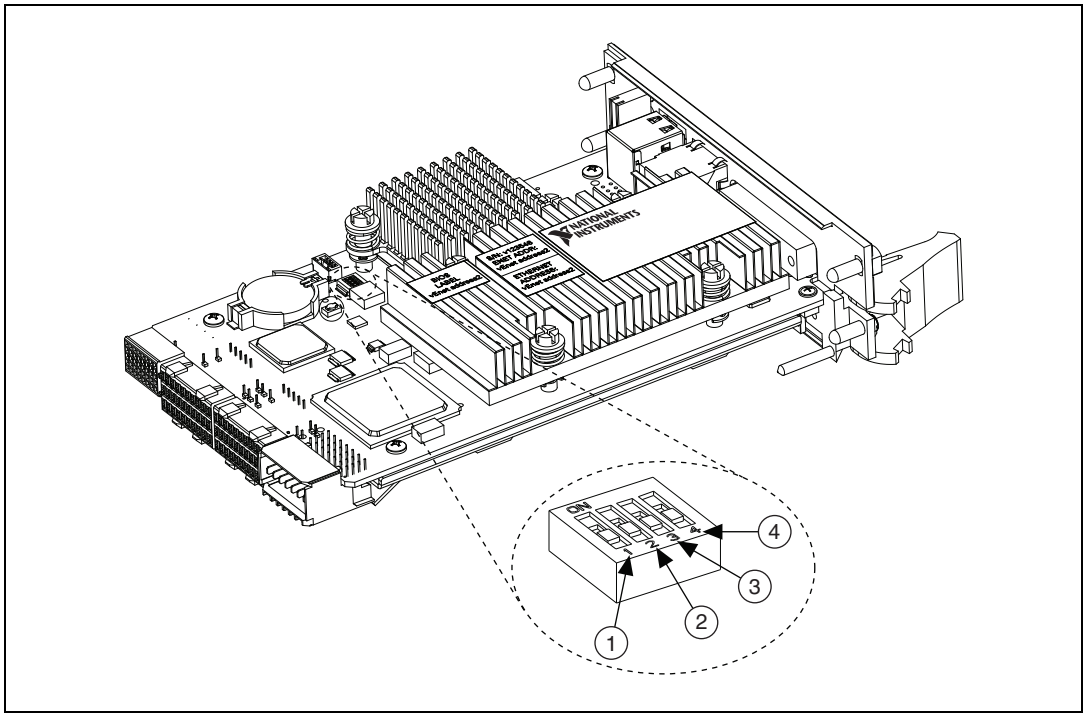
The NI PXIe-8101/8102 controller includes the following LabVIEW RT configuration switches:

- Switch 1—**Boot LabVIEW RT**: Set this switch to ON to boot LabVIEW RT.
- Switch 2—**Boot Safe Mode**: Set this switch to ON to boot LabVIEW RT into safe mode to reconfigure TCP/IP settings and to download or update software from a host computer. This switch overrides the behavior of Switch 1. Booting the controller into safe mode does not start the embedded LabVIEW RT engine. After changing the settings or software, reboot the controller with this switch OFF to resume normal operation.
- Switch 3—**Disable Startup VI**: Set this switch to ON to prevent VIs from automatically running at startup if the controller becomes inaccessible because of a startup VI.
- Switch 4—**Reset IP Address**: Set this switch to ON to reset the IP address and other TCP/IP settings to their factory defaults. Use this switch if moving the controller to a different subnet or if the current TCP/IP settings are valid.

Note By default, the target will automatically attempt to connect to the network using DHCP. If the target is unable to initiate a DHCP connection, the target connects to the network with a link-local IP address or `169.254.x.x`.

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Figure 2-5 shows the location of the LabVIEW RT configuration switches. The switches are shown in the OFF position.



1	Switch 1—Boot LabVIEW RT	3	Switch 3—Disable Startup VI
2	Switch 2—Boot Safe Mode	4	Switch 4—Reset IP Address

Figure 2-5. LabVIEW RT Configuration Switches

Drivers and Software

Your hard drive includes a directory called `images` in its root that contains software and soft copies of manuals for the peripherals. The directory structure under the `images` directory is logically organized into several levels.

In the `images` directory, you will find a `manuals` directory, an `os` directory, and a `drivers` directory.

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The `manuals` directory contains quick reference guides, technical reference manuals, and National Instruments software manuals, all in Adobe Acrobat format. To access any manual, change your directory to `c:\images\manuals` and list the contents of that directory.

The `os` directory contains a subdirectory corresponding to the operating system installed on your computer.

The `drivers` directory contains driver installers for the system peripherals. These files and directories are copied exactly from the manufacturer distribution disks, so the naming conventions vary from peripheral to peripheral.

PXI Express Features

PXI Express Trigger Connectivity

The SMB connector on the NI PXIe-8101/8102 front panel can connect to or from any PXI backplane trigger line. A trigger allocation process is needed to prevent two resources from connecting to the same trigger line, resulting in the trigger being double-driven and possibly damaging the hardware. At the time of this manual's publication, this software is not yet available for Windows. Contact National Instruments for more information.

PXI Chassis Configuration

The PXI Platform Services software installed on your controller and available on the *National Instruments Driver CD* included with your kit automatically identifies your PXI Express system components to generate a `pxiesys.ini` and `pxisys.ini` file. You can configure your entire PXI system and identify PXI-1 chassis through Measurement & Automation Explorer (MAX), included with your controller. MAX creates the `pxiesys.ini` and `pxisys.ini` file, which define your PXI system parameters. MAX also provides an interface to route and reserve triggers so dynamic routing, through drivers such as DAQmx, avoids double-driving and potentially damaging trigger lines. For more information about routing and reserving PXI triggers, refer to KnowledgeBase 3TJDOND8, *Using PXI Timing and Triggering Functionality*, at `ni.com/support`.

The configuration steps for single or multiple-chassis systems are the same.

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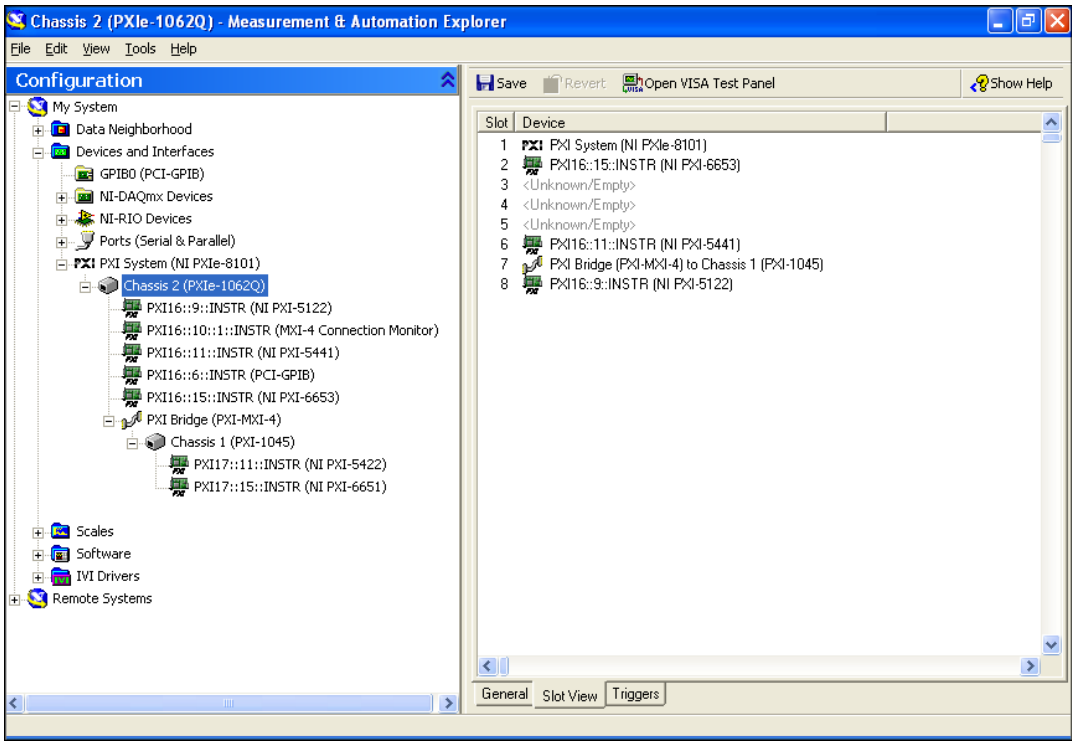


Figure 2-6. Multichassis Configuration in MAX

PXI System Configuration

1. Launch Measurement & Automation Explorer (MAX).
2. In the **Configuration** tree, expand **Devices and Interfaces**.
3. In the **Devices and Interfaces** tree, expand **PXI System**. Your PXI Express chassis is already identified and appears in the **PXI System** tree.
4. For each unidentified PXI chassis in the **PXI System** tree, right-click on the chassis and select the appropriate chassis model through the **Identify As** submenu. Further expanding the **PXI System** branch shows all devices in the system that NI-VISA can recognize.

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The PXI specification allows for many combinations of PXI chassis and system modules. To assist system integrators, PXI chassis and system module manufacturers must document their products' capabilities. PXI Express devices must provide a driver and/or `.ini` file for identification. For NI PXI Express chassis and controllers, these files are provided as part of the NI PXI Platform Services software included with your controller. System integrators, configuration utilities, and device drivers can use these `.ini` files.

The NI PXI Platform Services software uses the system module driver, chassis driver, and `chassis.ini` files to generate the PXI system description files (`pxisys.ini` and `pxiesys.ini`). Device drivers and other utility software read the `pxiesys.ini` and `pxisys.ini` files to obtain system information. For detailed information about initialization files, refer to the PXI specification at `www.pxisa.org`.

Upgrading RAM

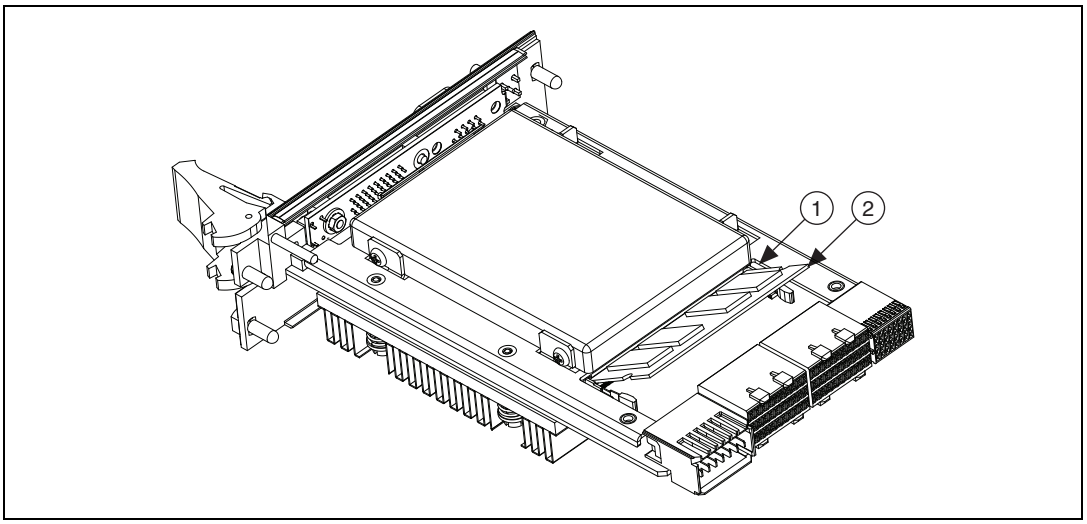
You can change the amount of installed RAM on the NI PXIe-8101/8102 by upgrading the SO-DIMM.

National Instruments offers the following types of SO-DIMMs for use with the NI PXIe-8101/8102 controller.

- PC2-6400 1 GB, 128 MB × 64, 1.18 in. max
- PC2-6400 2 GB, 256 MB × 64, 1.18 in. max
- PC2-6400 4 GB, 512 MB × 64, 1.18 in. max

Note National Instruments has tested and verified that the DDR2 SO-DIMMs we sell work with the NI PXIe-8101/8102. We recommend you purchase your DDR2 SO-DIMM modules from National Instruments. Other off-the-shelf DDR2 SO-DIMM modules are not guaranteed to work properly.

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1	DDR2 SO-DIMM Socket	2	DDR2 SO-DIMM Module

Figure 2-7. Installing a DDR2 SO-DIMM in an NI PXIe-8101/8102 Controller

Hard Drive Recovery

NI PXIe-8101/8102 controllers include two methods of restoring the original factory condition of your hard drive. Hard drive-based recovery stores a factory backup on a separate portion of your hard drive allowing you to restore your controller without additional media. The NI PXIe-8101/8102 controller also ships with an *OS Recovery* DVD that allows you to reinstall your operating system onto your hard drive through an external USB DVD-ROM. For more information on these tools, refer to KnowledgeBase 2ZKC02OK, *Hard Drive Recovery and OS Installation for PXI and VXI Controllers*, at `ni.com/support`.

Note Your system hot key is <F4>. To access the hard drive-based recovery tool, press and hold <F4> when video first appears during the boot process.

If you need to recover your factory-installed operating system from a DVD, you can use the included OS re-installation DVD with an external USB DVD-ROM drive. Boot the PXI Express controller using the OS re-installation DVD to recover the OS. After using the DVD to recover the OS you also may need to reinstall other software.

Note Recovering the OS erases the contents of your hard disk. Back up any files you want to keep.

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Installing an OS

NI PXIe-8101/8102 controllers include a preinstalled OS. In some cases, you may want to install a different OS. When doing so, consider the following guidelines.

Installing from a USB CD/DVD-ROM

The NI PXIe-8101/8102 supports the installation of Windows 7, XP, and Vista from a USB CD-ROM.

As an alternative to a USB CD-ROM drive, you can use an external SCSI CD-ROM with a PXI-SCSI adapter.

Note For additional assistance with installing or changing an operating system, refer to KnowledgeBase 2ZKC02OK, *Hard Drive Recovery and OS Installation for PXI and VXI Controllers*, at `ni.com/support`.

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3

I/O Information

Front Panel Connectors

Table 3-1 lists various peripherals and their corresponding NI PXIe-8101/8102 external connectors, bus interfaces, and functions.

Table 3-1. NI PXIe-8101/8102 Peripherals Overview

Peripheral	External Connector	Description
Video	DVI-I (24-pin DSUB)	Intel Extreme Graphics controller
Serial	COM1 (9-pin DSUB)	16550 RS-232 serial port
Ethernet	LAN (RJ45)	10/100/1000 Ethernet connection
Parallel	Parallel Port (36-pin champ)	IEEE 1284
USB	USB 4-pin Series A stacked receptacle	Hi-Speed USB
PXI trigger	Trigger (SMB)	Routing PXI triggers to or from the backplane trigger bus

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Front Panel

Figure 3-1 shows the front panel layout and dimensions of the NI PXIe-8101. The NI PXIe-8102 has the same front panel layout and dimensions. Dimensions are in inches (millimeters).

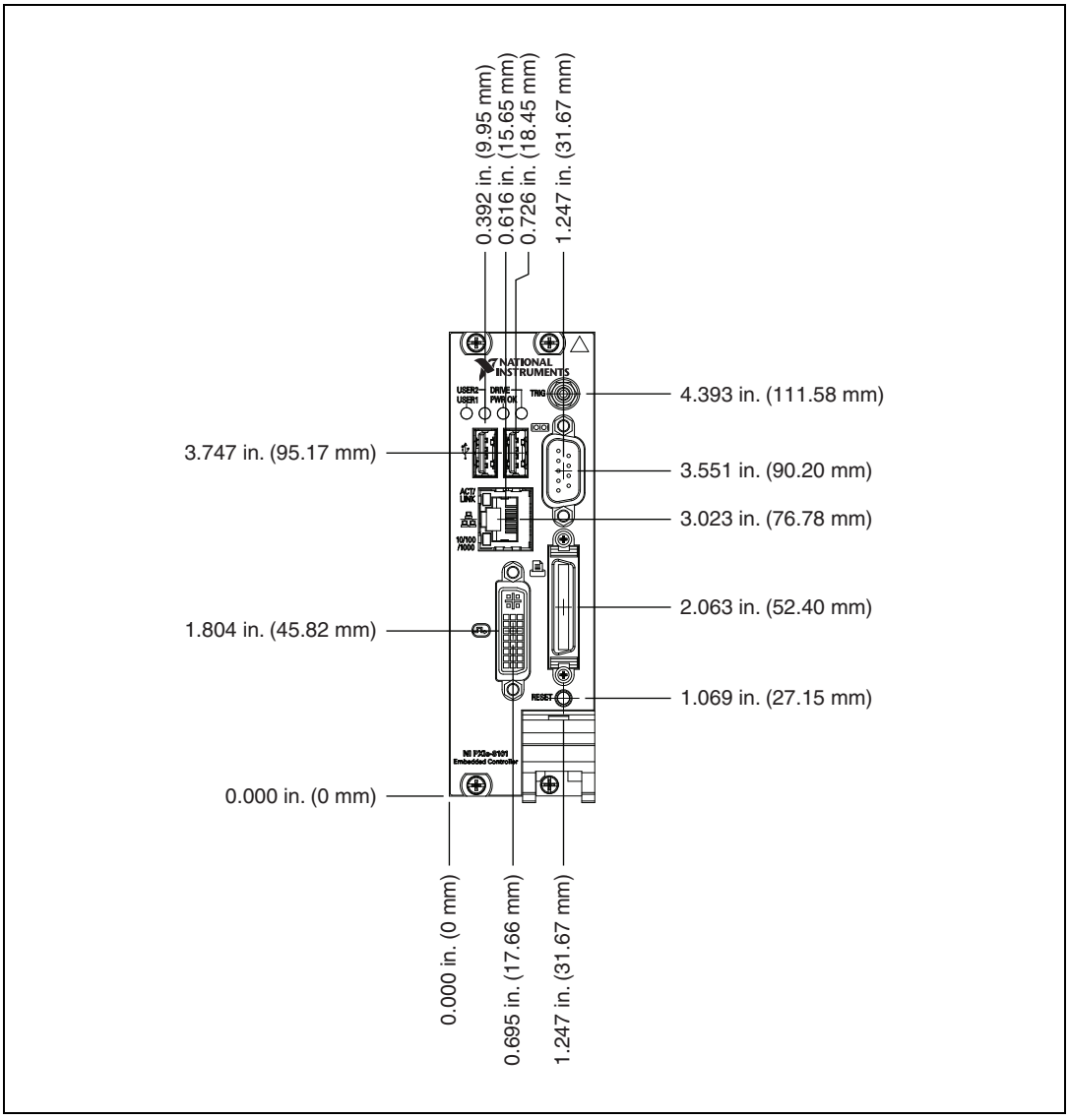


Figure 3-1. NI PXIe-8101 Front Panel Layout and Dimensions

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DVI-I

Figure 3-2 shows the location and pinouts for the DVI-I connector on the NI PXIe-8101/8102. Table 3-2 lists and describes the DVI-I connector signals.

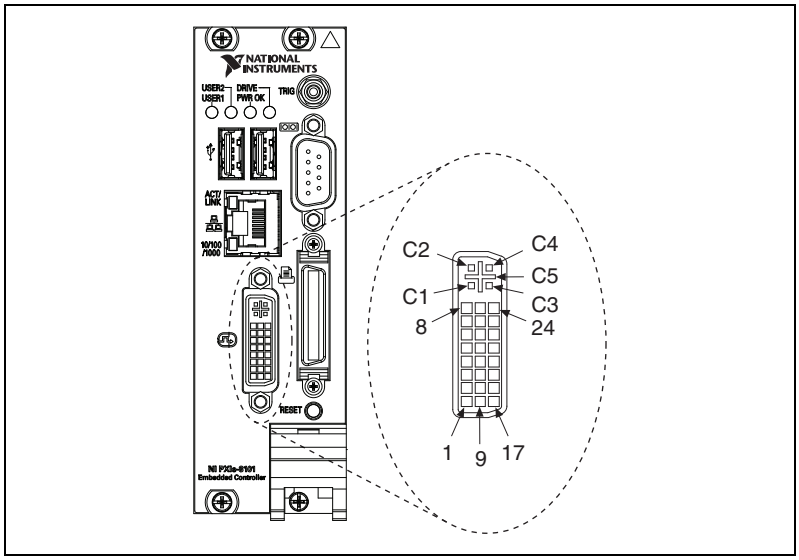


Figure 3-2. DVI-I Connector Location and Pinout

Table 3-2. DVI-I Connector Signals

Pin	Signal Name
1	TMDS Data2–
2	TMDS Data2+
3	TMDS Data2/4 Shield
4	Reserved
5	Reserved
6	DDC Clock [SCL]
7	DDC Data [SDA]
8	Analog vertical sync
9	TMDS Data1–
10	TMDS Data1+

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Table 3-2. DVI-I Connector Signals (Continued)

Pin	Signal Name
11	TMDS Data1/3 Shield
12	Reserved
13	Reserved
14	+5 V Power
15	Ground (for +5 V)
16	Hot Plug Detect
17	TMDS Data0–
18	TMDSData0+
19	TMDS Data0/5 Shield
20	Reserved
21	Reserved
22	TMDS Clock Shield
23	TMDS Clock +
24	TMDS Clock –
C1	Analog Red
C2	Analog Green
C3	Analog Blue
C4	Analog Horizontal Sync
C5	Analog GND Return: (analog R, G, B)

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COM1

Figure 3-3 shows the location and pinouts for the COM1 connector on the NI PXIe-8101/8102. Table 3-3 lists and describes the COM1 connector signal.

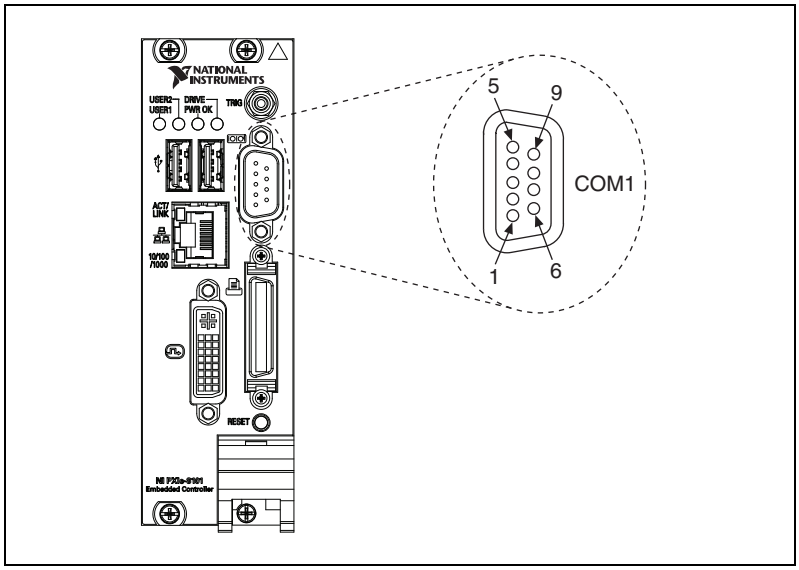


Figure 3-3. COM1 Connector Location and Pinout

Table 3-3. COM1 Connector Signals

Pin	Signal Name	Signal Description
1	DCD#	Data Carrier Detect
2	RXD#	Receive Data
3	TXD#	Transmit Data
4	DTR#	Data Terminal Ready
5	GND	Ground
6	DSR#	Data Set Ready
7	RTS#	Ready to Send
8	CTS#	Clear to Send
9	RI#	Ring Indicator
Note: The pound symbol (#) indicates an active low signal.		

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Ethernet

Figure 3-4 shows the location and pinouts for the Ethernet connector on the NI PXIe-8101/8102. Table 3-4 lists and describes the Ethernet connector signals.

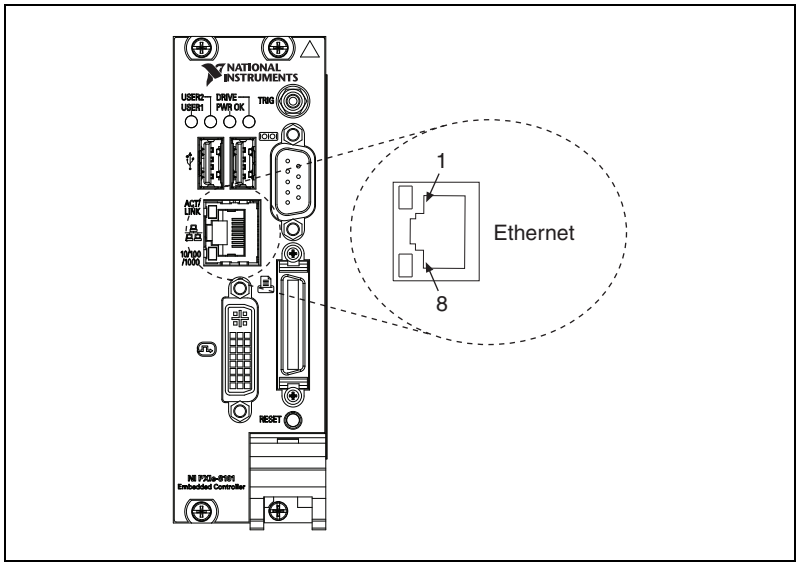


Figure 3-4. Ethernet Connector Location and Pinout

Table 3-4. Ethernet Connector Signals

Pin	Fast Ethernet	Gigabit Ethernet
1	TX+	TX_A+
2	TX–	TX_A–
3	RX+	RX_B+
4	NC	TX_C+
5	NC	TX_C–
6	RX–	RX_B–
7	NC	RX_D+
8	NC	RX_D–

Note The Ethernet controller can perform an automatic crossover, thus eliminating the need for crossover cables.

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Table 3-5. 10/100/1000 LAN Connector LED States

LED	Color	LED State	Condition
Top	Green	Off	LAN link is not established.
		On (steady state)	LAN link is established.
		On (brighter and pulsing)	The controller is communicating with another computer on the LAN.
Bottom	Unlit	Off	10 Mbit/sec data rate is selected.
	Green	On	100 Mbit/sec data rate is selected.
	Amber	On	1000 Mbit/sec data rate is selected.

Parallel Port

Figure 3-5 shows the location and pinouts for the IEEE 1284 (parallel) connector on the NI PXIe-8101/8102. Table 3-6 lists and describes the IEEE 1284 connector signals.

Parallel port adapter cables are available from National Instruments, part number 777169-01.

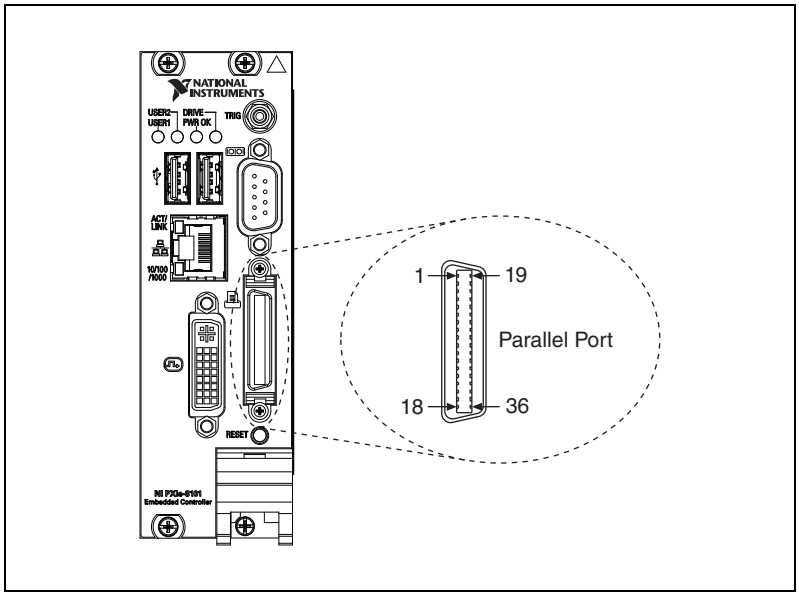


Figure 3-5. Parallel Port Connector Location and Pinout

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Table 3-6. Parallel Port Connector Signals

Pin	Default Configuration (LPT)	
	Signal Name	**Signal Description**
1	BUSY	Device Busy
2	SLCT	Select
3	ACK#	Acknowledge
4	FAULT#(ERROR#)	Fault
5	PAPEREND	Paper End
6	PD0	Data Bit 0
7	PD1	Data Bit 1
8	PD 2	Data Bit 2
9	PD3	Data Bit 3
10	PD4	Data Bit 4
11	PD5	Data Bit 5
12	PD6	Data Bit 6
13	PD7	Data Bit 7
14	INIT#	Initialize Printer
15	STROBE#	Strobe
16	SLCTIN#	Select Input
17	AUTOFD#	Auto Line Feed
18	+5V	+5 V
19–35	GND	Ground
36	NC	Not Connected

Note: The pound symbol (#) indicates an active low signal.

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Universal Serial Bus

Figure 3-6 shows the location and pinouts for the Universal Serial Bus (USB) connector on the NI PXIe-8101/8102. Each controller has four USB ports on the front panel. Table 3-7 lists and describes the USB connector signals.

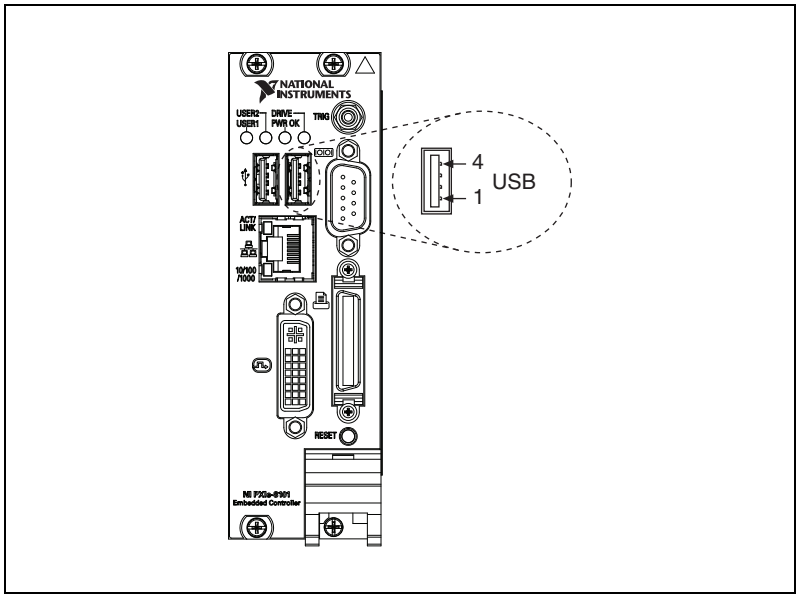


Figure 3-6. USB Connector Location and Pinout

Table 3-7. USB Connector Signals

Pin	Signal Name	Signal Description
1	VCC	Cable Power (+5 V)
2	–Data	USB Data –
3	+Data	USB Data +
4	GND	Ground

Note Install a noise-suppression ferrite (included in your shipping kit) around all attached external USB cables to ensure that your device meets all EMC standards applicable to your country. Install the ferrite so that each USB cable loops through the ferrite as shown in Figure 3-7. The ferrite should be installed so that it is approximately 50–75 mm (2–3 in.) from the end of the cable that plugs into the controller. The ferrite should accommodate from 2–4 USB cables depending on cable diameter.

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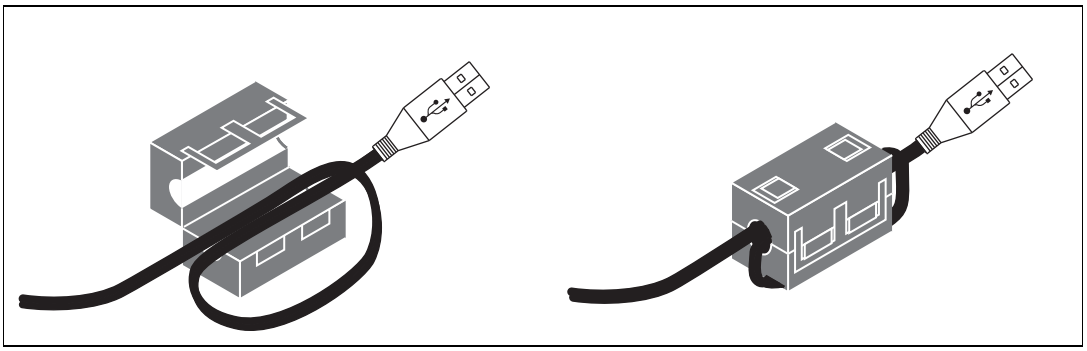


Figure 3-7. Installing a Ferrite on a Single USB Cable

Trigger

The TRIG connector is the software-controlled trigger connection for routing PXI triggers to or from the backplane trigger bus.

Figure 3-8 shows the TRIG connector location on the NI PXIe-8101/8102. Table 3-8 lists and describes the trigger connector signals.

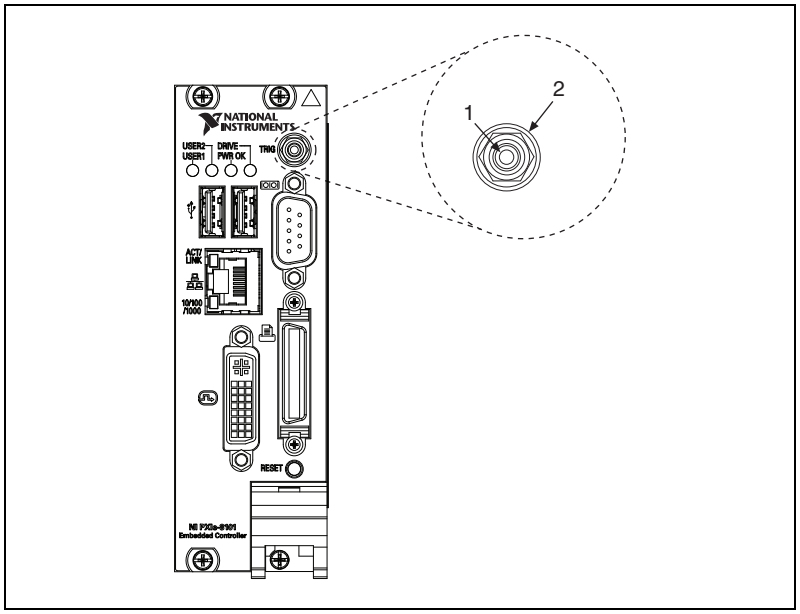


Figure 3-8. TRIG Connector Location and Pinout

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Table 3-8. TRIG Connector Signals

Pin	Signal Name	Signal Description
1	TRIG	Trigger
2 (Shield)	GND	Ground

Front Panel Features

The NI PXIe-8101/8102 has the following front-panel features:

- A controller reset pushbutton (press the button to generate a reset to the controller)
- Four front panel LEDs that show PC status
  - The **POWER OK** LED indicates the power status of the controller. The LED will indicate one of the following states:
    - ON steady—PXI and onboard power is on and within regulation limits.
    - BLINKING—One of the onboard power supplies is operating outside of the normal limits, or is not functioning.
    - OFF—The controller is powered off.
  - The **DRIVE** LED indicates when an access to the internal hard disk is occurring.
  - The **USER1** LED is a bi-color green/yellow LED. You can define the USER1 LED to meet the needs of your LabVIEW application.
  - The **USER2** LED is a bi-color green/yellow LED. You can define the USER1 LED to meet the needs of your LabVIEW application.

Data Storage

The NI PXIe-8101/8102 has the following data storage features:

- 2.5 in. SATA notebook internal hard drive
- USB storage support—USB CD/DVD-ROM, mass storage device, or floppy drive

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4

Common Configuration Questions

This chapter answers common configuration questions you may have when using a NI PXIe-8101/8102 embedded controller.

General Questions

What do the LEDs on the NI PXIe-8101/8102 front panel mean?

Refer to the LED status descriptions in the *Front Panel Features* section of Chapter 3, *I/O Information*.

After shutting down my NI PXIe-8101/8102 controller, the Ethernet LEDs continue to blink. Is it safe to remove my controller or disconnect power?

The NI PXIe-8101/8102 controller Ethernet device remains powered even after shutdown to support the Wake On LAN feature. It is safe to remove your controller or disconnect power.

How do I check the configuration of the memory, hard drive, time/date, and so on?

You can view these parameters in the BIOS setup. To enter the BIOS setup, reboot the NI PXIe-8101/8102 and press <Delete> during the memory tests. Refer to the *Accessing BIOS Setup Utility* section of Chapter 2, *Installation and Configuration*, for more information.

Can I use the internal Serial ATA drive and an external hard drive at the same time?

Yes.

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Boot Options

What devices can I boot from?

The NI PXIe-8101/8102 can boot from the following devices:

- The internal Serial ATA hard drive
- An external SCSI hard drive or CD/DVD-ROM if an SCSI adapter, such as the PXI-8214, is used
- A network PXE server on the same subnet
- An external USB mass storage device such as a USB hard drive, USB CD/DVD-ROM, or USB key
- An external USB floppy drive
- Most PCI or PCIe-based devices that provide an Option ROM

Note There are some limitations when booting from a USB device. Windows XP can be installed from a USB CD/DVD-ROM, but earlier versions of Windows cannot. The NI PXIe-8101/8102 BIOS configures the USB devices so that they will work in a DOS environment.

How do I configure the controller to boot from these devices?

There are two methods.

- Enter Setup and select the **Boot** menu. You will see a list of all bootable devices, ordered by device type. You can set the boot order using <+> and <–>. Set the order by device type and set the order for the devices listed within the device type.
- To boot from a different device without changing the boot order in the BIOS, press <F10> continuously while booting. After the BIOS completes the POST and just before the controller boots the OS, the **Boot** menu is displayed. You can select the device type you want to boot from.

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Cables and Connections

How do I plug both a PS/2 mouse and PS/2 keyboard into the controller?

The NI PXIe-8101/8102 has no PS/2 connector, and you need to use a USB Y-splitter cable as shown in Figure 4-1, or a similar device, to connect both a PS/2 mouse and PS/2 keyboard. National Instruments Part Number 778713-01 is such a cable and is available through the online catalog at `ni.com/products`.

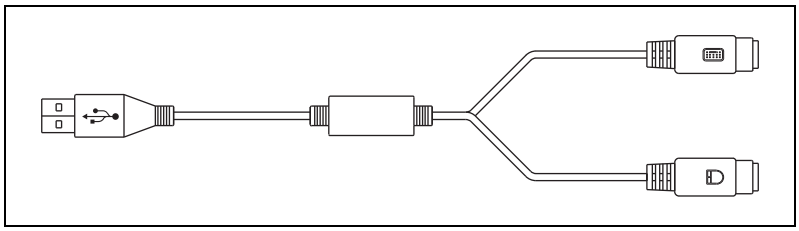


Figure 4-1. Y-Splitter Cable

What if I don't have a Y-splitter cable? Can I still use a mouse and keyboard?

If you do not have a Y-splitter cable, plug a USB keyboard into any USB connector. You can also plug a USB mouse into any USB connector.

How do I connect a standard 25-pin LPT cable to the NI PXIe-8101/8102?

The NI PXIe-8101/8102 uses a type C LPT connector. Most parallel port devices use a type A connector. To use a device with a standard type A LPT connector, you need to use a type C-to-type A LPT adapter. Parallel port adapter cables, part number 777169-01, are available through the online catalog at `ni.com/products`.

How do I connect a VGA monitor to the NI PXIe-8101/8102?

A VGA-to-DVI-I adapter (part number 762559-01) is included with your kit. You can use this adapter to connect a VGA monitor to the DVI-I port.

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How do I connect two monitors to the NI PXIe-8101/8102?

To use both DVI and VGA, you can purchase a DVI to DVI and VGA splitter cable through National Instruments (part number 780868-01). Refer to KnowledgeBase 497ERQ7N, *Dual Monitors with the DVI Connection on PXI controller*, at `ni.com/support` for more information.

Software Driver Installation

How do I install or reinstall the video driver?

Refer to KnowledgeBase 3H3COSD8, *What Peripheral Drivers Should I Use with My PXI or VXI Controller?*, at `ni.com/support`.

How do I install or reinstall the Ethernet driver?

Refer to KnowledgeBase 3H3COSD8, *What Peripheral Drivers Should I Use with My PXI or VXI Controller?*, at `ni.com/support`.

How do I install software from a CD?

The compact size of the NI PXIe-8101/8102 does not allow for an integrated USB CD/DVD-ROM drive. You have the following options:

- **USB CD/DVD-ROM**—You can install from a USB CD/DVD-ROM using a bootable installation CD.
- **SCSI CD-ROM**—Other types of CD-ROM drives are available. Check with the vendor to make sure Windows XP supports the drive.
- **Mapped network drive**—You can use the Ethernet to connect to another computer. If you share the USB CD/DVD-ROM drive on the other computer, you can map the shared USB CD/DVD-ROM drive to a drive letter on the NI PXIe-8101/8102.

A USB CD/DVD-ROM drive is available from National Instruments, part number 778492-01.

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Upgrade Information

How do I upgrade system memory?

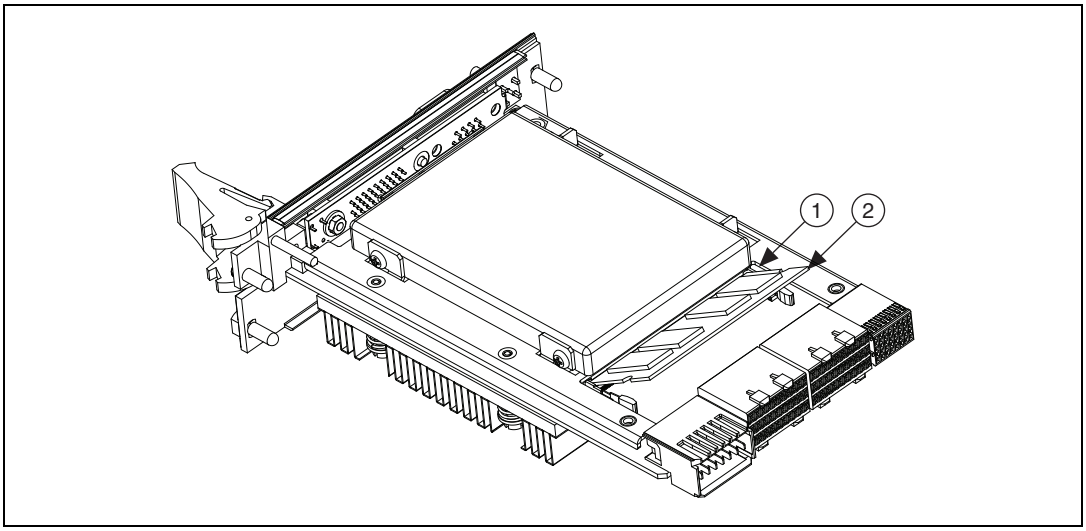
You can change the amount of installed RAM on the NI PXIe-8101/8102 by upgrading the DDR2 SO-DIMM.

To upgrade the RAM, remove the NI PXIe-8101/8102 from the PXI chassis.

National Instruments offers the following types of SO-DIMMs for use with the NI PXIe-8101/8102 controller.

- PC2-6400 1 GB, 128 MB × 64, 1.18 in. max
- PC2-6400 2 GB, 256 MB × 64, 1.18 in. max
- PC2-6400 4 GB, 512 MB × 64, 1.18 in. max

Note National Instruments has conducted thermal testing as well as mechanical shock and vibration testing, and has verified that the DDR2 SO-DIMMs we sell work with the NI PXIe-8101/8102. We recommend you purchase your DDR2 SO-DIMM modules from National Instruments. Other off-the-shelf DDR2 SO-DIMM modules are not guaranteed to work properly.



1	DDR2 SO-DIMM Module	2	DDR2 SO-DIMM Socket

Figure 4-2. Installing a DDR2 SO-DIMM in an NI PXIe-8101/8102 Controller

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How do I flash a new BIOS?

You can download the new BIOS from `ftp.ni.com/support/pxi`. For more information, refer to KnowledgeBase 2GIGKD0Z, *Determining and Upgrading PXI and VXI Embedded Controller BIOS Versions*.

Where do I get the latest software drivers?

The latest National Instruments software is available from `ni.com/downloads`. For peripheral drivers, refer to KnowledgeBase 3H3COSD8, *What Peripheral Drivers Should I Use with My PXI or VXI Controller?*, at `ni.com`.

My NI PXIe-8101/8102 does not have an internal floppy drive. Is there a way to use an external drive?

Yes. The NI PXIe-8101/8102 controller supports and can boot from USB floppy drives. A USB floppy drive will not work with Windows NT4, but will work with Windows 2000 or Windows XP. Refer to the *Boot Options* section for more information.

A USB floppy drive is available from National Instruments, part number 778492-02.

PXI Express Configuration

How do I use the SMB trigger on the front panel?

For details, refer to the *PXI Express Features* section of Chapter 2, *Installation and Configuration*.

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5

Troubleshooting

This chapter answers common troubleshooting questions you may have when using the NI PXIe-8101/8102 embedded computer.

What if the NI PXIe-8101/8102 does not boot?

Several problems can cause a controller not to boot. Here are some things to look for and possible solutions.

Things to Notice:

- Which LEDs come on? The **Power OK** LED should stay lit. The **Drive** LED should blink during boot as the disk is accessed.
- What appears on the display? Does it hang at some particular point (BIOS, Operating System, and so on.)? If nothing appears on the screen, try a different monitor. Does your monitor work with a different PC? If it hangs, note the last screen output that you saw for reference when consulting National Instruments technical support.
- What has changed about the system? Did you recently move the system? Was there electrical storm activity? Did you recently add a new module, memory chip, or piece of software?
- Refer to your chassis user manual for additional troubleshooting steps.

Things to Try:

- Make sure the chassis is plugged in to a working power source.
- Check any fuses or circuit breakers in the chassis or other power supply (possibly a UPS).
- Make sure the controller module is firmly seated in the chassis.
- Remove all other modules from the chassis.
- Remove any nonessential cables or devices.
- Try the controller in a different chassis.
- Try a similar controller in this same chassis.
- Clear the CMOS. (Refer to the *System CMOS* section of Chapter 2, *Installation and Configuration*.)

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- Recover the hard drive on the controller. (Refer to the *Hard Drive Recovery* section of Chapter 2, *Installation and Configuration*.)
- Make sure the RAM is properly seated.

My controller boots fine until I get to Windows, at which point I cannot read the screen. This may include garbled output, white screen, black screen, or an out of synch message from the monitor.

This problem usually results from having the video output set past the limits of the monitor. You will need to boot Windows in Safe Mode. To do this, reboot the controller. As Windows begins to boot, hold down <F8>. You should now be able to reset the video driver to lower settings. Try setting the resolution to 640 × 480 and the refresh rate to 60 Hz. Once you reboot, you can raise these values again, using the test option in Windows. These settings are accessible through the **Advanced** tab of the **Display** item in the **Control Panel**. Alternately, you can try a different monitor, preferably a newer and larger one.

My system boots fine as long as a particular module is not in my chassis.

The most common cause of this is a damaged module. Try the module in a different chassis or with a different controller. Also, remove any external cables or terminal blocks connected to the system. If the module does not work in these cases, it is likely damaged. Contact the module manufacturer for further troubleshooting.

Refer to the KnowledgeBase or product manuals section at `ni.com` for more information specific to the chassis and controller with which you are having difficulties.

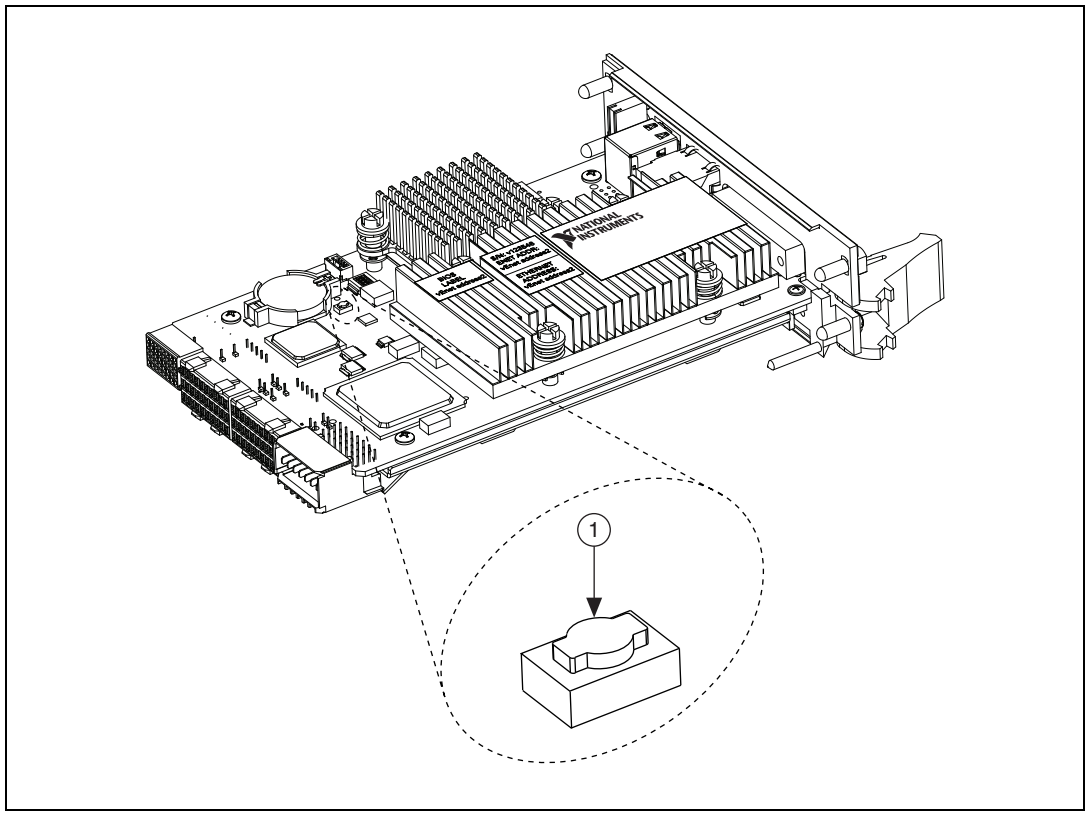
My CMOS is corrupted. How do I set it back to default?

1. Enter the BIOS setup program as described in the *Accessing BIOS Setup Utility* section of Chapter 2, *Installation and Configuration*.
2. Press <F9> to load BIOS defaults.
3. Answer **Y** (Yes) to the verification prompt.
4. Select **Save and Exit Setup**.

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As an alternative method, complete the following steps:

1. Power off the chassis.
2. Remove the controller from the chassis.
3. Press and hold down the pushbutton switch SW2 for 2–3 seconds. The SW2 switch location is shown in Figure 5-1.
4. Reinstall the controller in the chassis.



1 Push-Button Switch S2

Figure 5-1. Clearing the CMOS Contents

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A

Specifications

This appendix lists the electrical, mechanical, and environmental specifications of the NI PXIe-8101/8102 embedded controller.

Features

NI PXIe-8101/8102	
CPU—NI PXIe-8101	Intel® Celeron® Processor 575 (2.00 GHz single core processor), 667 MHz FSB
CPU—NI PXIe-8102	Intel® Dual-Core T3100 (1.9 GHz dual core processor), 800 MHz FSB
On-die L2 cache	1 MB
Single-Channel DDR2 RAM, PC2 6400	1 GB Standard 4 GB Maximum
Hard Drive	80 GB Serial ATA, minimum*
Ethernet	10/100/1000 BaseTX
PXI Express 4 Link Configuration	x1, x1, x1, x1
PXI Express 2 Link Configuration	x1, x1
Serial Ports (RS-232)	Yes (1)
Parallel Port	Yes (1)
Hi-Speed USB (2.0) Ports	Yes (2)
PS/2 Keyboard/Mouse Connector	No
PXI Trigger Bus Input/Output	Yes
Installed Operating System	Windows 7 Professional, Windows Vista Business, Windows Vista Business downgraded to Windows XP Professional
* Controllers configured for LabVIEW RT provide a 80 GB (minimum) SATA hard drive.	

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Electrical

NI PXIe-8101

Voltage (V)	Current (Amps)	
	Typical	Maximum
+3.3 V	2.25 A	4.75 A
+5 V	1.25 A	2.25 A
+12 V	1.5 A	2.25 A
–12 V	0 A	0 A
+5 V Aux	.330 A	.400 A
Note: Does not include any attached USB devices.		

NI PXIe-8102

Voltage (V)	Current (Amps)	
	Typical	Maximum
+3.3 V	2.25 A	4.75 A
+5 V	1.25 A	2.25 A
+12 V	1 A	2.5 A
–12 V	0 A	0 A
+5 V Aux	.330 A	.400 A
Note: Does not include any attached USB devices.		

Physical

Board dimensions .....2-slot 3U PXI module
4.0 cm × 13.0 cm × 21.6 cm
(1.59 in. × 5.14 in. × 8.51 in.)

Slot requirements.....One system slot plus
one controller expansion slot

Compatibility .....Fully compatible with
PXI specification

Weight .....0.645 kg (1.42 lb) typical

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Environment

Maximum altitude ..... 2,000 m (800 mbar)
(at 25 °C ambient temperature)

Pollution Degree 2

Indoor use only.

Caution Clean the NI PXIe-8101/8102 with a soft nonmetallic brush. Make sure that the device is completely dry and free from contaminants before returning it to service.

Operating Environment

NI PXIe-8101/8102

Ambient temperature range ..... 5 to 50 °C[1]
(Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2. Meets MIL-PRF-28800F Class 3 high temperature limit.)

Relative humidity range..... 10% to 90%, noncondensing
(Tested in accordance with IEC-60068-2-56.)

Caution The operating temperature must not be exceeded, even when used in a chassis with a higher temperature range.

Storage Environment

NI PXIe-8101/8102

Ambient temperature range ..... –40 to 65 °C
(Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2. Meets MIL-PRF-28800F Class 3 low temperature limit.)

Relative humidity range..... 5% to 95%, noncondensing
(Tested in accordance with IEC-60068-2-56.)

[1] Processor should not throttle CPU frequency under reasonable, worst case processor work loads in high operating temperatures.

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Shock and Vibration

Operating Shock .....30 g peak, half-sine, 11 ms pulse
(Tested in accordance with IEC-60068-2-27. Meets MIL-PRF-28800F Class 2 limits.)

Random Vibration

Operating .....5 to 500 Hz, 0.3 g_{rms}
(with solid-state hard drive)

Nonoperating .....5 to 500 Hz, 2.4 g_{rms}
(Tested in accordance with IEC-60068-2-64. Nonoperating test profile exceeds the requirements of MIL-PRF-28800F, Class 3.)

Note Specifications are subject to change without notice.

Safety

This product is designed to meet the requirements of the following standards of safety for electrical equipment for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA 61010-1

Note For UL and other safety certifications, refer to the product label or the *Online Product Certification* section.

Electromagnetic Compatibility

This product is designed to meet the requirements of the following standards of EMC for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- AS/NZS CISPR 11: Group 1, Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions

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Note For EMC declarations and certifications, refer to the *Online Product Certification* section.

Note For EMC compliance, operate this device with shielded cables and accessories.

CE Compliance CE

This product meets the essential requirements of applicable European Directives as follows:

- 2006/95/EC; Low-Voltage Directive (safety)
- 2004/108/EC; Electromagnetic Compatibility Directive (EMC)

Online Product Certification

To obtain product certifications and the Declaration of Conformity (DoC) for this product, visit `ni.com/certification`, search by model number or product line, and click the appropriate link in the Certification column.

Environmental Management

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the *NI and the Environment* Web page at `ni.com/environment`. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

Waste Electrical and Electronic Equipment (WEEE)

EU Customers At the end of the product life cycle, all products *must* be sent to a WEEE recycling center. For more information about WEEE recycling centers, National Instruments WEEE initiatives, and compliance with WEEE Directive 2002/96/EC on Waste and Electronic Equipment, visit `ni.com/environment/weee`.

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Battery Replacement and Disposal



Battery Directive This device contains a long-life coin cell battery. If you need to replace it, use the Return Material Authorization (RMA) process or contact an authorized National Instruments service representative. For more information about compliance with the EU Battery Directive 2006/66/EC about Batteries and Accumulators and Waste Batteries and Accumulators, visit `ni.com/environment/batterydirective`.

电子信息产品污染控制管理办法 （中国 RoHS）



中国客户 National Instruments 符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。
关于 National Instruments 中国 RoHS 合规性信息，请登录 `ni.com/environment/rohs_china`。
(For information about China RoHS compliance, go to `ni.com/environment/rohs_china`.)

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B

Technical Support and Professional Services

Visit the following sections of the award-winning National Instruments Web site at `ni.com` for technical support and professional services:

- **Support**—Technical support at `ni.com/support` includes the following resources:
  - **Self-Help Technical Resources**—For answers and solutions, visit `ni.com/support` for software drivers and updates, a searchable KnowledgeBase, product manuals, step-by-step troubleshooting wizards, thousands of example programs, tutorials, application notes, instrument drivers, and so on. Registered users also receive access to the NI Discussion Forums at `ni.com/forums`. NI Applications Engineers make sure every question submitted online receives an answer.
  - **Standard Service Program Membership**—This program entitles members to direct access to NI Applications Engineers via phone and email for one-to-one technical support as well as exclusive access to on demand training modules via the Services Resource Center. NI offers complementary membership for a full year after purchase, after which you may renew to continue your benefits.

    For information about other technical support options in your area, visit `ni.com/services`, or contact your local office at `ni.com/contact`.
- **Training and Certification**—Visit `ni.com/training` for self-paced training, eLearning virtual classrooms, interactive CDs, and Certification program information. You also can register for instructor-led, hands-on courses at locations around the world.
- **System Integration**—If you have time constraints, limited in-house technical resources, or other project challenges, National Instruments Alliance Partner members can help. To learn more, call your local NI office or visit `ni.com/alliance`.

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- **Declaration of Conformity (DoC)**—A DoC is our claim of compliance with the Council of the European Communities using the manufacturer's declaration of conformity. This system affords the user protection for electromagnetic compatibility (EMC) and product safety. You can obtain the DoC for your product by visiting `ni.com/certification`.

If you searched `ni.com` and could not find the answers you need, contact your local office or NI corporate headquarters. Phone numbers for our worldwide offices are listed at the front of this manual. You also can visit the Worldwide Offices section of `ni.com/niglobal` to access the branch office Web sites, which provide up-to-date contact information, support phone numbers, email addresses, and current events.

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Glossary

Symbol	Prefix	Value
n	nano	10^{-9}
µ	micro	10^{-6}
m	milli	10^{-3}
k	kilo	10^{3}
M	mega	10^{6}
G	giga	10^{9}
T	tera	10^{12}

Symbols

°	Degrees.
Ω	Ohms.
%	Percent.

A

A	Amperes.
AC	Alternating Current.

B

B	Bytes.
backplane	An assembly, typically a printed circuit board, with connectors and signal paths that bus the connector pins.
BIOS	Basic Input/Output System—BIOS functions are the fundamental level of any PC or compatible computer. BIOS functions embody the basic operations needed for successful use of the computer's hardware resources.

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C

C	Celsius.
cache	Small portion of high-speed memory used for temporary storage of frequently used data.
CMOS	Complementary Metal Oxide Semiconductor—A process used in making chips.
CompactPCI Express	An adaptation of the PCI specification for industrial and/or embedded applications that require a more robust mechanical form factor than desktop PCI. CompactPCI Express provides a standard form factor for those applications requiring the high performance of PCI as well as the small size and ruggedness of a rack-mount system.

D

DC	Direct Current.
DDR	Double Data Rate.
DMA	Direct Memory Access—A method by which data is transferred between devices and internal memory without intervention of the central processing unit.
DVI	Digital Visual Interface—A video connector designed to maximize the visual quality of digital display devices such as flat panel LCD computer displays and digital projectors. It was developed by an industry consortium, the Digital Display Working Group (DDWG).

E

ECP	Extended Capabilities Parallel.
EEPROM	Electronically Erasable Programmable Read Only Memory.
EMC	Electromagnetic Compatibility.
EMI	Electromagnetic interference.

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EPP	Enhanced Parallel Port.
expansion ROM	An onboard EEPROM that may contain device-specific initialization and system boot functionality.

F

FCC	Federal Communications Commission.

G

g	1. Grams. 2. A measure of acceleration equal to 9.8 m/s^2.
g_{rms}	A measure of random vibration—The root mean square of acceleration levels in a random vibration test profile.

H

Hz	Hertz—Cycles per second.

I

I/O	Input/Output—The techniques, media, and devices used to achieve communication between machines and users.
IDE	Integrated Drive Electronics—Hard disk and built-in controller.
IEEE	Institute of Electrical and Electronics Engineers.
in.	Inches.
instrument driver	A set of routines designed to control a specific instrument or family of instruments, and any necessary related files for LabWindows/CVI or LabVIEW.
interrupt	A means for a device to request service from another device.
interrupt level	The relative priority at which a device can interrupt.

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IRQ#	Interrupt signal.
ISA	Industry Standard Architecture—The original PC bus architecture, specifically the 16-bit AT bus.

K

KB	Kilobytes of memory.

L

LAN	Local Area Network—Communications network that serves users within a confined geographical area. It is made up of servers, workstations, a network operating system, and a communications link.
LED	Light-emitting diode.

M

m	Meters.
master	A functional part of a PXI device that initiates data transfers on the PXI backplane. A transfer can be either a read or a write.
MB	Megabytes of memory.
MTBF	Mean time between failure.

N

NI-DAQ	The National Instruments software for data acquisition instruments.
NI-VISA	The National Instruments implementation of the VISA standard—An interface-independent software that provides a unified programming interface for VXI and serial instruments.

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P

PCI	Peripheral Component Interconnect—The PCI bus is a high-performance 32-bit or 64-bit bus with multiplexed address and data lines.
PCMCIA	Personal Computer Memory Card International Association.
peripheral	Any hardware device connected to a computer, such as a monitor, keyboard, printer, plotter, disk or tape drive, graphics tablet, scanner, mouse, and so on.
PXI Express	PCI eXtensions for Instrumentation—An open implementation of CompactPCI Express that adds electrical features that meet the high-performance requirements of instrumentation applications by providing triggering, local buses, and system clock capabilities. PXI Express also offers two-way interoperability with CompactPCI Express products.

R

RAM	Random Access Memory—The computer's primary workspace.
resource	Hardware settings used by devices in a computer system, including ISA interrupt level, DMA channel, and I/O address.
RMS	Root mean squared. *See also* g_{rms}.

S

s	Seconds.
SDRAM	Synchronous Dynamic RAM (Random Access Memory)—Storage that the computer must refresh at frequent intervals.
SO-DIMM	Small Outline Dual In-line Memory Module.
SPI Bus	Serial Peripheral Interface—A standard for controlling most any digital electronics that accept a clocked serial stream of bits.

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U

USB	Universal Serial Bus.

V

V	Volts.
VGA	Video Graphics Array—The minimum video display standard for all PCs.

W

W	Watts.

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