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- Virtex-5 LX50 FPGA programmable with the LabVIEW FPGA Module
- 128 MB onboard DDR2 DRAM
- Access to 132 single-ended I/O lines configurable as 66 differential pairs
- Customizable I/O with the NI FlexRIO Adapter Module Development Kit (MDK)
- 100 MHz digital I/O with the NI 6581 adapter module for NI FlexRIO
- **Check Resources tab for compatible adapter modules**
- Information based on current version

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