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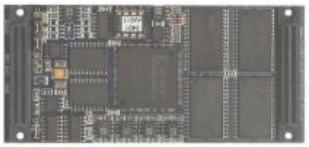
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Industry Packs

Industry Packs add to the flexibility of VME64x, these plug-in cards are based on an industry standard format and are designed to have wide application. They are very compact, 1.8 inches by 3.9 inches in size and they include an ID PROM which aids auto-configuration. They have connectors that are keyed for ease of installation. There is a broad range of products already available from a wide selection of suppliers all over the world. Software support is available from Hytec.



IP-ADC-8401

An 8-channel simultaneous sampling, 16 bit resolution ADC Industry Pack. Eight full differential inputs, input voltage range +/-10 Volts (or +/-5 Volts order option IP-ADC-8401-L) Built in memory, 1Mb or 2Mb option. Sample and hold circuit is built in, as is the programmable timer. Trigger is by the timer or the external input, at up to 100KHz update rate on all eight inputs. Accurate to 13.5 bits. With software calibration +/-0.01% absolute accuracy. The whole ADC may be isolated via a transition board and a single DC/DC converter. Inputs withstand +/-30 V.

IP-DAC-8402

16-channel 16bit resolution (13bit AA) DAC +/-10V (or +/-5 Volts order Option IP-DAC-8402-L) O/P. Absolute accuracy with software calibration 0.01%. Industry Pack contains 16 separate DAC's which may be isolated as a group, via the Transition Board and a DC/DC converter. The 2 Mb memory may be loaded and the DAC outputs triggered, by the internal program timer or by the external trigger input. This forms a simple arbitrary waveform generator. It can generate an output sequence or continuous waveform (16-32 Kilo words at <30KHz), via a memory time loop 16-1M words long for all 16 outputs. EPICS driver available.



IP-MUXADC-8403.1



DC 20bit accurate, 24 bit resolution, 16-channel multiplexed, ADC Industry Pack with 1MB RAM (Order Option IP-MUXADC-8403.2 2MB RAM). Absolute accuracy up to 19bits (less on high gain). The pack contains two eight input Sigma Delta micro controlled ADC chips with input amplifier which may be programmed via the IP interface. This is a slow ADC device, which will give about ten readings per second for each chip in to the local memory. The chips have provision for Opto-isolation via the Transition Board and a DC/DC converte There are a number of modes, which include the use of ON ch program to set DAC current sources for excitation of transducers. Open circuit detection, ratio-metric, resistance and thermocouple measurements. The ADC chip has a lot of attributes covered in the main specification. EPICS driver available.

IP-IDI-8501

Provides routing of 32 digital inputs or outputs when plugged into sites A and B of the 8001 Carrier Board. The pack includes an FPGA which is programmed to route the data and latch it to be read via the carrier board. Different variants (input and output) can be supplied by re-programming the FPGA's boot PROM. Special variants can be supplied for different I/O combinations.

IP-DIO-8505

Sixteen channels of buffered digital input/output. These are accessed as the top and bottom halves of one 16-bit location. Each set of eight signals can be selected to be inputs or outputs. At power-up or after an IP reset, all signals default to the input state. Reading the port when output mode is selected will read back the last data written. A further register associated with each port controls which bits may generate an interrupt when in input mode. All the above registers occupy I/O space on the IP card.

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