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Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
199485A-01L to 19485D01L	**NI PXIe-1078**
199485B-01L to 19485D01L	**NI PXIe-1078 TCXO**

Volatile Memory

Target Data	*Type*	*Size*	*Battery Backup*	*User[1] Accessible*	*System Accessible*	*Sanitization Procedure*
Fan/Voltage/Temp Monitor and Fan Control	SRAM	2 KB	No	No	No	Cycle Power

Non-Volatile Memory (*incl. Media Storage*)

Target Data	*Type*	*Size*	*Battery Backup*	*User Accessible*	*System Accessible*	*Sanitization Procedure*
PCIe switch configuration	EEPROM	256 KB	No	No	Yes	None
Bridge configuration	EEPROM	(x2)2 Kb	No	No	Yes	None
Chassis Descriptor configuration	EEPROM	2 Kb	No	No	Yes	None
Fan/Voltage/Temp Monitor and Fan Control	Flash	32 KB	No	No	No	None
Backplane clock generation/configuration	CPLD	440 MacroCells	No	No	No	None

[1] Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

Manufacturer: National Instruments

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
199485E-01L or later	**NI PXIe-1078**
199485E-03L or later	**NI PXIe-1078 OCXO**

Volatile Memory

Target Data	*Type*	*Size*	*Battery Backup*	*User[2] Accessible*	*System Accessible*	*Sanitization Procedure*
Fan/Voltage/Temp Monitor and Fan Control	SRAM	2 KB	No	No	No	Cycle Power

Non-Volatile Memory (*incl. Media Storage*)

Target Data	*Type*	*Size*	*Battery Backup*	*User Accessible*	*System Accessible*	*Sanitization Procedure*
PCIe switch configuration	EEPROM	256 KB	No	No	Yes	None
Bridge configuration	EEPROM	2 Kb	No	No	Yes	None
Chassis Descriptor configuration	EEPROM	2 Kb	No	No	Yes	None
Fan/Voltage/Temp Monitor and Fan Control	Flash	32 KB	No	No	No	None
Backplane clock generation/configuration	CPLD	440 MacroCells	No	No	No	None

[2] Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

Procedures

Procedure 1 –Board Assembly Part Number Identification:
To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: #####a-##L

Terms and Definitions

Cycle Power:
The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:
Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:
Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:
The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:
The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:
Per *NIST Special Publication 800-88 Revision 1*, "clearing" is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:
Per *NIST Special Publication 800-88 Revision 1*, "sanitization" is a process to render access to "Target Data" on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.

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