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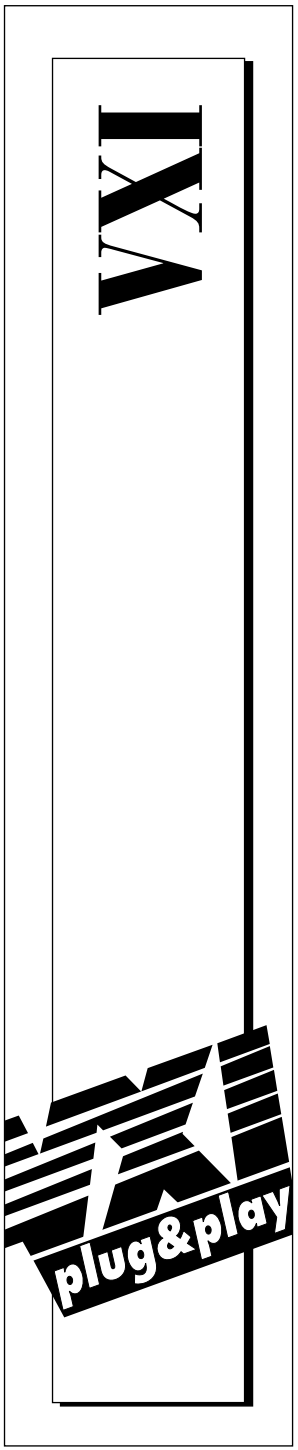
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VXIpc™ 800/700 Series User Manual

April 1997 Edition
Part Number 321124C-01

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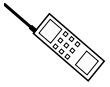
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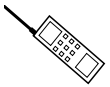
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If necessary, consult National Instruments or an experienced radio/television technician for additional suggestions. The following booklet prepared by the FCC may also be helpful: *Interference to Home Electronic Entertainment Equipment Handbook*. This booklet is available from the U.S. Government Printing Office, Washington, DC 20402.

Canadian Department of Communications

This Class A digital apparatus meets all requirements of the Canadian Interference-Causing Equipment Regulations.

Cet appareil numérique de la classe A respecte toutes les exigences du Règlement sur le matériel brouilleur du Canada.

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About This Manual

This manual contains instructions for installing and configuring the National Instruments VXIpc 800/700 Series embedded computer kit. The VXIpc 800/700 Series includes all the models of the VXIpc-800 and VXIpc-700 embedded computers.

Organization of This Manual

This manual is organized as follows:

- Chapter 1, *Introduction*, describes the VXIpc 800/700 Series of embedded VXI computers, lists what you need to get started, describes the hardware, and lists optional equipment and software.
- Chapter 2, *Functional Overview*, contains functional descriptions of each major logic block on the VXIpc 800/700 Series embedded computers.
- Chapter 3, *VXIpc 800 Series Configuration and Installation*, contains the instructions to configure and install the VXIpc 800 Series embedded computer.
- Chapter 4, *VXIpc 700 Series Configuration and Installation*, contains the instructions to configure and install the VXIpc 700 Series embedded computer.
- Chapter 5, *BIOS*, contains information on BIOS, the low-level interface between the hardware and PC software that configures and tests your hardware at boot up.
- Appendix A, *Specifications*, describes the environmental, electrical, and mechanical specifications of the VXIpc 800/700 Series embedded computer.
- Appendix B, *VXIpc 800 Series System Resources*, describes what system resources are available on the VXIpc 800 Series and where they are allocated.
- Appendix C, *VXIpc 700 Series System Resources*, describes what system resources are available on the VXIpc 700 Series and where they are allocated.

- Appendix D, *LED Indicators*, describes how to read the LEDs on the front panel to interpret the status of the VXIpc 800/700 Series.
- Appendix E, *Front Panel and Connectors*, describes the front panel and connectors on the VXIpc 800/700 Series.
- Appendix F, *Modifying and Installing I/O Expansion Boards*, explains how to modify and install an I/O board in the VXIpc 800 Series. This material is not relevant to the VXIpc 700 Series.
- Appendix G, *Common Questions*, answers common questions you may have when using the VXIpc 800/700 Series.
- Appendix H, *Customer Communication*, contains forms you can use to request help from National Instruments or to comment on our products and manuals.
- The *Glossary* contains an alphabetical list and description of terms used in this manual, including abbreviations, acronyms, metric prefixes, mnemonics, and symbols.
- The *Index* contains an alphabetical list of key terms and topics used in this manual, including the page where you can find each one.

Conventions Used in This Manual

The following conventions are used in this manual:

< >	Angle brackets enclose the name of a key on the keyboard (for example, <Option>.
-	A hyphen between two or more key names enclosed in angle brackets denotes that you should simultaneously press the named keys—for example, <Control-Alt-Delete>.
♦	The ♦ symbol indicates that the text following it applies only to a specific product, a specific operating system, or a specific software version.
bold	Bold text denotes the names of menus, menu items, dialog box buttons or options, or LEDs.
bold italic	Bold italic text denotes a note, caution, or warning.
`bold monospace`	Bold text in this font denotes the messages and responses that the computer automatically prints to the screen.
italic	Italic text denotes emphasis, a cross reference, or an introduction to a key concept. This font also denotes text from which you supply the appropriate word or value, as in Windows 3.*x*.

`monospace`	Text in this font denotes text or characters that are to be literally input from the keyboard, sections of code, programming examples, and syntax examples. This font is also used for the proper names of disk drives, paths, directories, programs, subprograms, subroutines, device names, functions, variables, filenames, and extensions.
VXIpc 700 Series	The terms *VXIpc 700 Series* and *VXIpc-700* refer to a series of C-size, single-slot VXI controllers. Currently, this series consists of the VXIpc-740 and VXIpc-745.
VXIpc 800 Series	The terms *VXIpc 800 Series* and *VXIpc-800* refer to a series of C-size, dual-slot VXI controllers. Currently, this series consists of the VXIpc-850 and VXIpc-860, with various processors and in different speeds.
VXIpc 800/700 Series	The term *VXIpc 800/700 Series* refers to all models of the VXIpc 800 and VXIpc 700 Series.
	Abbreviations, acronyms, metric prefixes, mnemonics, symbols, and terms are listed in the *Glossary*.

How to Use This Documentation Set

Begin by reading the *Getting Started with Your VXIpc 800/700 Series* manual for basic instructions on setting up the hardware and software. This is a brief quick start manual that describes how to get started with your kit using the default hardware and software settings. Refer to the following manuals for more information about the hardware or software.

This manual, the *VXIpc 800/700 Series User Manual*, contains more details about changing the installation or configuration from the defaults, and using the hardware.

The *NI-VXI Software Manual for the VXIpc 800/700 Series* contains more details about changing the NI-VXI software installation or configuration from the defaults, and using the NI-VXI software on the VXIpc 800/700. This manual is installed on your hard drive in the `c:\images\manuals` directory. Use the Acrobat Reader program to open the file called `NIVXISM.pdf`.

When you are familiar with the material in these manuals, you can begin to use the *NI-VXI User Manual*. This manual presents the concepts of VXI and prepares you for detailed explanations of the NI-VXI functions. Study the descriptions of each function given in the *NI-VXI*

Programmer Reference Manual to fully understand the purpose and syntax of each function. These two manuals are also availaable in the `c:\images\manuals` directory under the names `NIVXIUM.pdf` and `NIVXIPRM.pdf`, respectively.

You can also access online help for Windows 95/NT/3.1 in the NI-VXI folder.

Refer to the *NI-VXI Graphical Utilities Reference Manual* and the *NI-VXI Text Utilities Reference Manual* to learn more about the NI-VXI utilities.

Related Documentation

The following documents contain information that you may find helpful as you read this manual:

- ANSI/IEEE Standard 1014-1987, *IEEE Standard for a Versatile Backplane Bus: VMEbus*
- ANSI/IEEE Standard 1155-1993, *IEEE VMEbus Extensions for Instrumentation: VXIbus*
- ANSI/VITA 1-1994, *VME64*
- VXI-6, *VXIbus Mainframe Extender Specification*, Rev. 1.0, VXIbus Consortium

Customer Communication

National Instruments wants to receive your comments on our products and manuals. We are interested in the applications you develop with our products, and we want to help if you have problems with them. To make it easy for you to contact us, this manual contains comment and configuration forms for you to complete. These forms are in Appendix H, *Customer Communication*, at the end of this manual.

Chapter 1

Introduction

This chapter describes the VXIpc 800/700 Series of embedded VXI computers, lists what you need to get started, describes the hardware, and lists optional equipment and software. The VXIpc 800/700 Series includes the two-slot VXIpc 800 Series and the one-slot VXIpc 700 Series. This manual describes these computers together as the VXIpc 800/700 Series when functionality applies to all models.

Overview

Figure 1-1 shows a VXIpc-850 model with its cover removed.

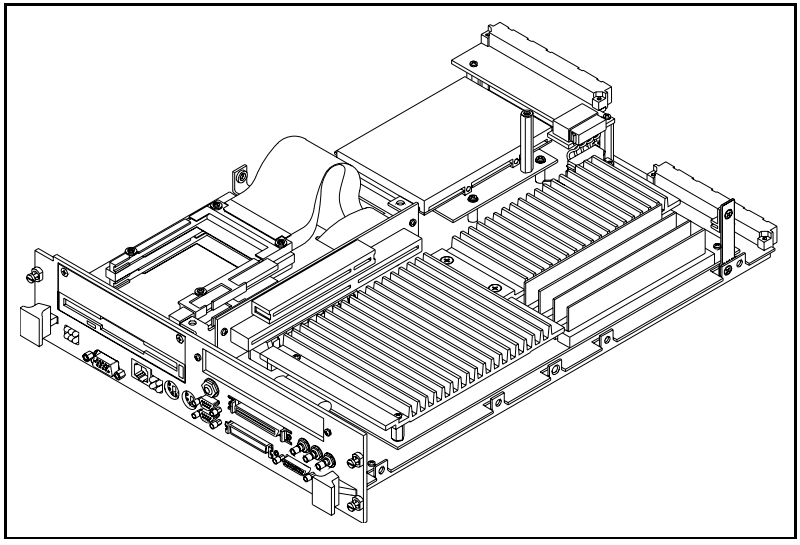


Figure 1-1. VXIpc-850 Embedded Computer

Figure 1-2 shows a VXIpc-860 model with its cover removed.

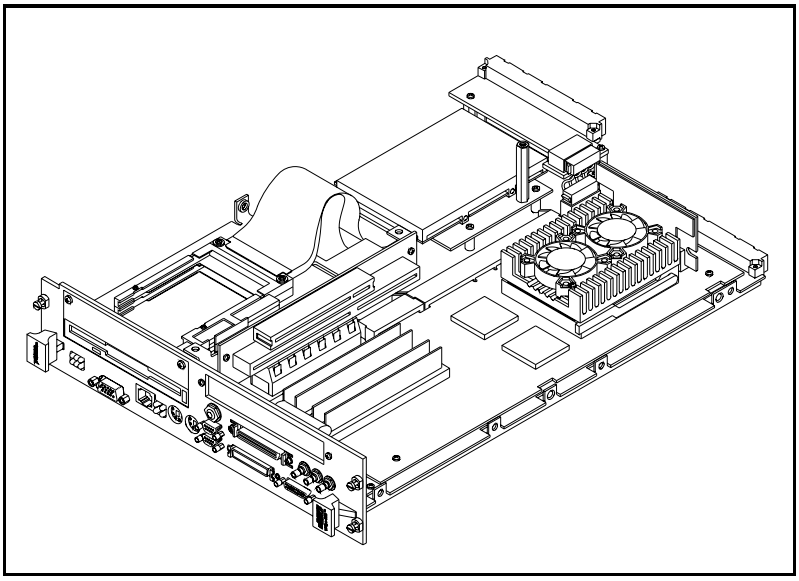


Figure 1-2. VXIpc-860 Embedded Computer

The VXIpc 700 Series includes the VXIpc-740 and VXIpc-745 models. Figure 1-3 shows the VXIpc-740 and Figure 1-4 shows the VXIpc-745. Notice the differences in the front panel. The VXIpc-745 has front-panel connections for Ethernet and IEEE 488.2 in addition to all the functionality of the VXIpc-740.

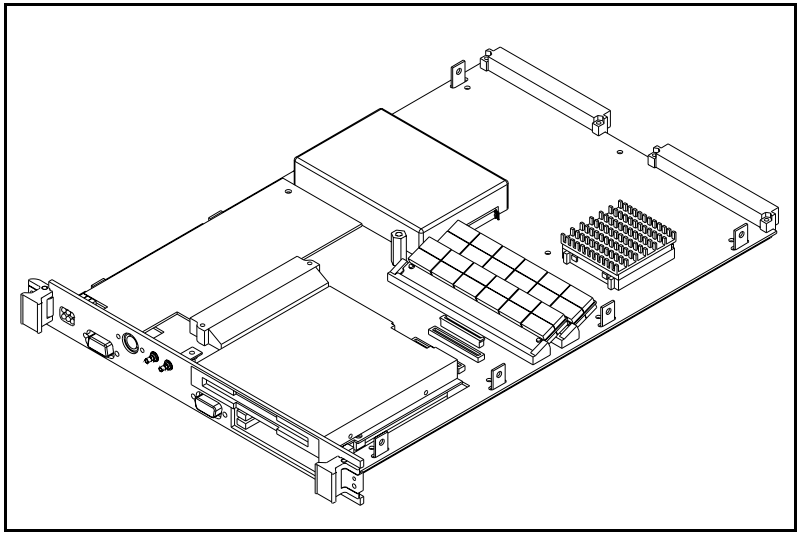


Figure 1-3. VXIpc-740 Embedded Computer

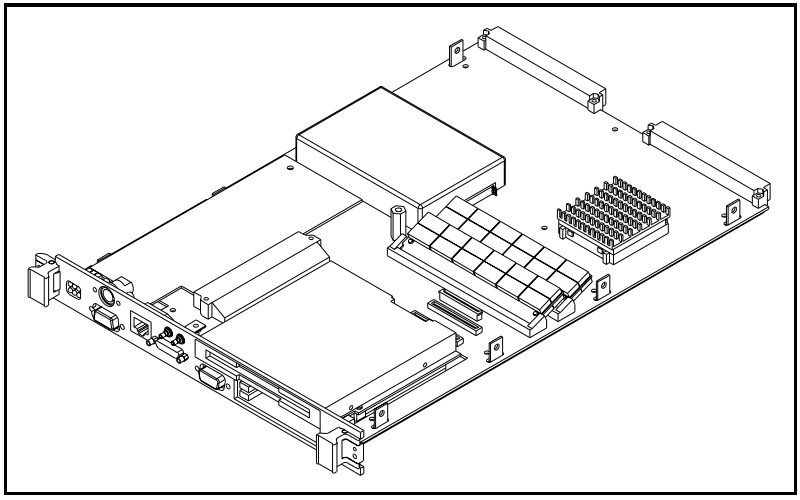


Figure 1-4. VXIpc-745 Embedded Computer

The VXIpc 800/700 Series controllers are C-size, embedded computers based on the Peripheral Component Interface (PCI) bus and Industry Standard Architecture (ISA). These computers are high-performance, easy-to-use platforms for controlling VXIbus systems, featuring complete VXI functionality through interactive utilities and C function calls. In addition, the VXIpc 800 Series and the VXIpc-745 have

Ethernet capability plus an IEEE 488.2 interface that is compatible with the NI-488.2 architecture. You can install a PCMCIA-GPIB card in a PCMCIA slot to get IEEE 488.2 capability on the VXIpc-740.

The VXIpc 800 Series is a custom computer that you install directly in two C-size slots of your VXIbus mainframe, while the VXIpc 700 Series requires only one slot. An embedded computer can take full advantage of the VXI high-performance backplane capabilities and give you direct control of VXI registers, memory, interrupts, and triggers.

All models in the VXIpc 800/700 Series are fully VXI*plug&play* compliant and are compatible with PC-compatible software tools, the National Instruments LabVIEW and LabWindows®/CVI application software, and the NI-VXI, NI-VISA, and NI-488.2 bus interface software.

Optional Equipment

You can contact National Instruments to order any of the following optional equipment:

- COM1/2 adapter cable for VXIpc 800 Series
- Enhanced parallel port adapter cable for VXIpc 800 Series
- Single-shielded 2 m GPIB cable for VXIpc 800 Series and VXIpc-745
- Upgrades for the VXIpc 800 Series modular CPU card

Optional Software

National Instruments has developed several software kits that you can use with the VXIpc 800/700 Series. The NI-VXI bus interface software for the VXIpc 800/700 includes a Resource Manager, graphical and text-based versions of an interactive VXI resource editor program, a comprehensive library of software routines for VXI/VME programming, and an interactive control program for interacting with VXI/VME. You can use this software to seamlessly program multiple-mainframe configurations and have software compatibility across a variety of VXI/VME controller platforms.

The NI-488.2 software kit gives you accessibility to the industry-standard NI-488.2 software for controlling external GPIB instruments through the GPIB port on the front panel of your VXIpc 800

Series or VXIpc-745, or through a PCMCIA slot on the VXIpc-740. The GPIB interface on your VXIpc controller is fully compatible with the NI-488.2 driver for a variety of operating systems. Any software using NI-488.2 will run on the VXIpc 800/700.

You can also use the National Instruments LabVIEW and LabWindows/CVI application programs and instrument drivers to ease your programming task. These standardized programs match the modular virtual instrument capability of VXI and can reduce your VXI/VMEbus software development time. These programs are fully VXI*plug&play* compliant and feature extensive libraries of VXI instrument drivers written to take full advantage of direct VXI control.

LabVIEW is a complete programming environment that departs from the sequential nature of traditional programming languages and features a graphical programming environment.

LabWindows/CVI is an interactive C development environment for building test and measurement and instrument control systems. It includes interactive code-generation tools and a graphical editor for building custom user interfaces.

LabVIEW and LabWindows/CVI include all the tools needed for instrument control, data acquisition, analysis, and presentation. When you order the LabVIEW Full Development System for Windows or the LabWindows/CVI Full Development System for Windows, you also get more than 500 complete instrument drivers, which are modular, source-code programs that handle the communication with your instrument to speed your application development.

Hardware Description

The VXIpc 800 Series controllers feature a modular, PCI-based local bus design that makes it easy to use interchangeable daughterboards to upgrade your controller to new microprocessors, without having to replace the motherboard or enclosure.

The VXIpc 800 Series currently consists of two models—the VXIpc-850 and the VXIpc-860—which vary in processors and speeds.

The VXIpc 700 Series consists of two models—the VXIpc-740 and the VXIpc-745. Both models use a 486 class microprocessor running at 100 MHz.

The CPU module and its memory sockets are easily accessible for you to install additional DRAM in the field:

- Up to 128 MB for the VXIpc-850
- Up to 256 MB for the VXIpc-860
- Up to 64 MB for the VXIpc 700 Series

The VXIpc 800/700 contains at least an 800 MB internal, enhanced IDE hard disk. For information on adding RAM by installing SIMMs, refer to Appendix A, *Specifications*.

Slot 0 Functionality

You can use the VXIpc 800/700 Series computers to achieve full VXI Slot 0 control of your VXI system. You can also install the VXIpc 800/700 in another slot and use it in Non-Slot 0 mode. You do not have to change any switches or jumpers when moving between these two modes, as the VXIpc 800/700 can automatically detect whether it is installed in Slot 0 and it will automatically enable or disable the Slot 0 onboard circuitry.

Custom Application-Specific Interface Chips

The VXIpc 800/700 Series uses the MITE and MANTIS custom ASICs to deliver high VXI performance, and can achieve more than 20 MB/s DMA block-mode data transfer rates across the VXI backplane.

The VXIpc 800 Series and the VXIpc-745 also have the TNT4882C custom ASIC to give full GPIB control of external instruments via a front-panel connector. This chip is also on the National Instruments PCMCIA-GPIB card that you can connect to the VXIpc-740 to achieve the same functionality. GPIB capability is fully compatible with IEEE 488.2 and the industry-standard NI-488.2 driver for a variety of operating systems.

The MITE custom ASIC is a sophisticated dual-channel DMA controller with standard interfaces for VXI and PCI. By using MITE DMA to transfer data and commands to and from devices, the MITE frees the computer's microprocessor to perform other tasks such as data analysis and presentation. In addition to DMA, the MITE incorporates the new VME64 MBLT (8-byte block transfers in which both the address bus and data bus are used to transfer data) directly into the

ASIC to perform the fastest transfer operation to instruments. With the multiple windowing scheme of the MITE, you can easily access all of VXI address space.

The VXI trigger interface on the VXIpc 800/700 is based on the MANTIS custom ASIC. The VXIpc 800/700 front panel has two SMB trigger I/O connectors, which you can use to route any of the TTL trigger lines between the backplane and external devices. The MANTIS ASIC on the VXIpc 800/700 provides the complete VXI interface to the backplane connector in a single chip. The VXIpc 800/700 can respond to all VXI-defined protocols on all P2 TTL and ECL trigger lines at the same time. The MANTIS features an internal cross-matrix switching system for routing between lines as well as to and from the front panel and onboard clocks.

☞ **Note:** ***The MANTIS ASIC contains the exact functionality of the TIC ASIC, which appeared on the VXIpc-486 Model 500 Series controllers. Any application that currently uses any of the TIC functionality, such as the crosspoint switch and counter/timers, can run on a controller containing the MANTIS ASIC without modification.***

Front Panel Features

The VXIpc 800/700 has the following front-panel features:

- PCMCIA slots
  - The VXIpc 800 Series has one Type I/II and one Type I/II/III slot.
  - The VXIpc 700 Series has either two Type I/II slots or one Type III slot.
- One full-size expansion slot (on the VXIpc-850 only) that accepts either a PCI or a PC ISA (XT-height) plug-in board
- One half-length expansion slot (on the VXIpc-860 only) that accepts either a PCI or a PC ISA (XT-height) plug-in board
- Internal 3.5 in. floppy drive
- System reset push-button
- Up to 13 front-panel connectors as listed in the following table

Connector	VXIpc-800	VXIpc-740	VXIpc-745
RS-232 Serial	Two	One	One
Extended Capabilities Parallel (ECP)	√		
VGA Controller	√	√	√
IEEE 488.2	√		√
10BaseT Ethernet	√		√
SCSI-2	√		
External Clock	√		
Trigger Output	√	√	√
Trigger Input	√	√	√
Audio Output	√		
PS/2-Style Keyboard	√	√	√
PS/2-Style Mouse	√		

- Six front-panel LEDs that show VXI and PC status
  - **SYSFAIL** LED indicates that the VMEbus SYSFAIL line is asserted.
  - **FAILED** LED indicates that the VXIpc 800/700 is driving the SYSFAIL signal.
  - **TEST** LED indicates that the VXIpc 800/700 is performing its self-tests or startup Resource Manager operations.
  - **ON LINE** LED indicates that the VXIpc 800/700 is performing or has completed its startup Resource Manager operations.
  - **ACCESS** LED indicates when the VXIpc 800/700 MODID line is asserted or the VXIbus registers or shared memory are accessed by another bus master.
  - **DRIVE** LED indicates when the internal hard drive is in use.

- Four front-panel LEDs that show Ethernet port status (VXIpc 800 Series only)
  - **RX** LED indicates that the VXIpc 800 Series is receiving data through its Ethernet port.
  - **TX** LED indicates that the VXIpc 800 Series is transmitting data through its Ethernet port.
  - **DSEL** LED indicates that the Ethernet circuitry is being accessed.
  - **LINK** LED reflects Ethernet link status.

Peripheral Expansion

The VXIpc 800/700 uses the PCI local bus and ISA bus for peripheral expansion. The PCIbus is a 32-bit multimaster bus that achieves a top throughput of 132 MB/s and can handle numerous peripherals. The ISA bus is the legacy peripheral bus found on current and older PCs.

For information on installing and configuring these peripherals for use with the VXIpc 800/700, refer to the `c:\images\manuals` directory. The `drivers.txt` file explains how to install support for each peripheral. This directory also contains manufacturer's documentation for the video, PCMCIA, and SCSI peripherals. Also refer to the `readme` files for the most up-to-date information.

Table 1-1 lists the various peripherals, indicates the VXIpc model(s) on which the peripheral is available, and describes the external connector, its bus interface, and its function.

Table 1-1. VXIpc 800/700 Series Peripherals Overview

Peripheral	Availability	External Connector	ISA or PCI	Function
Video	VXIpc-800, VXIpc-700	15-pin DSUB (standard VGA)	PCI	High-resolution/ color support for a Super VGA monitor
IDE	VXIpc-800, VXIpc-700	None	PCI	Supports internal fast ATA-2 hard drive
Ethernet	VXIpc-800, VXIpc-745	RJ-45	ISA	10BaseT Ethernet connection
PCMCIA	VXIpc-800	Type I/II and Type I/II/III	PCI	Supports two independent PCMCIA slots
	VXIpc-700	Two Type I/II or One Type III		
SCSI	VXIpc-800	36-pin SCSI-2	PCI	External SCSI-2 connection for hard drives, CD-ROM drives, and so on
GPIB	VXIpc-800, VXIpc-745	24-pin CHAMP	ISA	IEEE 488.2 interface compatible with the National Instruments AT-GPIB/TNT
VXI	VXIpc-800, VXIpc-700	Two 96-pin DIN (rear of board)	PCI	High-performance VXIbus interface
Serial	VXIpc-800, VXIpc-700	Serial Port (Mini DSUB)	ISA	16550 serial ports
Parallel	VXIpc-800	Parallel Port (IEEE 1284)	ISA	Extended capabilities

Chapter 2

Functional Overview

This chapter contains functional descriptions of each major logic block on the VXIpc 800/700 Series embedded computers.

VXIpc 800/700 Functional Description

The VXIpc 800/700 is a modular PC in a VXIbus C-size form factor. It includes many high-performance peripherals that normally require add-in cards on desktop PCs. In addition, it has a VXIbus interface that is controlled from the PCI local bus, providing extremely high performance and reliability.

Figure 2-1 is a functional block diagram of the VXIpc 800 Series. Figure 2-2 is a functional block diagram of the VXIpc 700 Series. Following the diagrams is a description of each logic block shown.

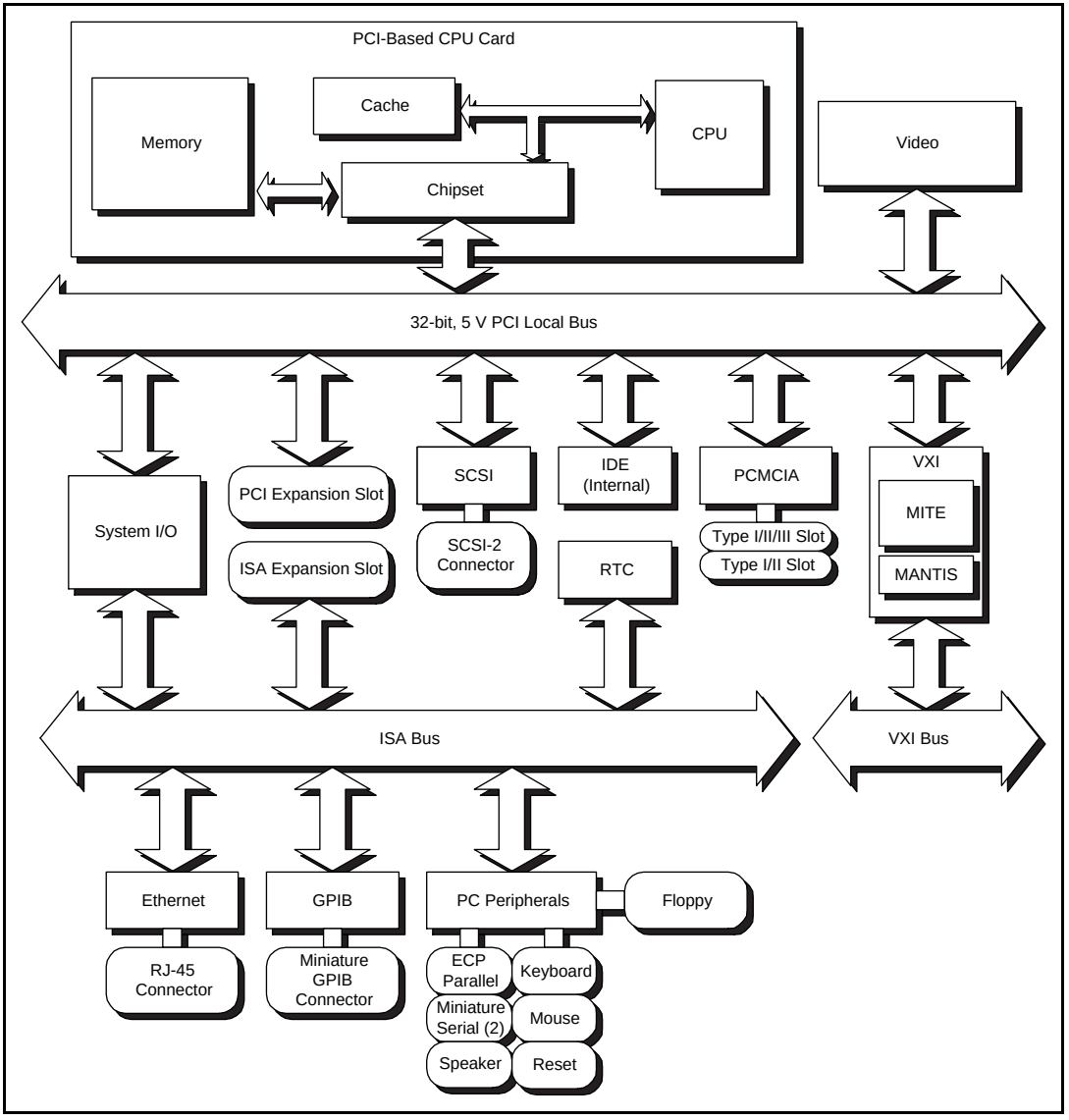


Figure 2-1. VXIpc 800 Series Block Diagram

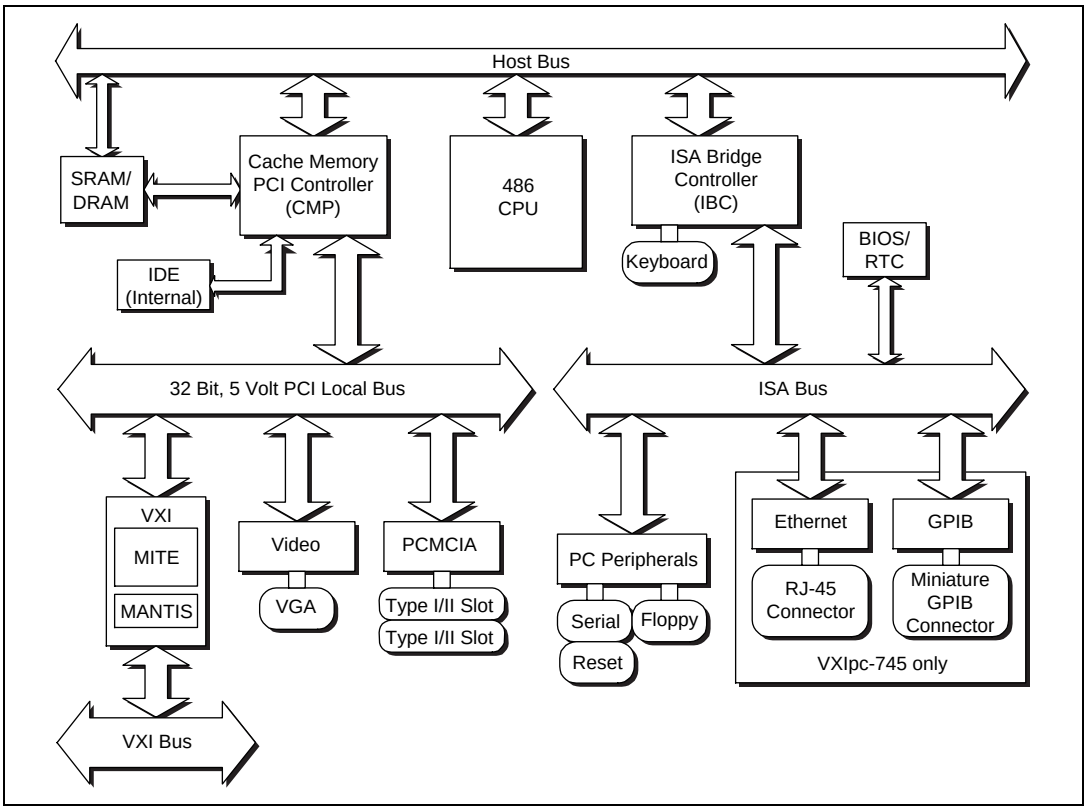


Figure 2-2. VXIpc 700 Series Block Diagram

The VXIpc 800/700 consists of the following logic blocks:

Video The video circuitry is a plug-in PCI card that has a 64-bit data path to up to 4 MB of EDO DRAM.

IDE This is dedicated PCI-IDE circuitry providing fast ATA-2 transfers to the internal hard drive. The IDE for the VXIpc 700 Series is built into the chip set.

PCMCIA This is a dedicated PCI-PCMCIA interface that supports two independent PCMCIA cards. On the VXIpc 800 Series, one socket can accommodate a Type I, II, or III card while

the other accommodates a Type I or II card. On the VXIpc 700 Series, you can install two Type I or II cards or one Type III card.

VXI This is the PCI-VXIbus interface circuitry. The MITE is a National Instruments ASIC developed to efficiently manage data transfers between the VXIbus and the processor (via the PCI bus). The MANTIS ASIC (also developed by National Instruments) performs VXIbus arbitration and manages interrupts and triggers. Also part of the VXIbus interface are the SMB connectors, which you can use to route triggers and the CLK10 signal to or from the VXIbus.

Ethernet This is an ISA-based Ethernet circuit on the VXIpc 800 Series and VXIpc-745. It uses an RJ-45 connector for access to an external Ethernet-based LAN.

GPIB This logic block represents the IEEE 488.2 port on the VXIpc 800 Series and VXIpc-745. It uses the National Instruments TNT4882 ASIC for maximum performance as an ISA-based GPIB controller.

PC Peripherals These blocks represent the other peripherals supplied by the VXIpc 800/700. The VXIpc 800 Series has PS/2 mouse and keyboard ports, two miniature serial ports, and ECP/EPP parallel port, speaker jack, reset button, and a 1.44 MB, 3.5 in. floppy drive. Refer to the *Front Panel Features* section of Chapter 1, *Introduction*, to see which of these apply to the models in the VXIpc 700 Series.

- **VXIpc 800 Series users only**—The following logic blocks apply *only* to the VXIpc 800 Series:

  CPU Card The modular architecture of the VXIpc 800 Series contains a plug-in CPU card that can accommodate various Pentium/Pentium Pro-class processors. This card includes the

CPU, chipset support that connects the CPU subsystem to the PCI bus, and the system cache and RAM.

System I/O This block has the bridge between the PCI bus and the ISA bus. It also has PCI bus arbitration logic and integrates PC-specific hardware such as the DMA and interrupt controllers.

PCI Expansion Slot This is a general-purpose PCI slot. It can accommodate any standard 5 V, 32-bit PCI card with a modified mounting bracket.

ISA Expansion Slot This is a general-purpose ISA expansion slot. The VXIpc-860 can accommodate a half-length XT-size card, while the VXIpc-850 can accommodate a full-length XT-size card. However, the DRAM configuration may mechanically interfere with a full-length card. Refer to Appendix F, *Modifying and Installing I/O Expansion Boards*, for more information.

SCSI The SCSI circuitry uses a PCI-SCSI bridge to provide a flexible SCSI-2 connection on the front panel, usable for such devices as external hard disks and CD-ROM drives.

VXIpc 800 Series Configuration and Installation

Chapter 3

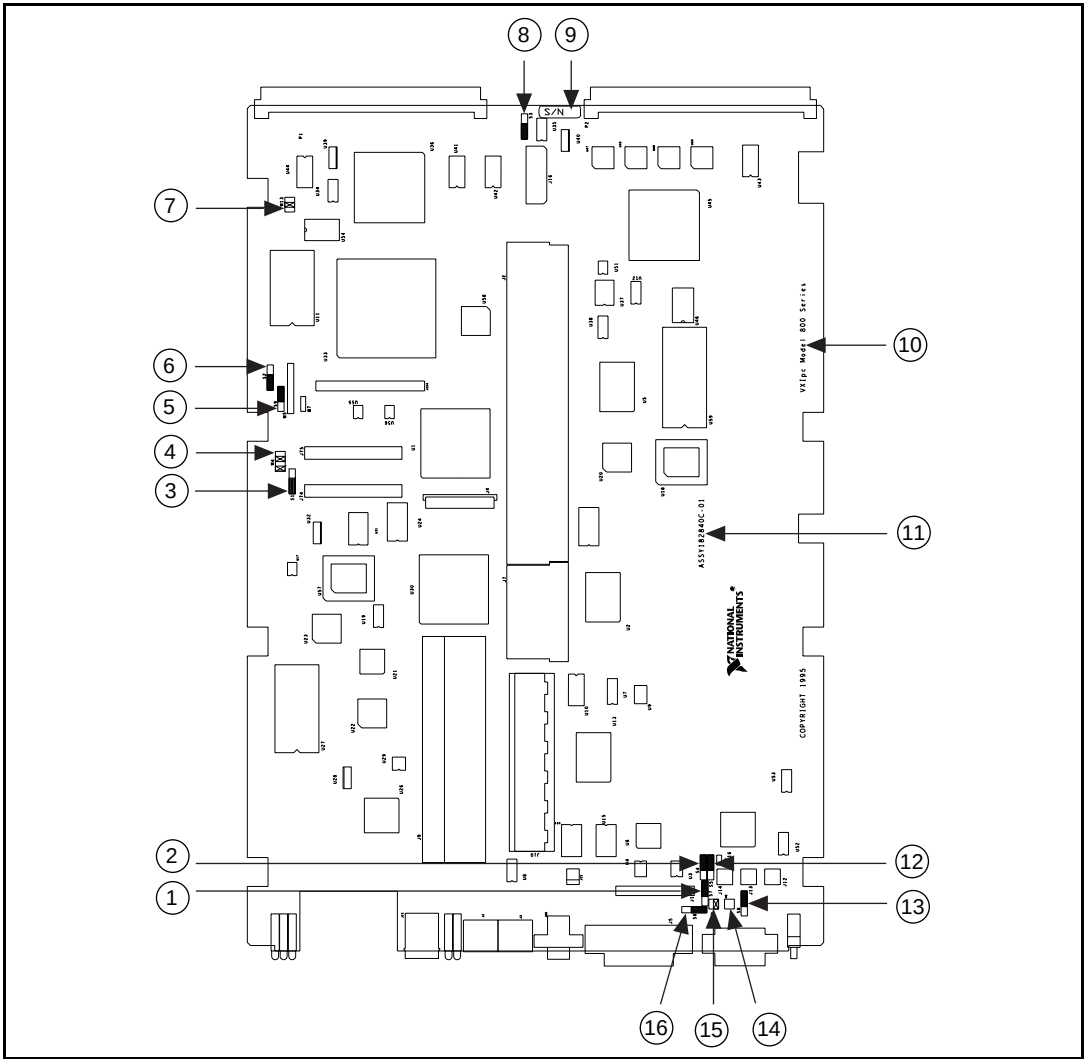
This chapter contains the instructions to configure and install the VXIpc 800 Series embedded computer. Unless otherwise noted, these instructions apply to all models in the VXIpc 800 Series, which currently consists of the VXIpc-850 and the VXIpc-860. If you have a VXIpc 700 Series computer, skip this chapter and refer to Chapter 4, *VXIpc 700 Series Configuration and Installation*.

Warning: ***Electrostatic discharge can damage several components on your VXIpc-800 module. To avoid such damage in handling the module, touch the antistatic plastic package to a metal part of your VXI chassis before removing the module from the package.***

Default Settings

This section summarizes the hardware default settings for the VXIpc 800 Series for easy reference. The module is set at the factory for the most commonly used configuration.

Figure 3-1 shows the location and factory-default settings of most of the configuration switches and jumpers on the VXIpc 800 Series.



1	Trigger In Termination (S7)	7	Slot 0 Identification (W13)	13	GPIB IRQ Level (S8)
2	CLK10 Inversion (S4)	8	CLK10 Source (S3)	14	CMOS (W2)
3	Ethernet EEPROM Config. (S1)	9	Serial Number	15	SCSI Termination (W1)
4	LPT DMA Channel (W4)	10	Product Name	16	CLK10 Termination (S6)
5	MITE EEPROM Config. (S9)	11	Assembly Number		
6	MITE Self Config. (S2)	12	CLK10 Direction (S5)		

Figure 3-1. VXIpc 800 Series Parts Locator Diagram

Table 3-1 lists the factory-default settings and options for the onboard jumpers and switches.

Table 3-1. VXIpc 800 Series Hardware Default Settings

Jumper/ Switch	Default Setting	Optional Setting
W1	Supply SCSI termination power	Disable SCSI termination
W2	Normal CMOS operation	Clear CMOS
W4	LPT1 uses DMA Channel 1	LPT uses DMA Channel 3
W13	Enable automatic Slot 0 detection	Force Slot 0; Force Non-Slot 0
S1	Enable Ethernet EEPROM configuration	Disable Ethernet EEPROM configuration (uses default power on values)
S2	Enable MITE self-configuration	Disable MITE self-configuration
S3	Source CLK10 from onboard oscillator	Source CLK10 from SMB
S4	Non-inverted CLK10 output	Inverted CLK10 output
S5	Receive CLK10 input from SMB	Source CLK10 output to SMB
S6	No termination on CLK10 input	Terminate CLK10 input to 50 Ω
S7	No termination on external trigger input	Terminate external trigger input to 50 Ω
S8	GPIB uses IRQ11	GPIB uses IRQ5
S9	MITE user configuration	MITE factory configuration

Configuring the VXIpc 800 Series

This section describes how to configure the following options on the VXIpc-800.

- VXIbus Slot 0/Non-Slot 0
- VXIbus CLK10 routing
- Trigger input termination
- EEPROM
- Installed system RAM

How to Remove the Metal Enclosure

The VXIpc-800 is housed in a metal enclosure comprised of a top and bottom cover to improve EMC performance and to provide easy handling. You must remove the top cover to change many of the switch and jumper settings. You must also remove the top cover to change the amount of DRAM installed on the module.

Remove the top cover by removing the 12 screws that attach it to the module.

VXIbus Slot 0/Non-Slot 0

The VXIpc-800 is configured at the factory to automatically detect if it is installed in Slot 0 of a VXIbus mainframe. With automatic Slot 0 detection, you can install the module into any VXIbus slot.

You can manually configure the VXIpc-800 for either Slot 0 or Non-Slot 0 operation by defeating the automatic-detection circuitry. Use the three-position jumper W13 to select automatic Slot 0 detection, Slot 0, or Non-Slot 0 operation. Figure 3-2 shows these three settings.



Warning: ***Do not install a device configured for Slot 0 into another slot without first reconfiguring it to either Non-Slot 0 or automatic configuration. Neglecting to do this could result in damage to the device, the VXIbus backplane, or both.***

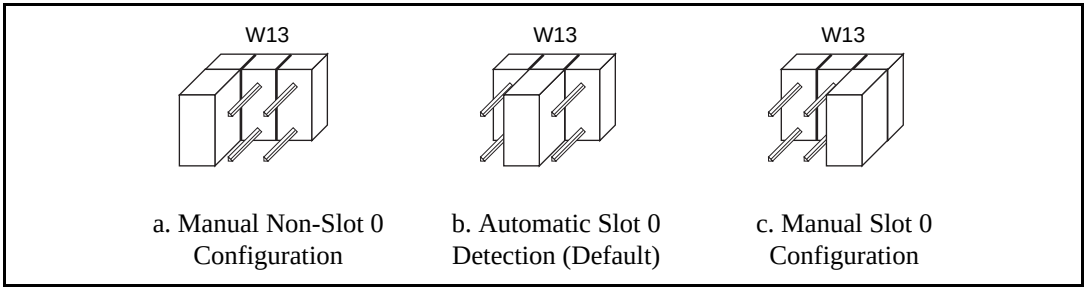


Figure 3-2. VXIbus Slot Configuration

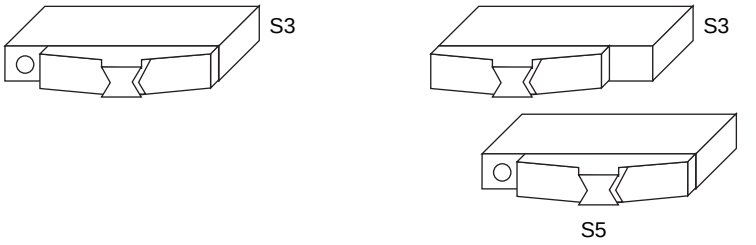
When the VXIpc-800 is installed in Slot 0, it becomes the VXIbus System Controller. In this role, it has VXIbus Data Transfer Bus Arbiter circuitry that accepts bus requests on all four VXIbus request levels, prioritizes the requests, and grants the bus to the highest priority requester. As VXIbus System Controller, the VXIpc-800 also drives the 16 MHz VXIbus system clock by an onboard 16 MHz oscillator.

As required by the VXIbus specification, the VXIpc-800 drives the 10 MHz signal CLK10 on a differential ECL output when installed in Slot 0. When not installed in Slot 0, the VXIpc-800 only receives the CLK10 signal.

VXIbus CLK10 Routing

When the VXIpc-800 is installed in Slot 0 of your mainframe, it supplies the VXIbus CLK10 signal. The VXIpc-800 can use two different sources to generate this signal: an onboard oscillator, or the external CLK SMB connector. Use switch S3 to select these options, as shown in Figure 3-3.

Notice that Figure 3-3b also shows switch S5. You must configure these switches as shown when using the corresponding CLK10 source setting of S3.



a. CLK10 Generated from Onboard Oscillator (Default)

b. CLK10 Generated from SMB

Figure 3-3. VXIbus CLK10 Routing

The VXIpc-800 can also be configured to drive the external CLK SMB signal from the VXIbus CLK10 signal. Switch S5 controls whether the VXIpc-800 drives or receives the external CLK SMB signal. If you change the S5 setting to drive CLK10 out the external CLK10 SMB connector (Figure 3-4b), do not set S3 to source CLK10 to the backplane from the SMB; instead use the setting shown in Figure 3-3a.

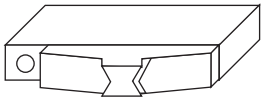


a. CLK10 SMB Receives 10 MHz Signal (Default)

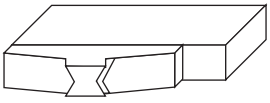
b. CLK10 SMB Drives 10 MHz Signal from VXIbus CLK10

Figure 3-4. SMB CLK10 Direction

When switch S5 is set so that the VXIpc-800 receives the SMB CLK10 signal, you have the option to add a 50 Ω termination to the signal by setting switch S6. Switch S6 is unused—its setting does not matter—when S5 is configured to drive the external CLK SMB signal. Figure 3-5 shows the settings for switch S6.



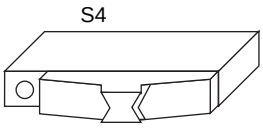
a. Does Not Terminate CLK10 SMB Input (Default)



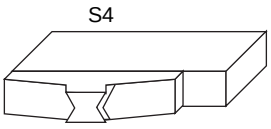
b. Terminates CLK10 SMB Input

Figure 3-5. SMB CLK10 Termination

You can use an additional switch, S4, to control the polarity of the external CLK SMB signal when S5 is configured to drive it. S4 is unused—its setting does not matter—when S5 is configured to receive the external CLK SMB signal.



a. Noninverted CLK10 Output from SMB (Default)



b. Inverted CLK10 Output from SMB

Figure 3-6. SMB CLK10 Polarity

Trigger Input Termination

You can use switch S7 to terminate the external trigger input SMB with 50 Ω to ground. Figure 3-7a shows the default setting for a non-terminated trigger input SMB. Use the setting of Figure 3-7b to terminate the trigger input SMB. Switch S7 is located above switches S4, S5, and S6, which have no effect on this configuration.

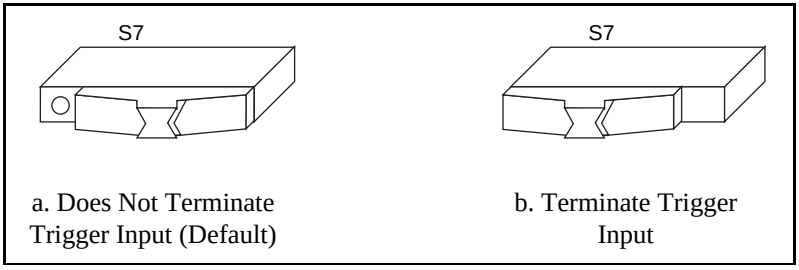


Figure 3-7. SMB Trigger Input Termination

EEPROM

The VXIpc-800 has an onboard EEPROM, which stores default register values for the VXI circuitry. These values are loaded when you power up the computer. These values read from the EEPROM tell the PCI interface of the VXIbus registers so that the VXI interface is ready to respond to Resource Manager accesses within the required 5 s of SYSRST* deasserting. You can use switch S2 to disable this power-on self-configuration (POSC) circuit. Although this makes the VXI circuitry unusable, it is sometimes helpful in debugging address and interrupt conflicts with add-in boards. In general, however, you should leave switch S2 in its factory-default setting. Figure 3-8 shows the possible configurations for S2.

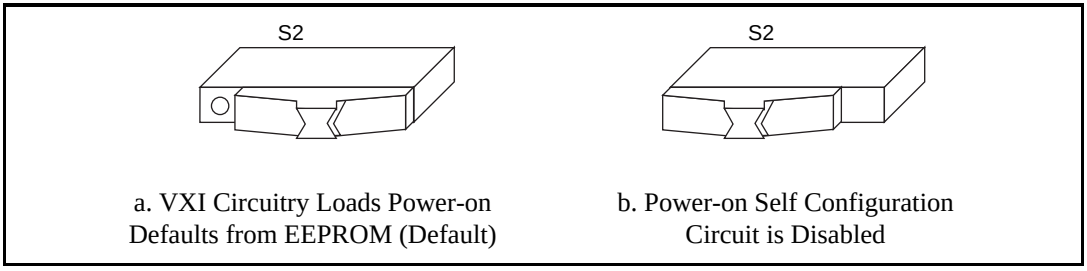


Figure 3-8. Power-On Self Configuration Status

The EEPROM is divided into two halves; one half is factory configured and one half is user configurable. Use switch S9 to control the operation of the EEPROM. The setting of this switch determines whether the VXIpc-800 boots off the factory-configured half or the user-modified settings. This is useful in the event that the user-configured half of the EEPROM becomes corrupted in such a way that the VXIpc-800 boots to an unusable state. In its default setting, the VXIpc-800 boots off the user-configurable half.

Figure 3-9 shows the configuration settings for EEPROM operation.

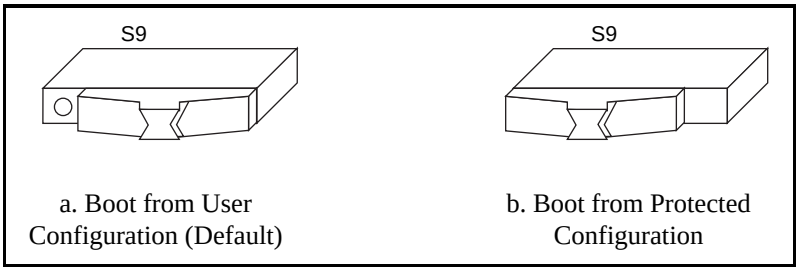


Figure 3-9. EEPROM Configuration

How to Fix an Invalid EEPROM Configuration

`VXIedit` is the software configuration utility in the NI-VXI software. This utility is described in the *NI-VXI Software Manual for the VXIpc 800/700 Series*, which you can access through the `c:\images\manuals` directory. You can use this utility to edit the configuration of the VXIpc-800. Some of these settings are stored in files that are read by the NI-VXI software, while other settings are stored directly in the VXIpc-800 EEPROM. Certain EEPROM configurations can lock up your PCI computer while it is booting up. Generally, only the size and location of the memory windows can cause your VXIpc-800 to lock up your system. For example, many PCI-based computers will not boot if a board in its system requests more memory space than the computer can allocate. If you encounter this situation you should reduce the size of the VXIpc-800 user window.

If this situation occurs after you change the configuration, perform the following steps to reconfigure the VXIpc-800.

1. Turn your computer off.

Warning: ***To protect both yourself and the mainframe from electrical hazards, the mainframe should remain off until you are finished changing the settings on the VXIpc-800 module.***

2. Change switch S9 to the OFF position as shown in Figure 3-9b to restore the factory configuration.
3. Turn on the computer. The computer should boot this time because the factory-default configuration is being used to initialize the VXIpc-800 module.

4. Run `VXIedit` to re-adjust the VXIpc-800 configuration. Refer to the *NI-VXI Software Manual for the VXIpc 800/700 Series* for instructions on using this utility.
5. After saving the configuration, exit Windows and turn off the computer.
6. Change switch S9 to the ON position, as shown in Figure 3-9a.
7. Turn on the computer. If the computer does not boot with this configuration, you will have to repeat these steps, modifying your configuration until a final configuration is reached.

Installed System RAM

The 16 MB of installed RAM is factory configured per customer order. You can change the amount of installed RAM on the VXIpc 800 Series by installing DRAM SIMMs. Refer to Appendix A, *Specifications*, for more information on SIMMs.

Configuring the PC

This section describes how to configure the following options on the PC.

- SCSI termination
- GPIB interrupt level
- System CMOS
- Parallel port DMA level
- Ethernet power-on defaults

SCSI Termination

The VXIpc-800 uses active termination on the SCSI-2 bus. Because the VXIpc-800 is always an end device, you should not need to disable the termination; however, for informational purposes Figure 3-10 shows the jumper settings for both enabled and disabled termination.

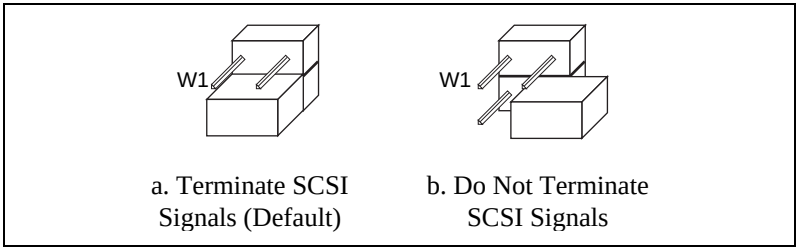


Figure 3-10. SCSI Termination

GPIB Interrupt Level

You can program the GPIB interface on the VXIpc-800 to use one of two ISA interrupts—5 or 11. Switch S8 controls which interrupt line the GPIB interface uses. Figure 3-11 shows these settings.

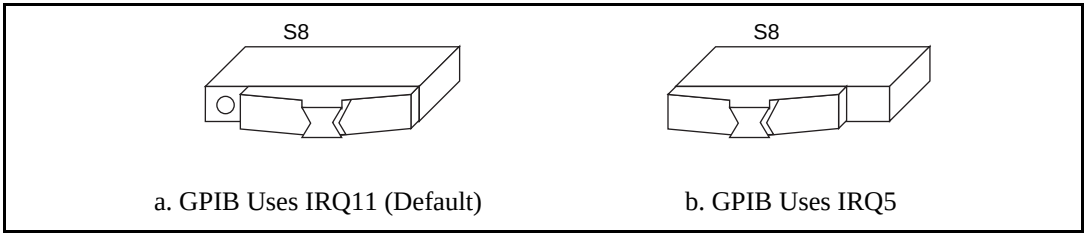


Figure 3-11. GPIB Interrupt Level

System CMOS

The VXIpc-800 contains a backed-up memory used to store BIOS defaults and configuration information.

To clear the CMOS contents, simply short the pins of W2 as shown in Figure 3-12b.

Warning: ***You should not keep these two pins shorted because the CMOS memory cannot be sustained when the power is turned off if these two pins are shorted.***

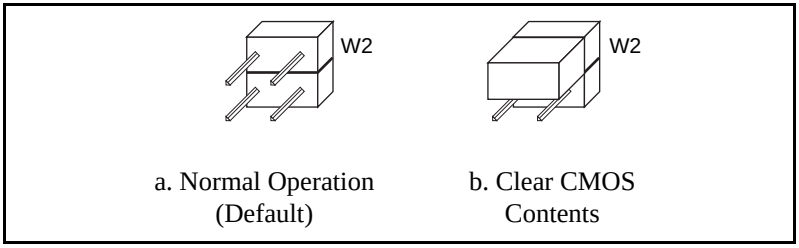


Figure 3-12. System CMOS

Parallel Port DMA Level

You can use jumper W4 to configure the parallel port for either DMA level 1 or DMA level 3. Refer to Figure 3-13 for the jumper settings.

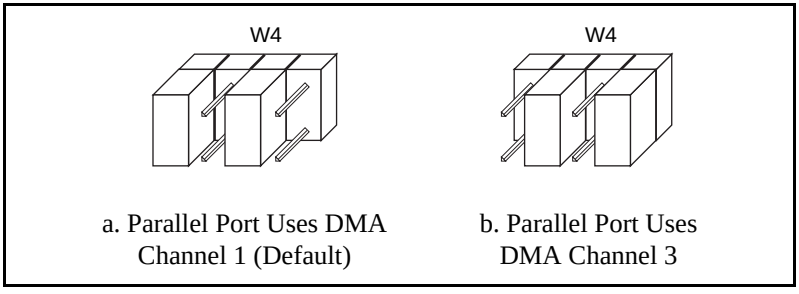


Figure 3-13. Parallel Port DMA Channel

Ethernet Power-On Defaults

The VXIpc-800 Ethernet circuitry loads its power-on settings from an EEPROM. You should not change switch S1 from its default setting; however, the switch setting required to do so is included in Figure 3-14 for informational purposes.

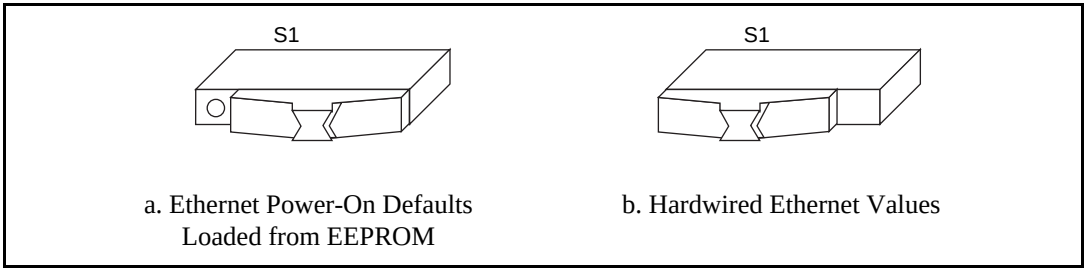


Figure 3-14. Ethernet Power-On Defaults

Installing the VXIpc 800 Series

This section contains general installation instructions for the VXIpc-800. Consult your VXIbus mainframe user manual or technical reference manual for specific instructions and warnings.

1. Plug in your mainframe before installing the VXIpc-800. The power cord grounds the mainframe and protects it from electrical damage while you are installing the module.

Warning: ***To protect both yourself and the mainframe from electrical hazards, the mainframe should remain off until you are finished installing the VXIpc-800 module.***

2. Remove or open any doors or covers blocking access to the mainframe slots.
3. If you are installing the VXIpc-800 into a D-size mainframe, install a support designed for installing C-size boards in D-size mainframes. The VXIpc-800 has no P3 connector and cannot provide P3 Slot 0 control to VXI devices requiring this capability.

Warning: ***If the VXIpc-800 is not configured for automatic Slot 0 detection, be certain that the slot you select in your VXIbus mainframe matches the VXIpc-800 configuration as either a Slot 0 device or a Non-Slot 0 device. If you install your VXIpc-800 into a slot that does not correspond with the jumper setting, you risk damage to the VXIpc-800, the VXIbus backplane, or both.***

4. Insert the VXIpc-800 in the slot you have selected by aligning the top and bottom of the module with the card-edge guides inside the mainframe. Slowly push the VXIpc-800 straight into the slot until its plug connectors are resting on the backplane receptacle

connectors. Using slow, evenly distributed pressure, press the module straight in until it seats in the expansion slot. The front panel of the VXIpc-800 should be even with the front panel of the mainframe.

5. Tighten the retaining screws on the top and bottom edges of the front panel.
6. Check the installation.
7. Connect the keyboard and mouse to the appropriate connectors. Use the keyboard adapter cable that your received with your kit to adapt AT-style keyboards to the VXIpc-800 mini-DIN connector.
8. Connect the VGA monitor video cable to the VGA connector.
9. Connect devices to ports as required by your system configuration. Some ports, such as the COM ports, have adapter cables that you can order from National Instruments.
10. Replace or close any doors or covers to the mainframe.

VXIpc 700 Series Configuration and Installation

Chapter 4

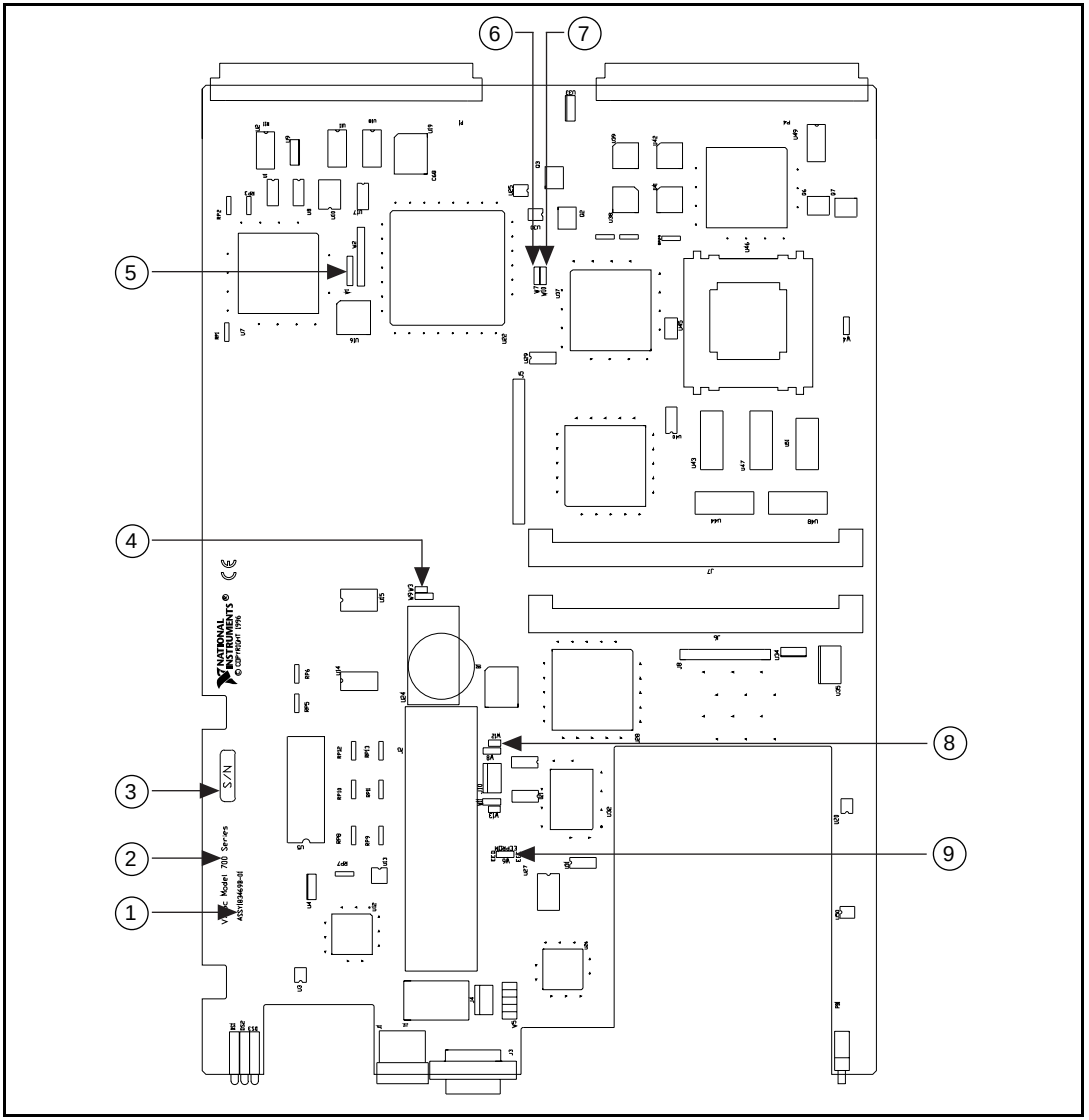
This chapter contains the instructions to configure and install the VXIpc 700 Series embedded computer. Unless otherwise noted, these instructions apply to all models in the VXIpc 700 Series, which includes the VXIpc-740 and the VXIpc-745. If you have a VXIpc 800 Series computer, refer to Chapter 3 for information on configuring and installing your controller.

Warning: ***Electrostatic discharge can damage several components on your VXIpc-700 module. To avoid such damage in handling the module, touch the antistatic plastic package to a metal part of your VXI chassis before removing the module from the package.***

Default Settings

This section summarizes the hardware default settings for the VXIpc 700 Series for easy reference. The module is set at the factory for the most commonly used configuration.

Figure 4-1 shows the location and factory-default settings of most of the configuration jumpers on the VXIpc 700 Series.



1	Assembly Number	4	CMOS (W3)	7	MITE Self Configuration (W10)
2	Product Name	5	Slot 0 Identification (W1)	8	Trigger In Termination (W12)
3	Serial Number	6	MITE EEPROM Configuration (W7)	9	Ethernet EEPROM Configuration (W6)

Figure 4-1. VXIpc 700 Series Parts Locator Diagram

Table 4-1 lists the factory-default settings and options for the onboard jumpers.

Table 4-1. VXIpc 700 Series Hardware Default Settings

Jumper/ Switch	Default Setting	Optional Setting
W1	Enable automatic Slot 0 detection	Force Slot 0; Force Non-Slot 0
W3	Normal CMOS operation	Clear CMOS
W6	Enable Ethernet EEPROM configuration	Disable Ethernet EEPROM configuration (uses default power on values)
W10	Enable MITE self-configuration	Disable MITE self-configuration
W7	MITE user configuration	MITE factory configuration
W12	No termination on external trigger input	Terminate external trigger input to 50 Ω

Configuring the VXIpc 700 Series

This section describes how to configure the following options on the VXIpc-700.

- VXIbus Slot 0/Non-Slot 0
- Trigger Input Termination
- EEPROM
- Installed system RAM

How to Remove the Metal Enclosure

The VXIpc-700 is housed in a metal enclosure comprised of a top and bottom cover to improve EMC performance and to provide easy handling. You must remove the top cover to change many of the switch and jumper settings. You must also remove the top cover to change the amount of DRAM installed on the module.

Remove the top cover by removing the 10 screws that attach it to the module.

VXIbus Slot 0/Non-Slot 0

The VXIpc-700 is configured at the factory to automatically detect if it is installed in Slot 0 of a VXIbus mainframe. With automatic Slot 0 detection, you can install the module into any VXIbus slot.

You can manually configure the VXIpc-700 for either Slot 0 or Non-Slot 0 operation by defeating the automatic-detection circuitry. Use the five-position jumper W1 to select automatic Slot 0 detection, Slot 0, or Non-Slot 0 operation. Figure 4-2 shows these three settings.

Warning: ***Do not install a device configured for Slot 0 into another slot without first reconfiguring it to either Non-Slot 0 or automatic configuration. Neglecting to do this could result in damage to the device, the VXIbus backplane, or both.***

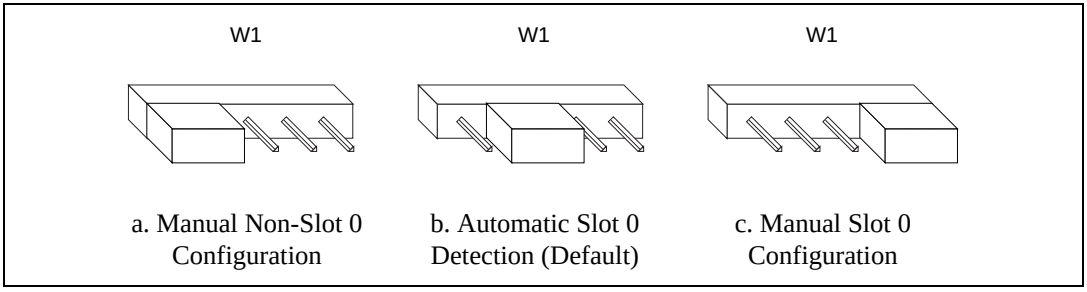


Figure 4-2. VXIbus Slot Configuration

When the VXIpc-700 is installed in Slot 0, it becomes the VXIbus System Controller. In this role, it has VXIbus Data Transfer Bus Arbiter circuitry that accepts bus requests on all four VXIbus request levels, prioritizes the requests, and grants the bus to the highest priority requester. As VXIbus System Controller, the VXIpc-700 also drives the 16 MHz VXIbus system clock by an onboard 16 MHz oscillator.

As required by the VXIbus specification, the VXIpc-700 drives the 10 MHz signal CLK10 on a differential ECL output when installed in Slot 0. When not installed in Slot 0, the VXIpc-700 only receives the CLK10 signal.

Trigger Input Termination

You can use jumper W12 to terminate the external trigger input SMB with 50 Ω to ground. Figure 4-3a shows the default setting for a non-terminated trigger input SMB. Use the setting of Figure 4-3b to terminate the trigger input SMB.



a. Trigger Input Unterminated (Default)

b. Terminate Trigger Input

Figure 4-3. SMB Trigger Input Termination

EEPROM

The VXIpc-700 has an onboard EEPROM, which stores default register values for the VXI circuitry. These values are loaded when you power up the computer. These values read from the EEPROM tell the PCI interface of the VXIbus registers so that the VXI interface is ready to respond to Resource Manager accesses within the required 5 s of SYSRST* deasserting. You can change jumper W10 to disable this power-on self-configuration (POSC) circuit. Although this makes the VXI circuitry unusable, it is sometimes helpful in debugging address and interrupt conflicts with add-in boards. In general, however, you should leave W10 in its factory-default setting. Figure 4-4 shows the possible configurations for W10.



a. VXI Circuitry Loads Power-on Defaults from EEPROM (Default)

b. Power-on Self Configuration Circuit is Disabled

Figure 4-4. Power-On Self Configuration Status

The EEPROM is divided into two halves; one half is factory configured and one half is user configurable. Use jumper W7 to control the operation of the EEPROM. The setting of this jumper determines

whether the VXIpc-700 boots off the factory-configured half or the user-modified settings. This is useful in the event that the user-configured half of the EEPROM becomes corrupted in such a way that the VXIpc-700 boots to an unusable state. In its default setting, the VXIpc-700 boots off the user-configurable half.

Figure 4-5 shows the configuration settings for EEPROM operation.

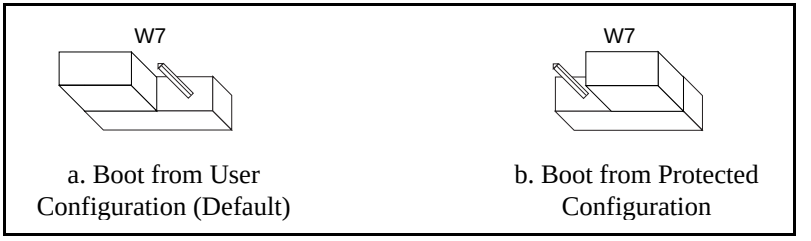


Figure 4-5. EEPROM Configuration

How to Fix an Invalid EEPROM Configuration

`VXIedit` is the software configuration utility in the NI-VXI software. This utility is described in the *NI-VXI Software Manual for the VXIpc 800/700 Series,* which you can access through the `c:\images\manuals` directory. You can use this utility to edit the configuration of the VXIpc-700. Some of these settings are stored in files that are read by the NI-VXI software, while other settings are stored directly in the VXIpc-700 EEPROM. Certain EEPROM configurations can lock up your PCI computer while it is booting up. Generally, only the size and location of the memory windows can cause your VXIpc-700 to lock up your system. For example, many PCI-based computers will not boot if a board in its system requests more memory space than the computer can allocate. If you encounter this situation you should reduce the size of the VXIpc-700 user window.

If this situation occurs after you change the configuration, perform the following steps to reconfigure the VXIpc-700.

1. Turn your computer off.

Warning: ***To protect both yourself and the mainframe from electrical hazards, the mainframe should remain off until you are finished changing the settings on the VXIpc-700 module.***

2. Change jumper W7 as shown in Figure 4-5b to restore the factory configuration.

3. Turn on the computer. The computer should boot this time because the factory-default configuration is being used to initialize the VXIpc-700 module.
4. Run `VXIedit` to re-adjust the VXIpc-700 configuration. Refer to the *NI-VXI Software Manual for the VXIpc 800/700 Series* for instructions on using this utility.
5. After saving the configuration, exit Windows and turn off the computer.
6. Change jumper W7 back to the position shown in Figure 4-5a.
7. Turn on the computer. If the computer does not boot with this configuration, you will have to repeat these steps, modifying your configuration until a final configuration is reached.

Installed System RAM

The 16 MB of installed RAM is factory configured per customer order. You can change the amount of installed RAM on the VXIpc 700 Series by installing DRAM SIMMs. Refer to Appendix A, *Specifications*, for more information on SIMMs.

Configuring the PC

This section describes how to configure the following options on the PC.

- System CMOS
- Ethernet power-on defaults

System CMOS

The VXIpc-700 contains a backed-up memory used to store BIOS defaults and configuration information.

To clear the CMOS contents, simply short the pins of W3 as shown in Figure 4-6b.

Warning: ***You should not keep these two pins shorted because the CMOS memory cannot be sustained when the power is turned off if these two pins are shorted.***

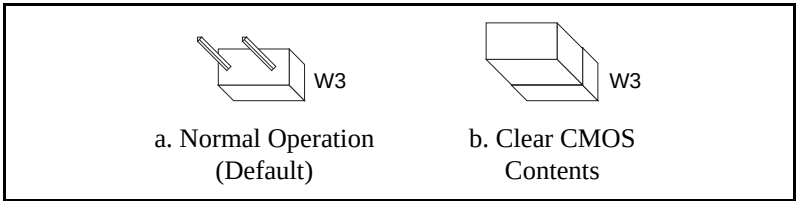


Figure 4-6. System CMOS

Ethernet Power-On Defaults

The VXIpc-700 Ethernet circuitry loads its power-on settings from an EEPROM. You should not change W6 from its default setting; however, the jumper setting required to do so is included in Figure 4-7 for informational purposes.

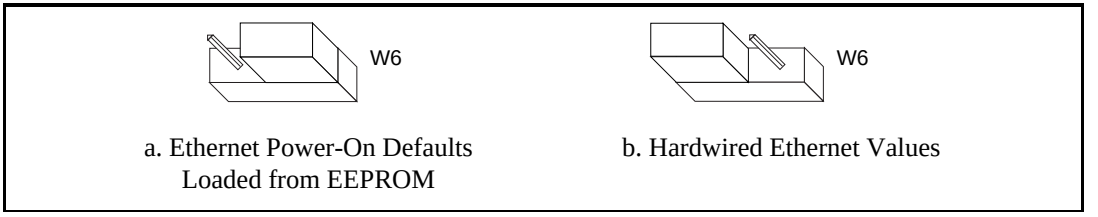


Figure 4-7. Ethernet Power-On Defaults

Installing the VXIpc 700 Series

This section contains general installation instructions for the VXIpc-700. Consult your VXIbus mainframe user manual or technical reference manual for specific instructions and warnings.

1. Plug in your mainframe before installing the VXIpc-700. The power cord grounds the mainframe and protects it from electrical damage while you are installing the module.

Warning: ***To protect both yourself and the mainframe from electrical hazards, the mainframe should remain off until you are finished installing the VXIpc-700 module.***

2. Remove or open any doors or covers blocking access to the mainframe slots.
3. If you are installing the VXIpc-700 into a D-size mainframe, install a support designed for installing C-size boards in D-size

mainframes. The VXIpc-700 has no P3 connector and cannot provide P3 Slot 0 control to VXI devices requiring this capability.

Warning: ***If the VXIpc-700 is not configured for automatic Slot 0 detection, be certain that the slot you select in your VXIbus mainframe matches the VXIpc-700 configuration as either a Slot 0 device or a Non-Slot 0 device. If you install your VXIpc-700 into a slot that does not correspond with the jumper setting, you risk damage to the VXIpc-700, the VXIbus backplane, or both.***

4. Insert the VXIpc-700 in the slot you have selected by aligning the top and bottom of the module with the card-edge guides inside the mainframe. Slowly push the VXIpc-700 straight into the slot until its plug connectors are resting on the backplane receptacle connectors. Using slow, evenly distributed pressure, press the module straight in until it seats in the expansion slot. The front panel of the VXIpc-700 should be even with the front panel of the mainframe.
5. Tighten the retaining screws on the top and bottom edges of the front panel.
6. Check the installation.
7. Connect the keyboard and mouse to the appropriate connectors. Use the keyboard adapter cable that your received with your kit to adapt AT-style keyboards to the VXIpc-700 mini-DIN connector. Connect the mouse to the COM1 serial port.
8. Connect the VGA monitor video cable to the VGA connector.
9. Connect devices to ports as required by your system configuration.
10. Replace or close any doors or covers to the mainframe.

Chapter 5

BIOS

This chapter contains information on BIOS, the low-level interface between the hardware and PC software that configures and tests your hardware at boot up. This BIOS (Basic Input Output System) provides an easy-to-use graphical user interface to allow you to configure system aspects according to your needs.

Entering BIOS Setup

To enter the BIOS setup program, perform the following steps.

1. Turn on or reboot the system. A screen appears with a series of diagnostic checks.
2. When **`Hit <DEL> if you want to run SETUP`** appears, press the <DEL> key to enter the BIOS setup program.
3. Choose options with the keyboard. Modify the settings to reflect system options.

Specifications

Appendix
A

This appendix describes the environmental, electrical, and mechanical specifications of the VXIpc 800/700 Series embedded computer.

Requirements

Characteristic	Specification
VXIbus Configuration Space	64 B
A24 or A32 Space	16 KB Minimum (Programmable)

Electrical

VXIpc-850		
	Current (A)	
Voltage (V)	Typical	Maximum (Fused)
+5	5.82 A	12 A
-5.2	224.5 mA	2 A
-2	67.2 mA	2 A
+12	2.26 mA	2 A
-12	2.43 mA	2 A

VXIpc-860		
	Current (A)	
Voltage (V)	Typical	Maximum (Fused)
+5	8.52 A	12 A
-5.2	224.5 mA	2 A
-2	67.2 mA	2 A
+12	2.60 mA	2 A
-12	2.43 mA	2 A

VXIpc 700 Series		
	Current (A)	
Voltage (V)	Typical	Maximum (Fused)
+5	3.85 A	7 A
-5.2	331.4 mA	2 A
-2	78.1 mA	2 A
+12	3.92 mA	2 A
-12	2.98 mA	2 A

Physical

Characteristic	Specification
Size	VXIpc 800 Series Two-slot VXIbus C-Size Module (233.35 by 340 by 60.96 mm) VXIpc 700 Series One-slot VXIbus C-Size Module (233.35 by 340 by 30.48 mm)
Board Dimensions	Fully Enclosed, Shielded VXI C-Size Board 233.35 by 340 mm (9.187 by 13.386 in.)
Slot Requirements	VXIpc 800 Series Two VXI C-Size Slots VXIpc 700 Series One VXI C-Size Slot
Compatibility	Fully Compatible with VXI Specification
VXI Keying Class	Class 1 TTL
MTBF	VXIpc-850 22,094 hours VXIpc-860 Contact National Instruments VXIpc-740 41,982 hours VXIpc-745 41,563 hours
Weight	VXIpc 800 Series 2.5 Kg (5.5 lb) Typical (16 MB DRAM Installed) VXIpc 700 Series 1.6 Kg (3.66 lb) Typical (16 MB DRAM Installed)

Environmental

Characteristic	Specification
Temperature	0° to 55° C Operating; -20° to 70° C Storage
Relative Humidity	0% to 95% Noncondensing, Operating; 0% to 95% Noncondensing, Storage
EMI	FCC Class A Verified, EC Verified
Vibration	Operational: 5 to 500 Hz, 0.31 g, 3 axes Non-operational: 5 to 500 Hz, 2.5 g, 3 axes
Functional Shock	MIL-T-28800E Class 3 (per Section 4.5.5.4.1) Half-Sine Shock Pulse (11 ms duration, 30 g peak, 3 shocks per face)

☞ **Note:** ***Random vibration profiles were developed in accordance with MIL-T-28800E and MIL-STD-810E Method 514. Test levels exceed those recommended in MIL-STD-810E for Category 1 (Basic Transportation, Figures 514.4-1 through 514.4-3). Test report available upon request.***

VMEbus Capability Codes

Capability Code	Description
A32, A24, A16 (master)	VMEbus master A32, A24, and A16 addressing
A32, A24, A16 (slave)	VMEbus slave A32, A24, and A16 addressing
D64, D32, D16, D08(EO) (master)	VMEbus master D64, D32, D16, and D08 data sizes
D64, D32, D16, D08(EO) (slave)	VMEbus slave D64, D32, D16, and D08 data sizes

Capability Code	Description
BLT, MBLT (master)	VMEbus master block and D64 transfers
BLT, MBLT (slave)	VMEbus slave block and D64 transfers
RMW (master)	VMEbus master read/modify/write transfers
RMW (slave)	VMEbus slave read/modify/write transfers
RETRY (master)	VMEbus master retry support
RETRY (slave)	VMEbus slave retry support
FSD	First slot detector
SCON	VMEbus System Controller (Automatic Detection)
PRI, RRS	Prioritized or Round Robin Select arbiter
ROR, FAIR	Release on Request and FAIR bus requester
IH(7-1)	Interrupt handler for levels 7-1
I(7-1)	Interrupt requester for levels 7-1
D32, D16, D08(O) (Interrupt Handler)	VMEbus D32, D16, D08(O) interrupt handler
D32, D16, D08(O) (Interrupter)	VMEbus D32, D16, D08(O) interrupter
ROAK, RORA	Release on Acknowledge or Register Access interrupter
BTO(x)	VMEbus bus timer (programmable limit)
LOCK	Can lock the VMEbus for indivisible transfers

Adding RAM

To add RAM to the VXIpc 800/700, remove the top cover and add SIMM modules to the empty SIMM sockets.

National Instruments recommends the following types of SIMMs for use with the VXIpc-850 and VXIpc 700 Series controllers (Fast Page Mode):

8 MB: 2 MB x 36 SIMMs — 70 ns

16 MB: 4 MB x 36 SIMMs — 70 ns

32 MB: 8 MB x 36 SIMMs — 70 ns

The VXIpc-860 can use Fast Page Mode (FPM), Extended Data Out (EDO), or Bursts Extended Data Out (BEDO). National Instruments recommends the following types of SIMMs for use with the VXIpc-860:

8 MB: 2 MB x 32/36 SIMMs — 60 or 70 ns

16 MB: 4 MB x 32/36 SIMMs — 60 or 70 ns

32 MB: 8 MB x 32/36 SIMMs — 60 or 70 ns

64 MB: 16 MB x 32/36 SIMMs — 60 or 70 ns

The height of SIMMs used with VXIpc 800 Series controllers should be 0.85 in. or less, or 1.2 in. for VXIpc 700 Series controllers. Contact National Instruments for recommendations on specific manufacturers.

☞ **Note:** ***The VXIpc-800 must be populated with pairs of SIMMs because the DRAM interface is 64 bits wide.***

VXIpc 800 Series System Resources

Appendix B

This appendix describes what system resources are available on the VXIpc 800 Series and where they are allocated. Because PCI is a relatively new addition to PCs, this appendix describes how PCI interrupts fit into a PC architecture before listing the devices that use them.

PCI Interrupts

PCI interrupts can be shared by multiple devices and are therefore more flexible than ISA interrupts. They do not actually connect to the processor directly; they are instead mapped through ISA interrupts in the system I/O module. The interrupt handler for a particular ISA interrupt must know if it will be acknowledging a PCI device.

Resource Tables

This section provides tables describing where system resources are assigned. Table B-1 lists how the ISA interrupts are allocated on the VXIpc 800 Series and whether they are driven by a PCI interrupt. Table B-2 lists DMA channel allocation, and Table B-3 gives the I/O address map.

Table B-1. VXIpc 800 Series ISA Interrupt Resource Allocations

ISA Interrupt	PCI Interrupt	Device
NMI	None	Parity
0	None	Timer
1	None	Keyboard
2	None	IRQ Expansion (8-15)
3	None	COM2
4	None	COM1

Table B-1. VXIpc 800 Series ISA Interrupt Resource Allocations (Continued)

ISA Interrupt	PCI Interrupt	Device
5	INTC, INTB	GPIB/PCMCIA Slot/Expansion
6	None	Floppy Drive
7	None	LPT1
8	None	RTC
9	None	Ethernet
10	INTA	VXI, SCSI, PCMCIA
11	None	GPIB/PCMCIA Slot/Expansion
12	None	Mouse
13	None	FPERR
14	INTD	IDE
15	None	PCMCIA Slot/Expansion

Table B-2. VXIpc 800 Series DMA Channel Resource Allocations

DMA Channel	Device
0	Free
1	Free
2	Floppy Drive
3	Free
4	Free
5	GPIB
6	Free
7	Free

Table B-3. VXIpc 800 Series I/O Address Map

I/O Address	Device
000-00F	8237 DMA #1
020-021	8259 PIC #1
040-043	8253 Timer
060-066	8742 Controller
070-077	CMOS RAM & NMI Mask Reg
078-07B	BIOS Timer
080-090	DMA Page Registers
092	Reserved
094-09F	DMA Page Registers
0A0-0A1	8259 PIC #2
0B2-0B3	Advanced Power Management
0C0-0DF	8237 DMA #2 (word mapped)
0F0-0FF	Numeric Processor Error Reg
170-177	Reserved
1F0-1F7	IDE
200-270	Free
278-27F	LPT*
280-2DF	Free
2C0-2DF	GPIB
2E8-2EF	COM*
2F8-2FF	COM*
300-30F	Ethernet
310-36F	Free
370-377	Reserved
378-37F	LPT*
380-3B0	Free
3BC-3BF	LPT*

Table B-3. VXIpc 800 Series I/O Address Map (Continued)

I/O Address	Device
3E8-3EF	COM*
3F0-3F7	Floppy
3F8-3FF	COM*
40A	*Scatter/Gather Int Status Reg*
40B	*DMA1 Extended Mode Regs*
410-41F	*Scatter/Gather Status*
420-43F	*Scatter/Gather Descriptor Table Pointer*
481-488	*DMA High Page Regs*
4D0-4D1	*Interrupt Edge/Level Control Regs*
4D6	*DMA2 Extended Mode Regs*
* Relocatable	

☞ **Note:** ***I/O locations in italics indicate addresses above 1 KB. These locations are not usually accessible in an ISA-based system; however, newer PCI-based chipsets have increased the amount of I/O space available.***

VXIpc 700 Series System Resources

Appendix C

This appendix describes what system resources are available on the VXIpc 700 Series and where they are allocated. Because PCI is a relatively new addition to PCs, this appendix describes how PCI interrupts fit into a PC architecture before listing the devices that use them.

PCI Interrupts

PCI interrupts can be shared by multiple devices and are therefore more flexible than ISA interrupts. They do not actually connect to the processor directly; they are instead mapped through ISA interrupts in the system I/O module. The interrupt handler for a particular ISA interrupt must know if it will be acknowledging a PCI device.

Resource Tables

This section provides tables describing where system resources are assigned. Table C-1 lists how the ISA interrupts are allocated on the VXIpc 700 Series and whether they are driven by a PCI interrupt. Table C-2 lists DMA channel allocation, and Table C-3 gives the I/O address map.

Table C-1. VXIpc 700 Series ISA Interrupt Resource Allocations

ISA Interrupt	PCI Interrupt	Device
NMI	None	Parity
0	None	Timer
1	None	Keyboard
2	None	IRQ Expansion (8-15)
3	None	PCMCIA Slot
4	None	COM1

Table C-1. VXIpc 700 Series ISA Interrupt Resource Allocations (Continued)

ISA Interrupt	PCI Interrupt	Device
5	INTD	PCMCIA
6	None	Floppy Drive
7	None	PCMCIA Slot
8	None	RTC
9	None	Ethernet
10	INTC	VXI
11	None	GPIB
12	None	PCMCIA Slot
13	None	FPERR
14	INTD	IDE
15	None	PCMCIA Slot

Table C-2. VXIpc 700 Series DMA Channel Resource Allocations

DMA Channel	Device
0	Free
1	Free
2	Floppy Drive
3	Free
4	Free
5	GPIB
6	Free
7	Free

Table C-3. VXIpc 700 Series I/O Address Map

I/O Address	Device
000-00F	8237 DMA #1
020-021	8259 PIC #1
040-043	8253 Timer
060-066	8742 Controller
070-077	CMOS RAM & NMI Mask Reg
078-07B	BIOS Timer
080-090	DMA Page Registers
092	Reserved
094-09F	DMA Page Registers
0A0-0A1	8259 PIC #2
0B2-0B3	Advanced Power Management
0C0-0DF	8237 DMA #2 (word mapped)
0F0-0FF	Numeric Processor Error Reg
170-177	Reserved
1F0-1F7	IDE
200-270	Free
278-27F	Free
280-2DF	Free
2C0-2DF	GPIB
2E8-2EF	COM*
2F8-2FF	COM*
300-30F	Ethernet
310-36F	Free
370-377	Reserved
378-37F	Free
380-3B0	Free
3BC-3BF	Free

Table C-3. VXIpc 700 Series I/O Address Map (Continued)

I/O Address	Device
3E8-3EF	COM*
3F0-3F7	Floppy
3F8-3FF	COM*
* Relocatable	

Appendix D

LED Indicators

This appendix describes how to read the LEDs on the front panel to interpret the status of the VXIpc 800/700 Series.

VXIbus Interface Status LEDs

The VXIbus interface status LEDs are located at the top of the module and include four LEDs: **FAILED**, **SYSFAIL**, **ONLINE**, and **TEST**. They indicate the various stages of initialization that occur as the VXIpc 800/700 boots. The following paragraphs describe each LED.

SYSFAIL LED

The **SYSFAIL** LED is lit when the VMEbus SYSFAIL signal is asserted. It does not necessarily indicate that the VXIpc 800/700 is asserting SYSFAIL, only that there is a device in the system asserting SYSFAIL.

FAILED LED

The **FAILED** LED is lit when the VXIpc 800/700 is driving the SYSFAIL signal. The VXIpc 800/700 asserts SYSFAIL when the PASSED bit in its VXIbus status register is clear. The PASSED bit is set by the power-on self configuration circuitry (POSC) when it has completed initializing the VXIbus interface.

ONLINE LED

The **ONLINE** LED is lit when the Resource Manager has successfully completed and the VXIbus interface is ready for application programs.

TEST LED

The **TEST** LED is lit when the power-on self configuration circuitry is configuring the VXIbus interface.

LEDs and System Startup Cycle

Table D-1 shows a system startup cycle and possible points of failure, up to and including the state in which the `VXIinit` initialization program asserts the **ONLINE** LED.

Table D-1. LEDs and System Startup Status

Step	LEDs Lit	Status	Possible Problem if VXIpc 800/700 Fails
1	None	Machine just turned on.	The VXIpc 800/700 is not receiving power.
2	**FAILED**, **SYSFAIL**	Now asserting SYSFAIL because VXIbus interface has not been initialized yet.	Power-on self configuration (POSC) cannot execute because of problems with system reset or because the POSCEN switch is incorrectly configured.
3	**FAILED**, **TEST**	VXI interface is being initialized by MITE power-on self configuration (POSC) circuitry.	POSC has stalled.
4	**TEST**	POSC circuitry has initialized VXI interface, setting PASSED and DONE bits.	POSC stalled before clearing the **TEST** LED.
5	None	POSC cycles are complete. VXI port is ready to respond to Resource Manager inquiries.	POSC completed successfully; however, the Resource Manager either hung or was not executed.
6	**ONLINE**	Resource Manager has been executed, indicating that the VXI software can now communicate with the VXI circuitry.	Resource Manager interface initialized successfully.

If either the **SYSFAIL** or **FAILED** LED remains lit, perform the following steps:

1. Power off the mainframe.
2. Remove all other modules from the mainframe.
3. Make sure that the VXIpc 800/700 jumper settings are correct.
4. Make sure that the VXIpc 800/700 is seated properly in the mainframe.
5. Power on the mainframe and observe whether the **SYSFAIL** and **FAILED** LEDs become unlit some time before the operating system boots.

Board Access LEDs

The board access LEDs—**ACCESS** and **DRIVE**—indicate when board resources have been accessed. The following paragraphs describe these LEDs.

ACCESS LED

When lit, the **ACCESS** LED indicates that the VXIpc 800/700 MODID line is asserted or that another VXIbus master is accessing VXIbus shared registers or shared memory.

DRIVE LED

The **DRIVE** LED light indicates that an access to the internal hard disk drive is occurring.

Ethernet LEDs (VXIpc 800 Series Only)

The Ethernet LEDs (**RX**, **TX**, **LINK**, and **DSEL**) indicate the status of the Ethernet interface on the VXIpc 800 Series.

RX LED

The **RX** LED lights when the Ethernet interface is receiving a packet.

TX LED

The **TX** LED lights when the Ethernet interface is transmitting a packet.

LINK LED

The **LINK** LED indicates LINK status and can generally be used to check if Ethernet activity is occurring.

DSEL LED

The **DSEL** (Device Select) LED lights when Ethernet registers are being accessed.

Front Panel and Connectors

Appendix E

This appendix describes the front panel and connectors on the VXIpc 800/700 Series. This material contains the information relevant to VXI*plug&play* Specification VPP-8, *VXI Module/Mainframe to Receiver Interconnection*.

☞ **Note:** ***The illustrations in this appendix show the mating face of the connectors. An asterisk suffix (*) on a signal name indicates that the signal is active low.***

As shown in Table E-1, the VXIpc-745 and VXIpc-740 each have a subset of the connectors found on the VXIpc-800.

Table E-1. VXIpc 800/700 Series Connectors

Connector	VXIpc-800	VXIpc-740	VXIpc-745
RS-232 Serial	Two	One	One
Extended Capabilities Parallel (ECP)	√		
VGA Controller	√	√	√
IEEE 488.2	√		√
10BaseT Ethernet	√		√
SCSI-2	√		
External Clock	√		
Trigger Output	√	√	√
Trigger Input	√	√	√
Audio Output	√		
PS/2-Style Keyboard	√	√	√
PS/2-Style Mouse	√		

Front Panel

Figures E-1 through E-3 show the front panel layout of the VXIpc-800, VXIpc-745, and VXIpc-740, respectively. The drawings show dimensions relevant to key elements on the front panel. Dimensions are shown in inches and millimeters, with millimeter dimensions in square brackets. The front panel thickness for all models in the VXIpc 800/700 Series is 2.49 mm (0.098 in.).

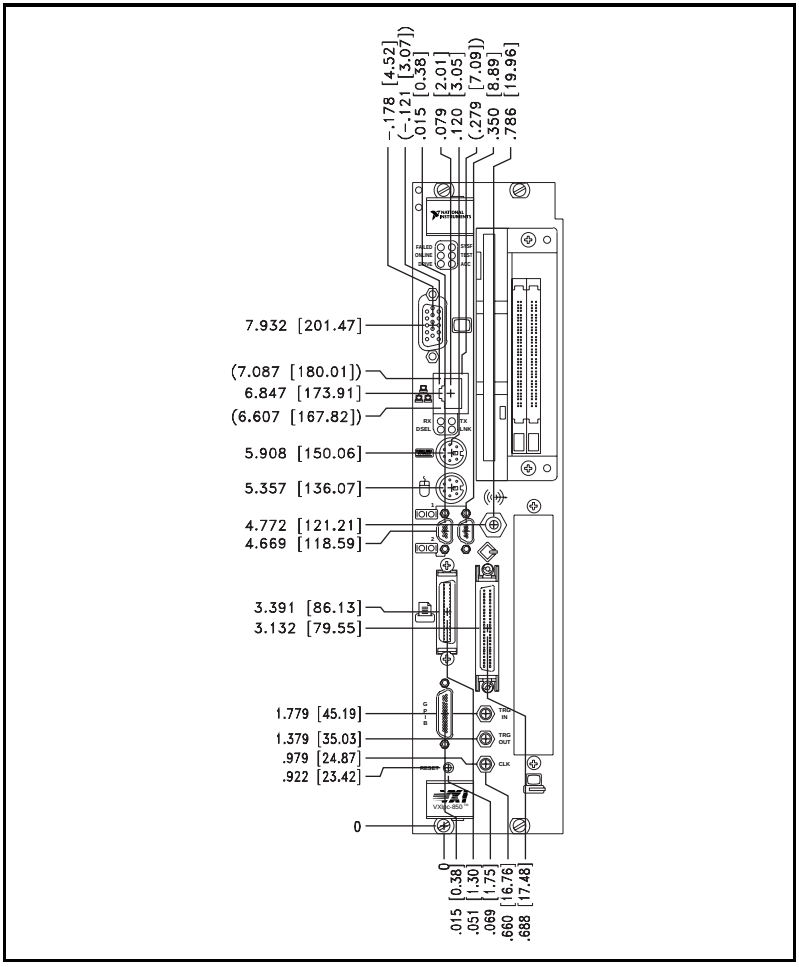


Figure E-1. VXIpc-800 Front Panel Layout and Dimensions

Figure E-2 shows the front panel layout of the VXIpc-745.

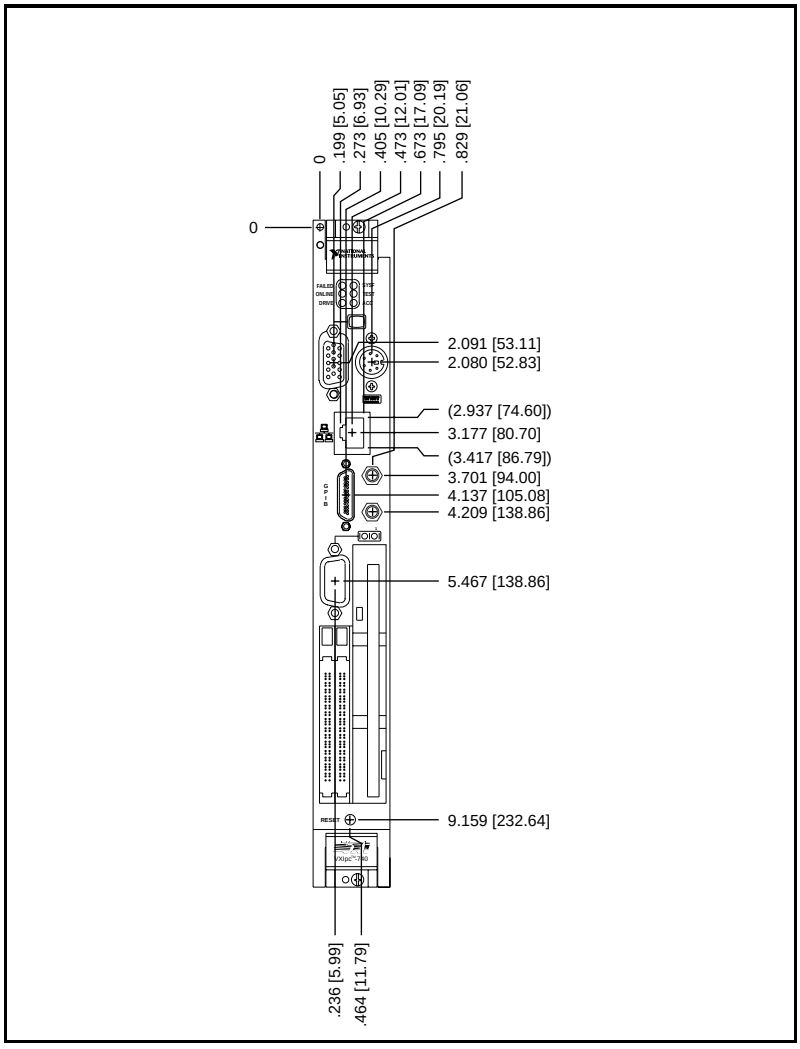


Figure E-2. VXIpc-745 Front Panel Layout and Dimensions

Figure E-3 shows the front panel layout of the VXIpc-740.

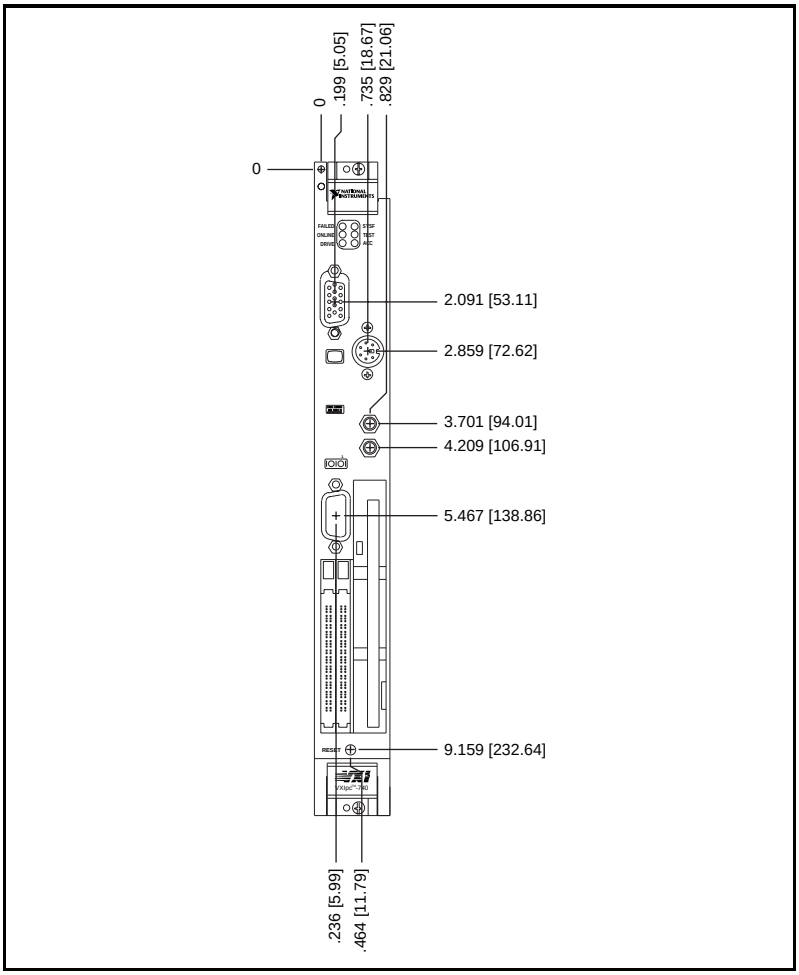


Figure E-3. VXIpc-740 Front Panel Layout and Dimensions

Keyboard and Mouse

Figure E-4 shows the location and pinouts for the keyboard connector on the VXIpc 800/700 Series, and the mouse connector on the VXIpc-800. For the VXIpc-700 you need to use the COM1 serial port if you want to connect a mouse. Table E-2 gives the name and description for the keyboard and mouse connector signals.

Amp manufactures a mating connector with part numbers 212437-4 (housing), 212435-7 (ferrule), and 66735-4 (pin contact).

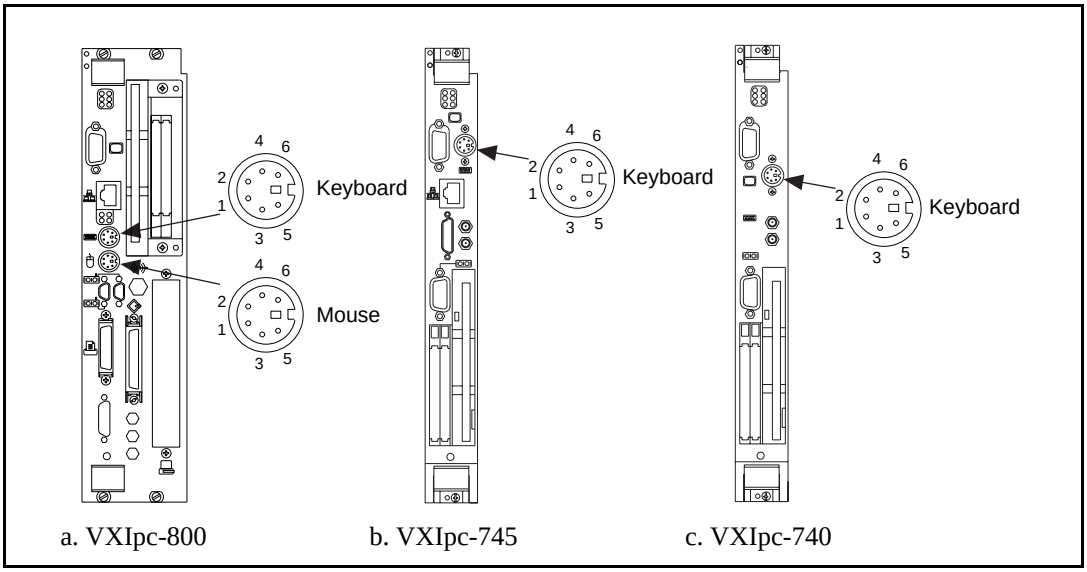


Figure E-4. Keyboard and Mouse Connectors Location and Pinout

Table E-2. Keyboard and Mouse Connector Signals

Pin	Signal Name	Signal Description
1	DATA	Data
2	NC	Not Connected
3	GND	Ground
4	+5V	+5 volts
5	CLK	Clock
6	NC	Not Connected

VGA

Figure E-5 shows the location and pinouts for the VGA connector on the VXIpc 800/700 Series. Table E-3 gives the name and description for the VGA connector signals.

Amp manufactures a mating connector with part numbers 748364-1 (housing) and 748333-2 (pin contact).

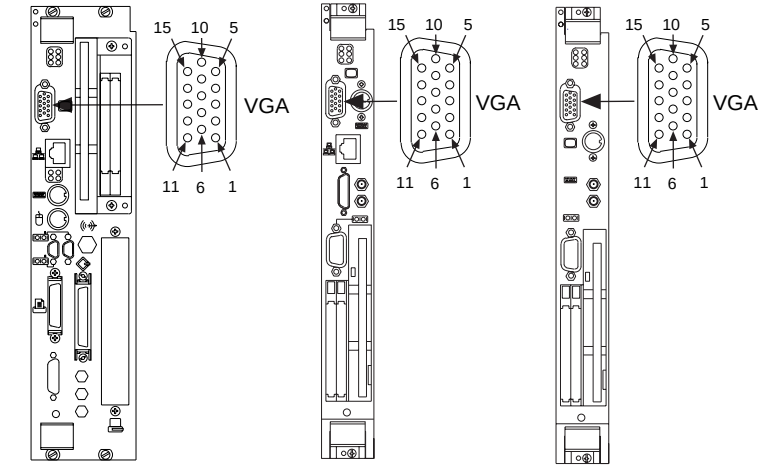


Figure E-5. VGA Connector Location and Pinout

Table E-3. VGA Connector Signals

Pin	Signal Name	Signal Description
1	R	Red
2	G	Green
3	B	Blue
4	NC	Not Connected
5	GND	Ground
6	GND	Ground
7	GND	Ground

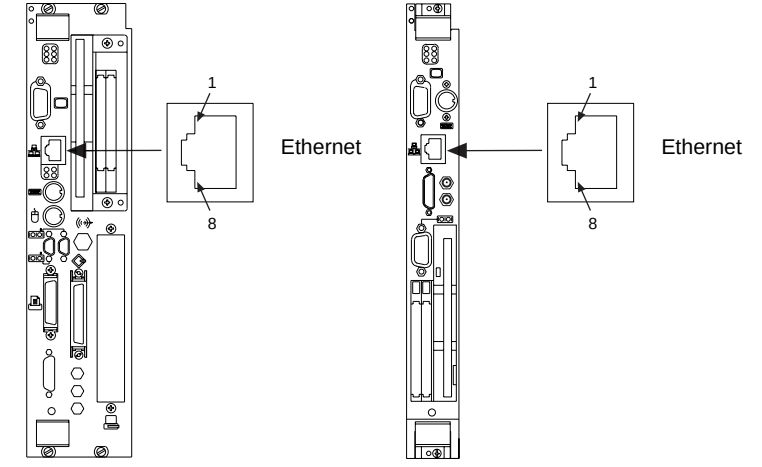
Table E-3. VGA Connector Signals (Continued)

Pin	Signal Name	Signal Description
8	GND	Ground
9	+5 VDC	+5 VDC
10	GND	Ground
11	NC	Not Connected
12	SD	Serial Data
13	HSync	Horizontal Sync
14	VSync	Vertical Sync
15	SC	Serial Clock

Ethernet

Figure E-6 shows the location and pinouts for the Ethernet connector on the VXIpc-800 and VXIpc-745. Table E-4 gives the name and description for the Ethernet connector signals.

Amp manufactures a mating connector, part number 554739-1.



a. VXIpc-800 b. VXIpc-745

Figure E-6. Ethernet Connector Location and Pinout

Table E-4. Ethernet Connector Signals

Pin	Signal Description
1	Differential Transmit
2	Differential Transmit
3	Differential Receive
4	NC
5	NC
6	Differential Receive
7	NC
8	NC

COM1 and COM2

Figure E-7 shows the location and pinouts for the COM1 connector on all models in the VXIpc 800/700 Series and the COM2 connector on the VXIpc-800. Table E-5 gives the name and description for the COM1 and COM2 connector signals.

ITT Cannon manufactures a serial port mating connector, part number MDSM-9SC-Z11, for the COM1 and COM2 connectors on the VXIpc-800.

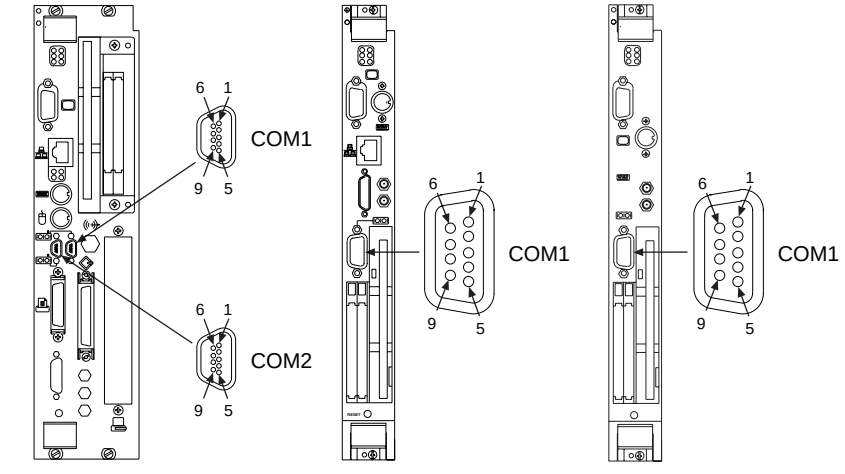


Figure E-7. COM1 and COM2 Connectors Location and Pinout

Table E-5. COM1 and COM2 Connector Signals

Pin	Signal Name	Signal Description
1	DCD*	Data Carrier Detect
2	RXD*	Receive Data
3	TXD*	Transmit Data
4	DTR*	Data Terminal Ready
5	GND	Ground

Table E-5. COM1 and COM2 Connector Signals (Continued)

Pin	Signal Name	Signal Description
6	DSR*	Data Set Ready
7	RTS*	Ready to Send
8	CTS*	Clear to Send
9	RI*	Ring Indicator

Parallel Port

Figure E-8 shows the location and pinouts for the IEEE-1284 connector on the VXIpc-800. Table E-6 gives the name and description for the IEEE-1284 connector signals.

Amp manufactures a parallel port compatible connector, part number 2-175677-5.

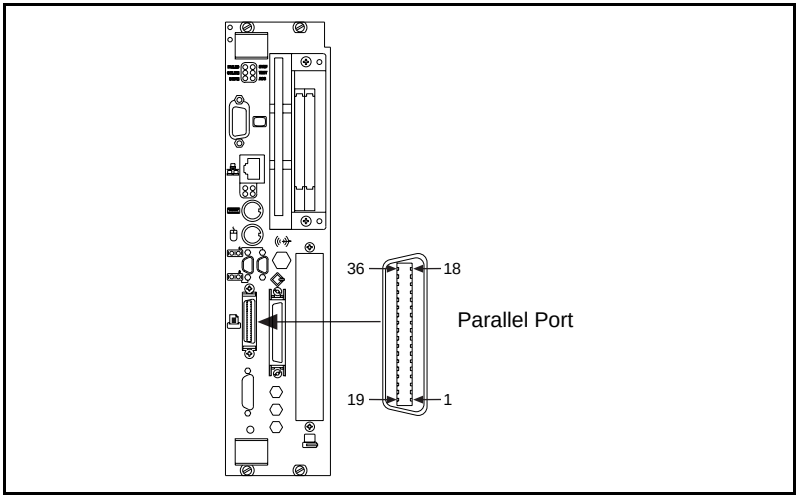


Figure E-8. Parallel Port Connector Location and Pinout

Table E-6. Parallel Port Connector Signals

Pin	Signal Name	Signal Description
1	BUSY*	Device Busy
2	SLCTIN*	Select Input
3	ACK*	Acknowledge
4	FAULT*	Fault
5	ERROR	Error
6	PD0	Data Bit 0
7	PD1	Data Bit 1
8	PD 2	Data Bit 2
9	PD3	Data Bit 3

Table E-6. Parallel Port Connector Signals (Continued)

Pin	Signal Name	Signal Description
10	PD4	Data Bit 4
11	PD5	Data Bit 5
12	PD6	Data Bit 6
13	PD7	Data Bit 7
14	INIT*	Initialize Printer
15	STROBE*	Strobe
16	SLCT	Select
17	AUTOFD	Auto Line Feed
18	+5V	+5 Volts
19-35	GND	Ground
36	NC	Not Connected

SCSI

Figure E-9 shows the location and pinouts for the SCSI connector on the VXIpc-800. Table E-7 gives the name and description for the SCSI connector signals.

Amp manufactures a SCSI compatible connector, part number 749111-4.

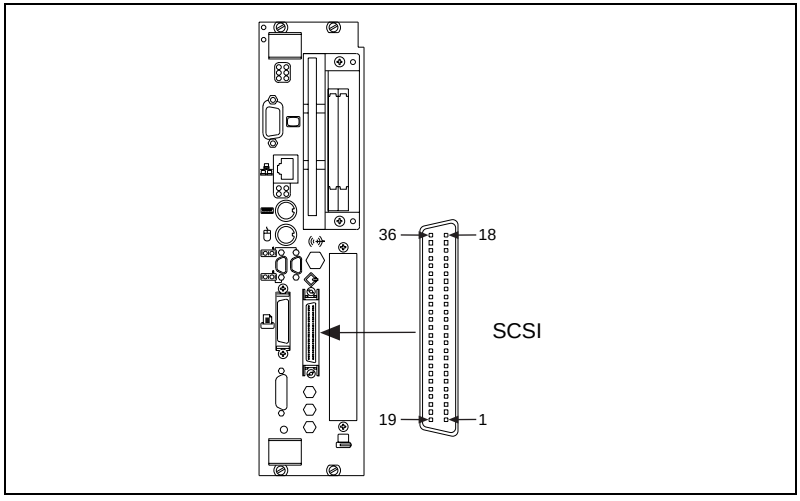


Figure E-9. SCSI Connector Location and Pinout (SCSI II)

Table E-7. SCSI Connector Signals

Pin	Signal Name	Signal Description
1–11, 15–25, 35–36, 40, 42	GND	Ground
12–14, 37–39	NC	Not Connected
26	D0	Data 0
27	D1	Data 1
28	D2	Data 2
29	D3	Data 3
30	D4	Data 4
31	D5	Data 5

Table E-7. SCSI Connector Signals (Continued)

Pin	Signal Name	Signal Description
32	D6	Data 6
33	D7	Data 7
34	DP	Parity
38	TERMPWR	Termination Power
41	ATN*	Attention
43	BUSY*	Busy
44	ACK*	Acknowledge
45	RESET*	Reset
46	MSG*	Message
47	SEL*	Select
48	CMD_DATA	Command/Data
49	REQ*	Request
50	IN_OUT	In/Out

GPIB (IEEE-488.2)

Figure E-10 shows the location and pinouts for the GPIB connector on the VXIpc-800 and VXIpc-745. Table E-8 gives the name and description for the GPIB connector signals.

ITT Cannon manufactures a GPIB mating connector, part number MDSM-255C-Z11.

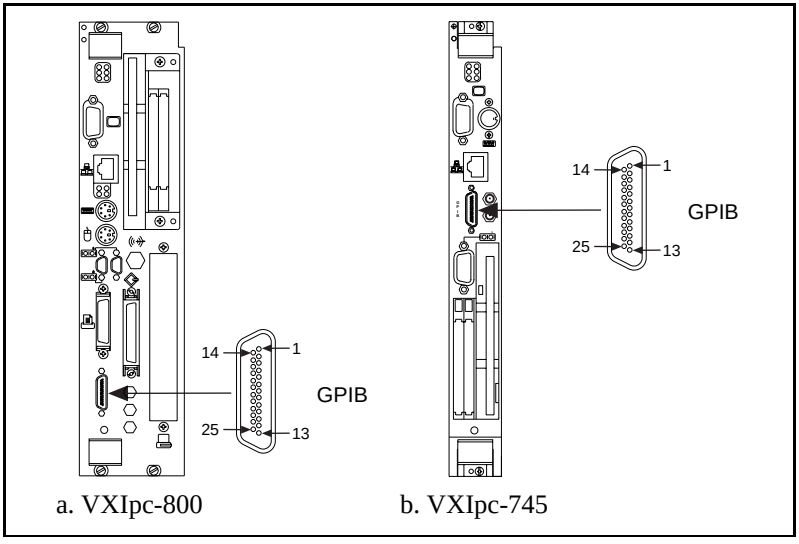


Figure E-10. GPIB Connector Location and Pinout

Table E-8. GPIB Connector Signals

Pin	Signal Name	Signal Description
1	DIO1*	Data Bit 1
2	DIO2*	Data Bit 2
3	DIO3*	Data Bit 3
4	DIO4*	Data Bit 4
5	EOI*	End or Identify
6	DAV*	Data Valid
7	NRFD*	Not Ready for Data
8	NDAC*	Not Data Accepted

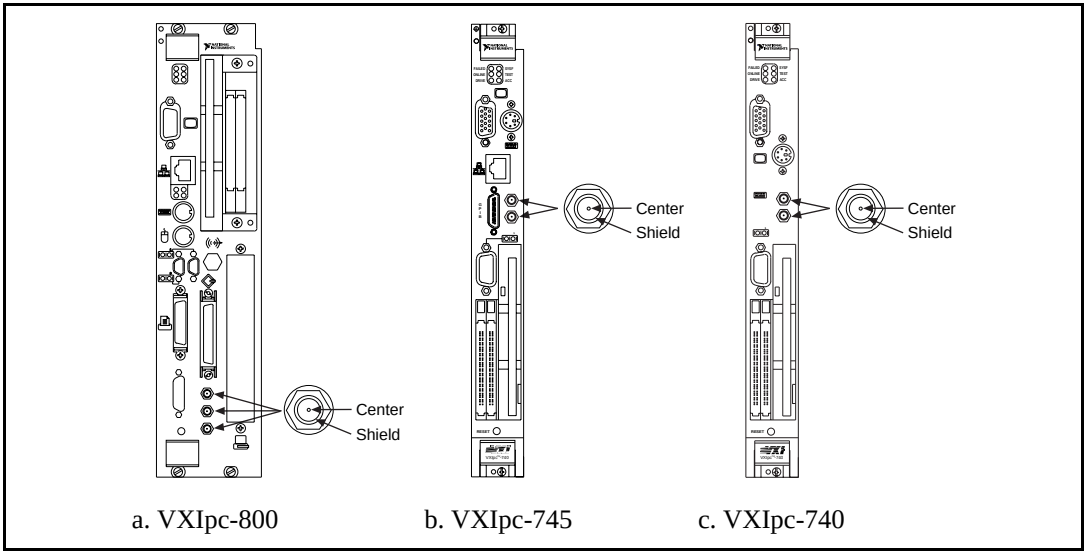
Table E-8. GPIB Connector Signals (Continued)

Pin	Signal Name	Signal Description
9	IFC*	Interface Clear
10	SRQ*	Service Request
11	ATN*	Attention
12	SHIELD	Chassis ground
13	DIO5*	Data Bit 5
14	DIO6*	Data Bit 6
15	DIO7*	Data Bit 7
16	DIO8*	Data Bit 8
17	REN*	Remote Enable
18-25	GND	Logic Ground

External SMBs

Figure E-11 shows the location and pinouts for the SMB connectors on the VXIpc 800/700. The SMB connectors are used for an external clock signal and TTL trigger input and output. Table E-9 gives the name and description for the SMB connector signals. Also see Table E-10 for a description of the signal characteristics for the SMB connections.

Amp manufactures an SMB mating connector, part number 1-413985-0.



a. VXIpc-800 b. VXIpc-745 c. VXIpc-740

Figure E-11. SMB Connectors Location and Pinout

Table E-9. SMB Connector Signals

Pin	Signal Description
Center	TTL Trigger or Clock Signal
Shield	Ground

Speaker

Figure E-12 shows the location of the speaker connection on the VXIpc-800.

Switchcraft manufactures a mating speaker connector, part number 750.

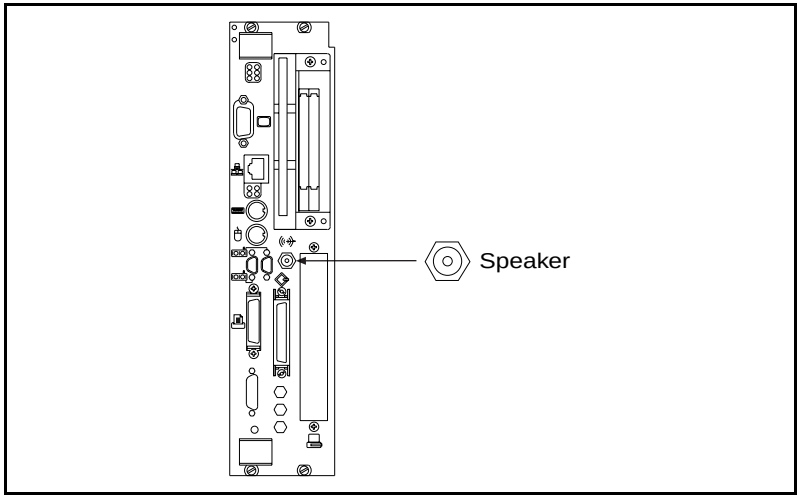


Figure E-12. Speaker Connection Location

Signal Characteristics

Refer to the relevant standard for the signal characteristics for VGA, SCSI, Ethernet, keyboard, mouse, parallel, serial, and GPIB.

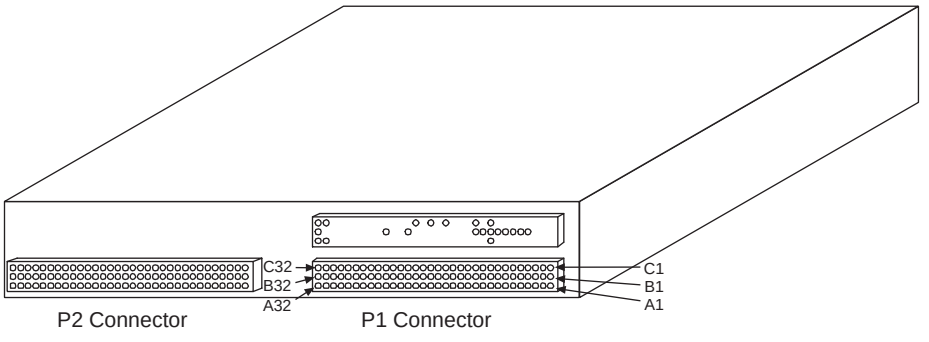
Table E-10 shows the signal characteristics for the SMB and speaker connections.

Table E-10. Signal Characteristics for SMB and Speaker Connections

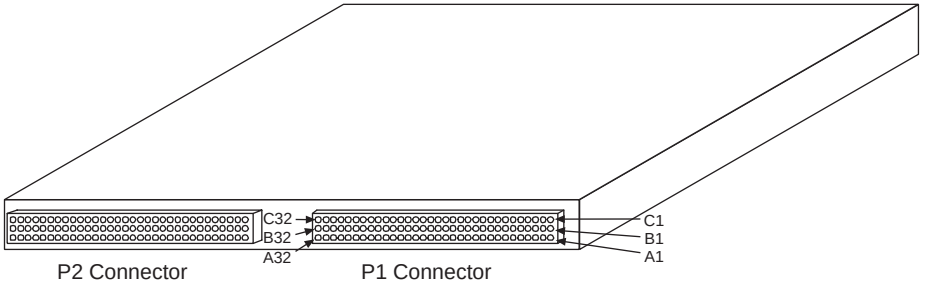
Signal	Voltage Range	Maximum Current	Frequency Range
SMB (TRIG out, CLK out)	0 to 3.4 V	200 mA	DC-10 MHz
Speaker (VXIpc-800 only)	0 to 4.3 V	75 mA	DC-20 kHz
SMB (TRIG in)	0 to 5 V	100 mA*	DC-10 MHz
* with 50 Ω termination			

VXIbus P1 and P2

Figure E-13 shows the location and pinouts for the VXIbus connector on the VXIpc 800/700 Series. Table E-11 gives the name and description for the VXIbus P2 connector signals. Table E-12 gives the name and description for the VXIbus P1 connector signals.



a. VXIpc-800 Series



b. VXIpc-700 Series

Figure E-13. VXIbus Connectors Location and Pinout

Table E-11. VXIbus P2 Connector Signals

Pin	Row C	Row B	Row A
1	CLK10+	+5 V	ECLTRG0
2	CLK10-	GND	-2 V
3	GND	Not Connected	ECLTRG1
4	-5.2 V	A24	GND
5	Not Connected	A25	MODID12
6	Not Connected	A26	MODID11
7	GND	A27	-5.2 V
8	Not Connected	A28	MODID10
9	Not Connected	A29	MODID09
10	GND	A30	GND
11	Not Connected	A31	MODID08
12	Not Connected	GND	MODID07
13	-2 V	+5 V	-5.2 V
14	Not Connected	D16	MODID06
15	Not Connected	D17	MODID05
16	GND	D18	GND
17	Not Connected	D19	MODID04
18	Not Connected	D20	MODID03
19	-5.2 V	D21	-5.2 V
20	Not Connected	D22	MODID02
21	Not Connected	D23	MODID01
22	GND	GND	GND
23	TTLTRG1*	D24	TTLTRG0*
24	TTLTRG3*	D25	TTLTRG2*
25	GND	D26	+5 V
26	TTLTRG5*	D27	TTLTRG4*
27	TTLTRG7*	D28	TTLTRG6*

Table E-11. VXIbus P2 Connector Signals

Pin	Row C	Row B	Row A
28	GND	D29	GND
29	Not Connected	D30	Not Connected
30	GND	D31	MODID00
31	Not Connected	GND	GND
32	Not Connected	+5 V	Not Connected

Table E-12. VXIbus P1 Connector Signals

Pin	Row C	Row B	Row A
1	D08	BBSY*	D00
2	D09	BCLR*	D01
3	D10	ACFAIL*	D02
4	D11	BG0IN*	D03
5	D12	BG0OUT*	D04
6	D13	BG1IN*	D05
7	D14	BG1OUT*	D06
8	D15	BG2IN*	D07
9	GND	BG2OUT*	GND
10	SYSFAIL*	BG3IN*	SYSCLK
11	BERR*	BG3OUT*	GND
12	SYSRESET*	BR0*	DS1*
13	LWORD*	BR1*	DS0*
14	AM5	BR2*	WRITE*
15	A23	BR3*	GND
16	A22	AM0	DTACK*
17	A21	AM1	GND
18	A20	AM2	AS*

Table E-12. VXIbus P1 Connector Signals (Continued)

Pin	Row C	Row B	Row A
19	A19	AM3	GND
20	A18	GND	IACK*
21	A17	Not Connected	IACKIN*
22	A16	Not Connected	IACKOUT*
23	A15	GND	AM4
24	A14	IRQ7*	A07
25	A13	IRQ6*	A06
26	A12	IRQ5	A05
27	A11	IRQ4	A04
28	A10	IRQ3	A03
29	A09	IRQ2	A02
30	A08	IRQ1	A01
31	+12 V	Not Connected	-12 V
32	+5 V	+5 V	+5 V

Modifying and Installing I/O Expansion Boards

Appendix F

This appendix explains how to modify and install an I/O board in the VXIpc 800 Series. This material is not relevant to the VXIpc 700 Series. Notice that some material is different between the VXIpc-850 and the VXIpc-860.

Height of VXIpc-850 Plug-In Boards

In general, the VXIpc-850 can accommodate any standard-size PCI card and any XT-height ISA card. The height of an I/O card is measured from the bottom of the bus connector to the top of the board, as shown in Figure F-1.

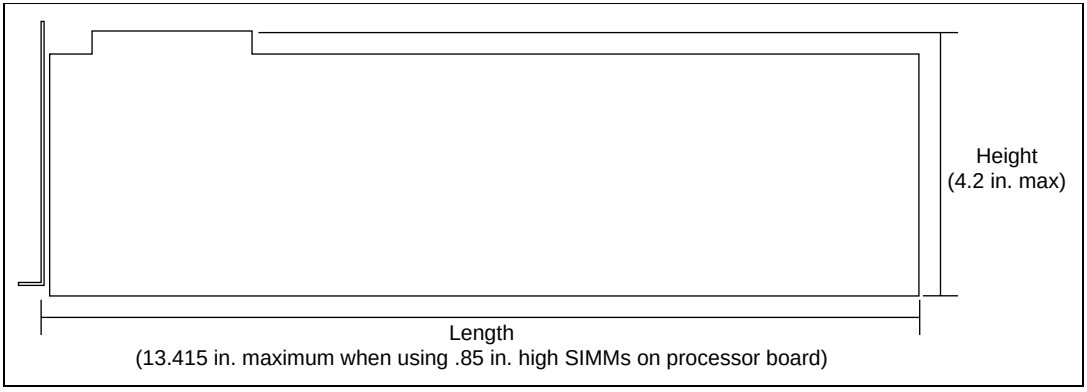


Figure F-1. I/O Board Dimensions for VXIpc-850 Expansion Slot

Both XT-height ISA cards and PCI cards are 4.2 in. high. The VXIpc-850 does not support AT-height ISA cards, which are 4.8 in. high.

Length of VXIpc-850 Plug-In Boards

The VXIpc-850 will accommodate PCI or ISA I/O boards that are up to 4.2 in. high and up to 13.415 in. long when DRAM SIMMs 0.85 in. or shorter are used on the processor board (National Instruments provides 0.85 in. SIMMs). Due to slight variances in I/O board geometries, however, take care in every application to identify possible contact with the SIMMs. The maximum recommended board length is 7.72 in. if you are using SIMMs taller than 0.85 in. If you want to install a longer board with taller system DRAM SIMMs, you must consider the strong possibility of contact with the SIMMs.

Because DRAM SIMMs taller than 0.85 in. are more likely to interfere with ISA cards than PCI cards, the two are discussed separately in the following sections.

Length of PCI Plug-In Boards

The VXIpc-850 accommodates any standard 32-bit, 5 V PCI I/O board. The maximum standard dimensions of a PCI I/O board are 12.283 in. by 4.2 in. PCI cards are installed in the VXIpc-850 such that the board components face the processor card, which contains the processor and system RAM. Exceptionally tall PCI components (higher than 0.52 in.) on a plug-in board may physically contact the DRAM SIMMs installed on the CPU card, as shown in Figure F-2.

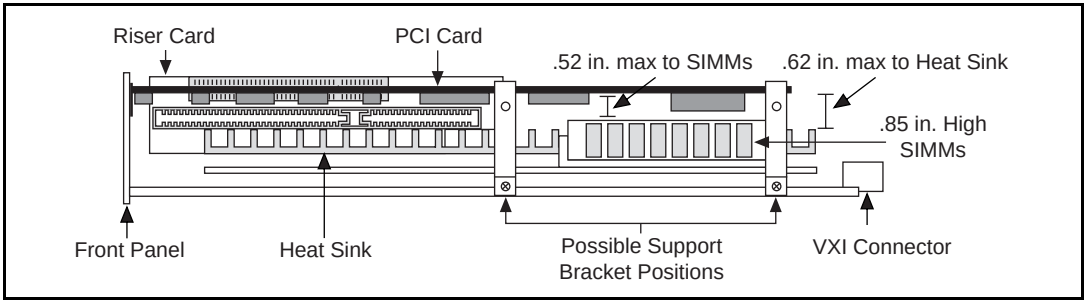


Figure F-2. Component Height Restrictions for PCI Plug-In Boards Used with the VXIpc-850

Because larger DRAM sizes may employ taller SIMMs, you must be careful that the component height of the PCI board does not interfere with the SIMMs in such a way that electrical shorts occur or that the PCI board is stressed enough to compromise its physical or electrical integrity.

Length of ISA Plug-In Boards

Because ISA boards mount with the components facing away from the CPU card, which contains the processor and system RAM, tall DRAM SIMMs may interfere with the circuit card of the plug-in board. Most SIMM modules are at least 1 in. high and would extend into the ISA circuit card keepout area. Therefore, National Instruments provides SIMMs of 0.85 in. high, and does not recommend the use of cards that are longer than 7.72 in. if you choose to use taller SIMMs. They can be made to fit; however, take care to ensure the following conditions:

- Contact of the board with the SIMMs should not cause electrical shorts (because of through-hole component leads on the side of the plug in board that contacts the SIMMs).
- Contact of the board with the SIMMs does not cause physical stress to the plug-in board.
- Contact of the board with the SIMMs does not cause physical stress to the riser board into which the plug-in board is inserted.

Figure F-3 shows the length restrictions for ISA cards for the VXIpc-850 when tall SIMMs are used.

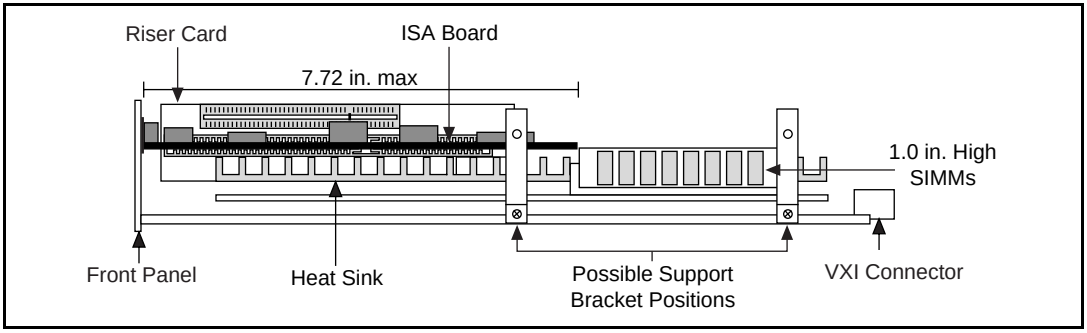


Figure F-3. Length Restrictions for ISA Plug-In Boards Used with the VXIpc-850 when SIMMs Taller than 0.85 in. Are Used

Height of VXIpc-860 Plug-In Boards

In general, the VXIpc-860 can accommodate any standard-size PCI card and any XT-height ISA card. The height of an I/O card is measured from the bottom of the bus connector to the top of the board, as shown in Figure F-4.

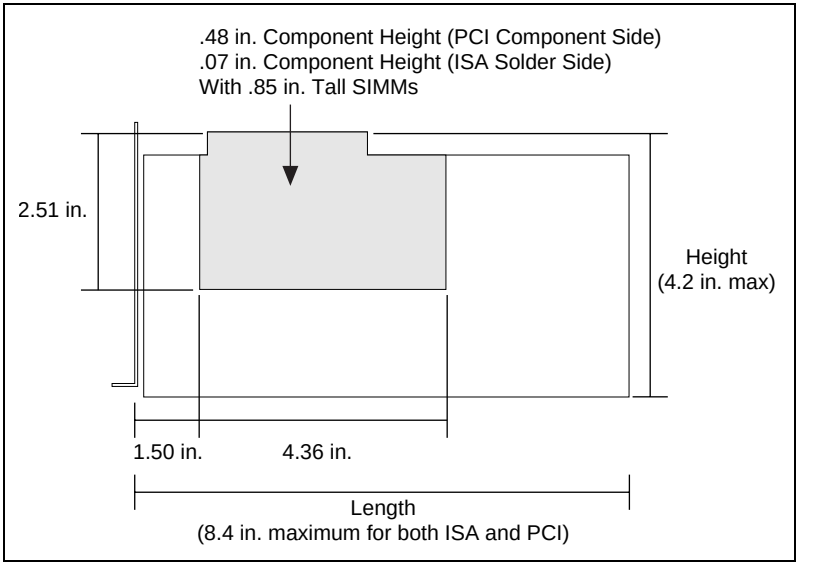


Figure F-4. I/O Board Dimensions for VXIpc-860 Expansion Slot

Both XT-height ISA cards and PCI cards are 4.2 in. high. The VXIpc-860 does not support AT-height ISA cards, which are 4.8 in. high.

Length of VXIpc-860 Plug-In Boards

The VXIpc-860 can accommodate PCI or ISA I/O boards that are up to 4.2 in. high and up to 8.4 in. long. When DRAM SIMMs 0.85 in. are used on the processor board, the component-side component height on a PCI I/O board shall be 0.48 in. tall in the area shown in Figure F-4. For the same height DRAM SIMMs, the ISA I/O board solder-side component height shall be 0.07 in. tall in the area shown in Figure F-4. Be sure to allow for clearance if you want to use taller DRAM SIMMs.

Because DRAM SIMMs taller than 0.85 in. are more likely to interfere with ISA cards than PCI cards, the two are discussed separately in the following sections.

Length of PCI Plug-In Boards

The VXIpc-860 accommodates any 32-bit, 5 V PCI I/O boards that are a maximum of 8.4 in. long with the standard height of 4.2 in. (see Figure F-5). PCI cards are installed in the VXIpc-860 such that the board components face the processor board, which contains the processor and system RAM. PCI components (higher than 0.48 in. in the area shown in Figure F-4) on a plug-in board may physically contact the DRAM SIMMs installed on the CPU card, as shown in Figure F-5.

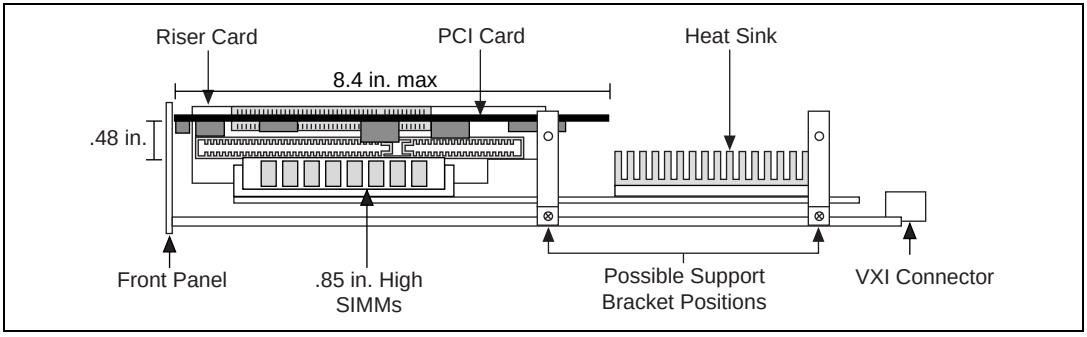


Figure F-5. Length Restrictions for PCI Plug-In Boards Used with the VXIpc-860

Because larger DRAM sizes may employ taller SIMMs, you must be careful that the component height of the PCI board does not interfere with the SIMMs in such a way that electrical shorts occur or that the PCI board is stressed enough to compromise its physical or electrical integrity.

Length of ISA Plug-In Boards

Because ISA boards mount with the components facing away from the CPU card, which contains the processor and system RAM, tall DRAM SIMMs may interfere with the circuit card of the plug-in board. Most SIMM modules are at least 1 in. high and would extend into the ISA circuit card. Therefore, National Instruments provides SIMMs of 0.85 in. high to give a clearance of 0.07 in. to the ISA circuit card only in the area shown in Figure F-4. If you choose to use taller SIMMs (0.92 maximum height), take care to ensure the following conditions:

- Contact of the board with the SIMMs should not cause electrical shorts (because of through-hole component leads on the side of the plug in board that contacts the SIMMs).
- Contact of the board with the SIMMs does not cause physical stress to the plug-in board.
- Contact of the board with the SIMMs does not cause physical stress to the riser board into which the plug-in board is inserted.

Figure F-6 shows the length restrictions for ISA cards for the VXIpc-860 when tall SIMMs are used.

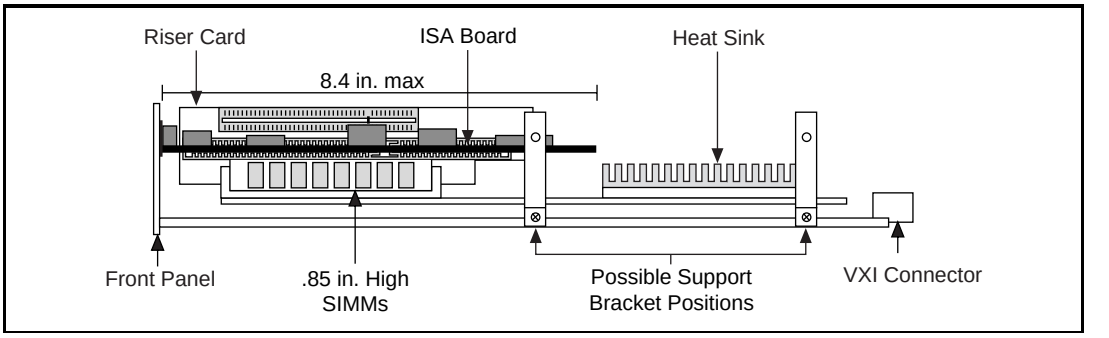


Figure F-6. Length Restrictions for ISA Plug-In Boards Used with the VXIpc-860 when SIMMs Taller than 0.85 in. Are Used

Installing an I/O Board

The following material applies equally to all models in the VXIpc 800 Series.

Materials Needed

You need the following pieces to install an expansion board in the VXIpc-800.

- A user-defined panel is required on the expansion boards based on National Instruments blank PCI or ISA panels. The panel should be manufactured from 0.03 in. (0.76 mm) thick 1010 cold rolled steel. The finish should be 0.0003 in. (0.008 mm) nickel plate over 0.0001 in. (0.003 mm) copper flash. Contact National Instruments if you have any questions about specifications for user-defined panels.

As an alternative, you can modify the blank panels provided with the VXIpc-800. Custom panel design services are available from National Instruments for a nominal fee.

- PEM nut or a 4-40 nut with lock washer (hole diameter should be 0.125 in. [3.18 mm]).
- 4-40 x 1/4 in. stainless steel panhead screw.

Installation Steps

Follow these steps to install the expansion board.

1. Notice that the VXIpc-800 includes one card guide that can be rotated to secure the top of either a PCI or an ISA plug-in board. You may use the card guide in either of two locations, depending on the length of your installed board. Notice the possible locations as shown previously (Figures F-2 and F-3 for the VXIpc-850, or Figures F-5 and F-6 for the VXIpc-860). Loosen the screw holding the guide and rotate the guide to the uppermost position for PCI boards or the lowermost position for ISA boards. See Figure F-7.
2. Replace the manufacturer-supplied panel bracket with the custom bracket. The two holes in the bracket align with existing holes in the VXIpc-800 front panel.
3. Seat the board firmly in one of the expansion bus connectors.
4. Attach the board to the front panel with the two screws. You can use a PEM nut on the back (far) side of the bracket, or a 4-40 nut with a lock washer (hole diameter should be 0.125 in.). In either case, use a 4-40 x 1/4 in. stainless steel panhead screw to mount the board/bracket assembly to the front panel.

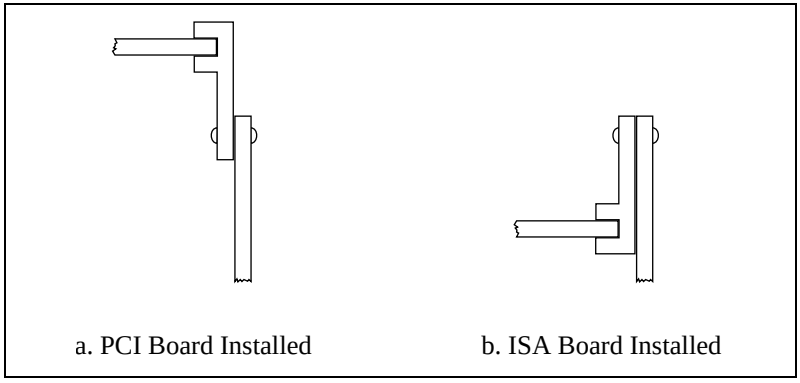


Figure F-7. PCI Board and ISA Board Installed in a VXIpc-800

Figures F-8 and F-9 give dimensions and instructions for creating a connector cutout and expansion bracket for a PCI board.

☞ **Note:** ***In the following drawings, dimensions are given in inches and millimeters, with the millimeter dimensions in square brackets.***

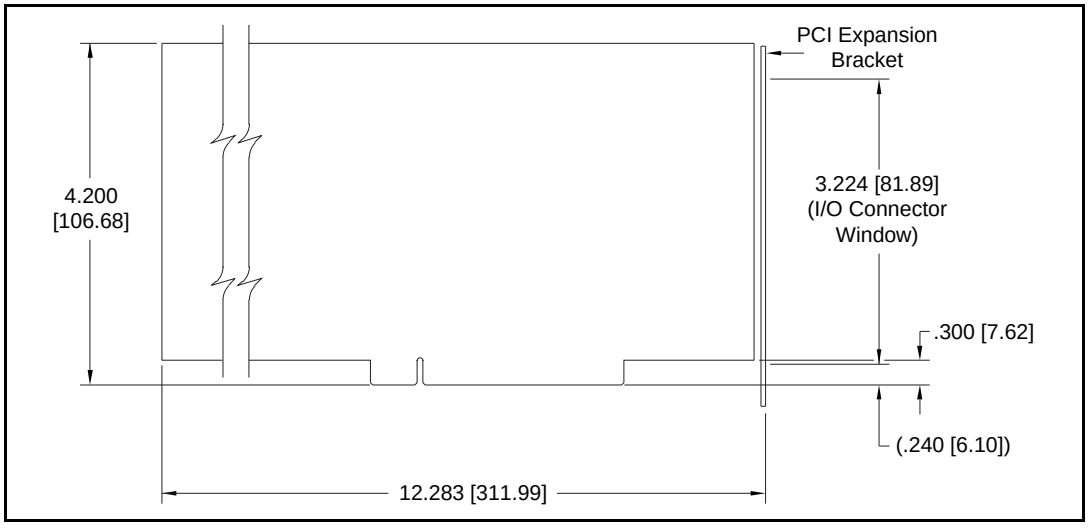


Figure F-8. Side View of PCI Board with Expansion Bracket

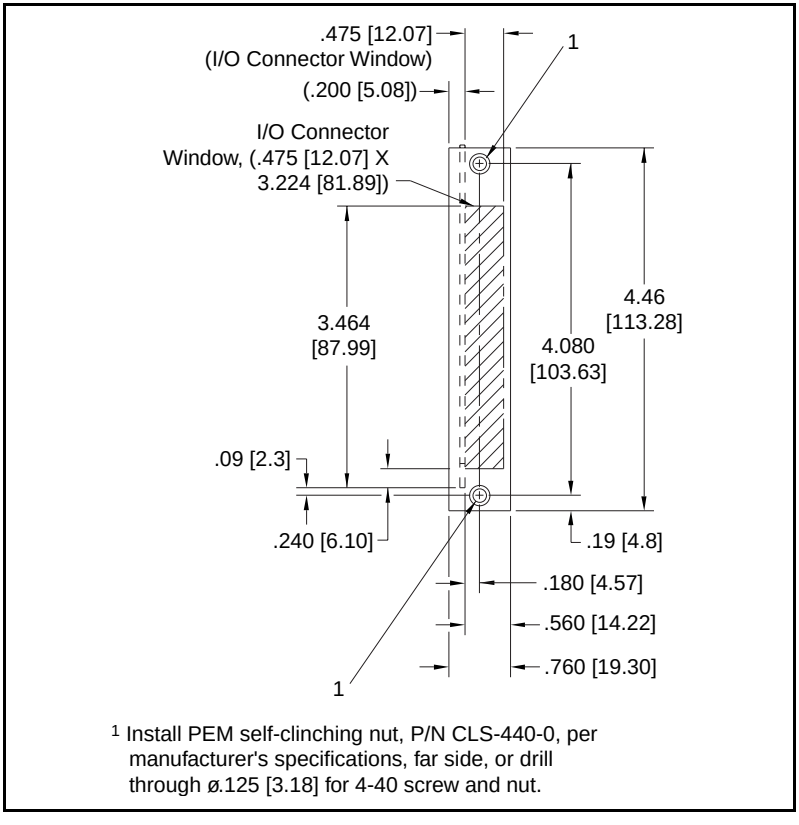
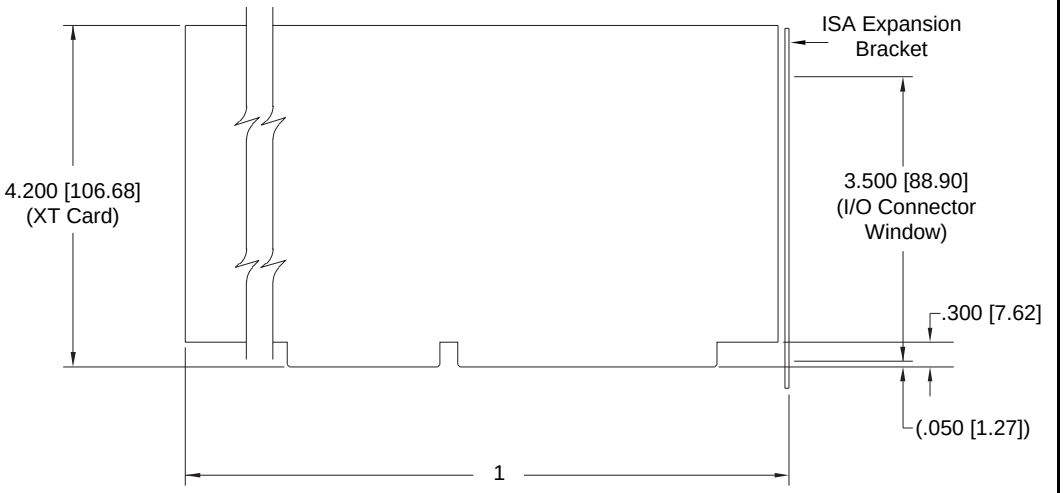


Figure F-9. Front View of PCI Board Expansion Bracket

Figures F-10 and F-11 give dimensions and instructions for creating a connector cutout and expansion bracket for an ISA board.



[1] Length of XT ISA card shall be 7.72 [196.1] if SIMMs on processor card are taller than .85 [21.6]. Otherwise ISA specification length of 13.415 [340.74] may be used.

Figure F-10. Side View of ISA Board with Expansion Bracket

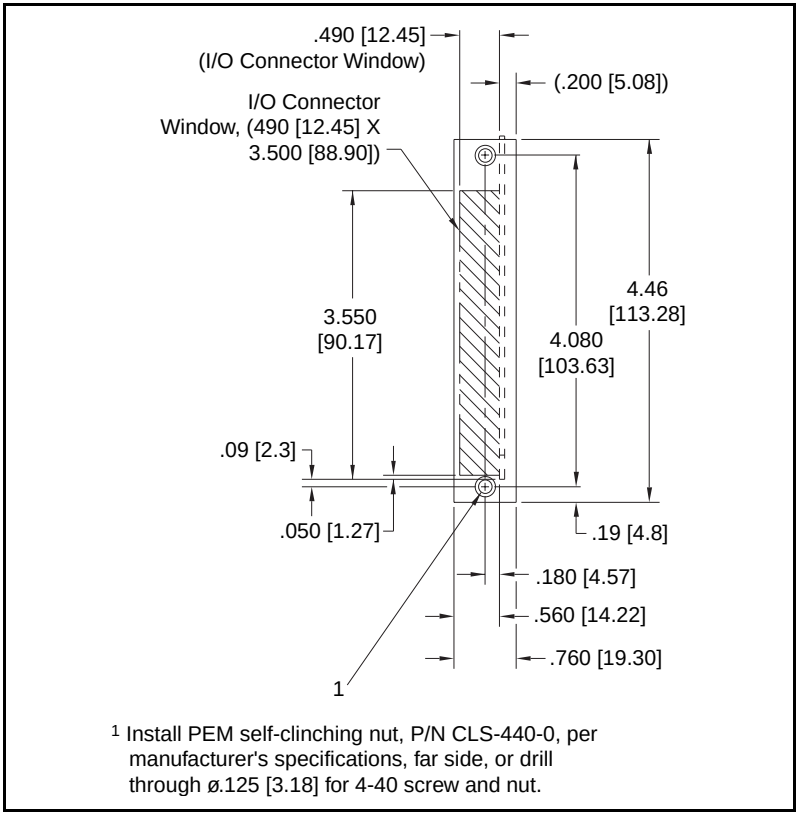


Figure F-11. Front View of ISA Board Expansion Bracket

Common Questions

Appendix G

This appendix answers common questions you may have when using the VXIpc 800/700 Series.

What do the LEDs on the front of the VXIpc 800/700 mean?

Refer to Appendix D, *LED Indicators*, for a description of the front panel LEDs.

Is something wrong with the VXIpc 800/700 if the red SYSFAIL and FAILED LEDs stay lit after booting the VXIpc 800/700?

If either the **SYSFAIL** or **FAILED** LED remains lit, refer to Appendix D, *LED Indicators*, for troubleshooting steps.

Can I access 32-bit registers in my VXIbus system from the VXIpc 800/700?

Yes. The VXIpc 800/700 uses the 32-bit PCI bus to interface to the VXIbus. In fact, the VXIbus circuitry on the VXIpc 800/700 also supports the new VME64 standard for D64 accesses. Refer to your VXI software manual for more information.

What kind of signal is CLK10 and what kind of signal do I need for an external CLK10?

CLK10 is a differential ECL signal on the backplane. However, the oscillator for the VXIpc 800/700 and the EXTCLK input on the VXIpc-800 front panel use TTL levels; therefore, you need to supply a TTL-level signal for EXTCLK. Our voltage converters convert the signal to differential ECL. You cannot drive CLK10 externally on the VXIpc 700 Series.

What is the accuracy of the CLK10 signal?

The CLK10 signal generated by the VXIpc 800/700 is ±100 ppm (0.01%) as per the VXIbus specification. If you need a more accurate CLK10 signal on the VXIpc 800 Series, you can use the EXTCLK connector on its front panel.

If I boot the computer without video, and then plug in the video, why is it in black and white?

When the computer first boots, the video chips try to synchronize with the monitor. If the monitor is not there, the video chips cannot synchronize and establish color. You need to have the monitor attached at boot time to get color.

I've installed the SCSI software. Why doesn't the VXIpc 800 Series recognize my SCSI device?

- **VXIpc 800 Series users only**—This problem usually occurs when SCSI has not been enabled in the BIOS. Enter your BIOS by pressing <DEL> at boot time. In the **Setup** window, click on the **Chipset** icon. Select the **On Board PCI SCSI** option and enable it.

What type of video interface is onboard the VXIpc 800/700? What video drivers are included with the VXIpc 800/700? Can I use Super VGA with my VXIpc 800/700? If my application requires a special type of video display, how do I configure my VXIpc-800/700?

The VXIpc 800/700 uses the Trident Microsystems TGUI96*xx* chip family, a combination graphics accelerator and RAMDAC. The TGUI9660 is the first chip used from this family. Subsequent pin-compatible chips will follow. The chips in this family are compatible with the Standard VGA video output MS Windows video driver, as well as the Trident Microsystems video driver. For more information on the video driver, refer to the `c:\images\manuals` directory.

- **VXIpc 800 Series users only**—If your application requires a special type of video display, you can remove the PCI video card included with your VXIpc 800 Series. Contact National Instruments for information on how to do this.

What kind of monitor can I use with the VXIpc 800/700?

VXIpc 800/700 computers that use Super VGA video output will work only with monitors having a horizontal scan rate of at least 50 kHz and a vertical scan rate of 60 Hz.

Warning: ***Make sure that your monitor meets this specification. Enabling the Super VGA option on a monitor that does not meet this specification will cause damage to your monitor.***

What if my keyboard connector does not fit into the keyboard port on the VXIpc 800/700?

You can plug keyboards that have a 6-pin Mini DIN PS/2 type connector directly into the VXIpc 800/700. You can use the keyboard adapter cable that is included with every VXIpc 800/700 kit to adapt the larger AT keyboard connector to the 6-pin Mini DIN connector.

How do I connect an external speaker to get audio capability?

- **VXIpc 800 Series users only**—A twisted-pair cable connects the front panel audio connector to the VXIpc 800 Series motherboard. Connect the external speaker to this front-panel connector. The center pin of the connector provides the audio signal. The shield of the connector is GROUND.

How do I add RAM to the VXIpc 800/700? What is the maximum amount of RAM that I can have on the VXIpc 800/700?

For information about adding RAM to the VXIpc 800/700, refer to Appendix A, *Specifications*.

Which interrupt levels are free to be used by ISA bus boards? Which area of upper memory (adapter space) is free for use by ISA bus boards or expanded memory manager software programs?

See Appendix B, *VXIpc 800 Series System Resources,* or Appendix C, *VXIpc 700 Series System Resources,* for information on the available port I/O register space, upper memory area, interrupts, and DMA channels.

How do I install the VXIpc 800/700 in a slot other than Slot 0?

The VXIpc 800/700 automatically detects whether it is in Slot 0 of a VXIbus mainframe. You do not need to change jumper settings to install the VXIpc 800/700 in a slot other than Slot 0 unless you have defeated the first slot detector (FSD) circuitry by changing the appropriate jumper setting on the VXIpc 800/700.

Refer to either Chapter 3, *VXIpc 800 Series Configuration and Installation*, or Chapter 4, *VXIpc 700 Series Configuration and Installation*, for information on enabling and defeating the FSD circuitry.

How do I check the configuration of the memory, floppy drive, hard drive, time/date, and so on?

You can view these parameters in the BIOS setup. To enter the BIOS setup, reboot the VXIpc 800/700 and press the <DEL> key during the memory tests. Refer to Chapter 5, *BIOS*, for more information.

Can I upgrade my VXIpc 800/700?

- **VXIpc 800 Series users only**—You can upgrade the modular CPU card to upgrade the processor. Contact National Instruments for information.

How can I boot from an external SCSI hard drive?

- **VXIpc 800 Series users only**—In the **Setup/Standard** window change the Master drive setting from **User** to **SCSI**, and make sure that SCSI is enabled in the **Setup/Chipset** window.

Can I use the internal IDE drive and an external SCSI hard drive at the same time?

- **VXIpc 800 Series users only**—Yes, but you can only boot from the internal IDE drive in this configuration.

My CMOS is corrupted. How do I set it back to default?

1. Select **Default/Optimal**.
2. Save and exit.

Customer Communication

Appendix
H

For your convenience, this appendix contains forms to help you gather the information necessary to help us solve your technical problems and a form you can use to comment on the product documentation. When you contact us, we need the information on the Technical Support Form and the configuration form, if your manual contains one, about your system configuration to answer your questions as quickly as possible.

National Instruments has technical assistance through electronic, fax, and telephone systems to quickly provide the information you need. Our electronic services include a bulletin board service, an FTP site, a Fax-on-Demand system, and e-mail support. If you have a hardware or software problem, first try the electronic support systems. If the information available on these systems does not answer your questions, we offer fax and telephone support through our technical support centers, which are staffed by applications engineers.

Electronic Services



Bulletin Board Support

National Instruments has BBS and FTP sites dedicated for 24-hour support with a collection of files and documents to answer most common customer questions. From these sites, you can also download the latest instrument drivers, updates, and example programs. For recorded instructions on how to use the bulletin board and FTP services and for BBS automated information, call (512) 795-6990. You can access these services at:

United States: (512) 794-5422
Up to 14,400 baud, 8 data bits, 1 stop bit, no parity

United Kingdom: 01635 551422
Up to 9,600 baud, 8 data bits, 1 stop bit, no parity

France: 01 48 65 15 59
Up to 9,600 baud, 8 data bits, 1 stop bit, no parity



FTP Support

To access our FTP site, log on to our Internet host, `ftp.natinst.com`, as `anonymous` and use your Internet address, such as `joesmith@anywhere.com`, as your password. The support files and documents are located in the `/support` directories.



Fax-on-Demand Support

Fax-on-Demand is a 24-hour information retrieval system containing a library of documents on a wide range of technical information. You can access Fax-on-Demand from a touch-tone telephone at (512) 418-1111.



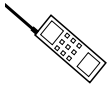
E-Mail Support (currently U.S. only)

You can submit technical support questions to the applications engineering team through e-mail at the Internet address listed below. Remember to include your name, address, and phone number so we can contact you with solutions and suggestions.

`support@natinst.com`

Telephone and Fax Support

National Instruments has branch offices all over the world. Use the list below to find the technical support number for your country. If there is no National Instruments office in your country, contact the source from which you purchased your software to obtain support.



	Telephone	Fax
Australia	03 9879 5166	03 9879 6277
Austria	0662 45 79 90 0	0662 45 79 90 19
Belgium	02 757 00 20	02 757 03 11
Canada (Ontario)	905 785 0085	905 785 0086
Canada (Quebec)	514 694 8521	514 694 4399
Denmark	45 76 26 00	45 76 26 02
Finland	09 527 2321	09 502 2930
France	01 48 14 24 24	01 48 14 24 14
Germany	089 741 31 30	089 714 60 35
Hong Kong	2645 3186	2686 8505
Israel	03 5734815	03 5734816
Italy	02 413091	02 41309215
Japan	03 5472 2970	03 5472 2977
Korea	02 596 7456	02 596 7455
Mexico	5 520 2635	5 520 3282
Netherlands	0348 433466	0348 430673
Norway	32 84 84 00	32 84 86 00
Singapore	2265886	2265887
Spain	91 640 0085	91 640 0533
Sweden	08 730 49 70	08 730 43 70
Switzerland	056 200 51 51	056 200 51 55
Taiwan	02 377 1200	02 737 4644
U.K.	01635 523545	01635 523154

Technical Support Form

Photocopy this form and update it each time you make changes to your software or hardware, and use the completed copy of this form as a reference for your current configuration. Completing this form accurately before contacting National Instruments for technical support helps our applications engineers answer your questions more efficiently.

If you are using any National Instruments hardware or software products related to this problem, include the configuration forms from their user manuals. Include additional pages if necessary.

Name _____

Company _____

Address _____

_____

Fax (___)_____ Phone (___) _____

Computer brand _____ Model _____ Processor_____

Operating system (include version number) _____

Clock speed _____MHz RAM _____MB Display adapter _____

Mouse ___yes ___no Other adapters installed _____

Hard disk capacity _____MB Brand _____

Instruments used _____

_____

National Instruments hardware product model _____ Revision _____

Configuration _____

National Instruments software product _____ Version _____

Configuration _____

The problem is: _____

_____

_____

_____

_____

List any error messages: _____

_____

_____

The following steps reproduce the problem:_____

_____

_____

_____

_____

_____

Hardware and Software Configuration Form

Record the settings and revisions of your hardware and software on the line to the right of each item. Complete a new copy of this form each time you revise your software or hardware configuration, and use this form as a reference for your current configuration. Completing this form accurately before contacting National Instruments for technical support helps our applications engineers answer your questions more efficiently.

National Instruments Products

VXIpc 800 Series Hardware Settings

VXIpc 800 Series Model Number_____

VXIpc 800 Series Part Number_____

VXIpc 800 Series Serial Number _____

Processor Speed_____

DRAM SIMMs Installed_____

Hard Drive Size_____ Video Memory _____

Slot Location _____

W1 Setting: SCSI Termination _____

W2 Setting: CMOS _____

W4 Setting: LPT1 DMA _____

W13 Setting: Slot 0 Detection_____

S1 Setting: Ethernet EEPROM _____

S2 Setting: MITE Self-Configuration _____

S3 Setting: CLK10 Source _____

S4 Setting: Inverted/Non-inverted CLK10 Output _____

S5 Setting: CLK10 SMB_____

S6 Setting: CLK10 Input Termination _____

S7 Setting: External Trigger Input Termination _____

S8 Setting: GPIB IRQ Level _____

S9 Setting: MITE User/Factory Configuration _____

VXIpc 700 Series Hardware Settings

VXIpc 700 Series Model Number _____

VXIpc 700 Series Part Number _____

VXIpc 700 Series Serial Number _____

Processor Speed _____

DRAM SIMMs Installed _____

Hard Drive Size _____ Video Memory _____

Slot Location _____

W1 Setting: Slot 0 Detection _____

W3 Setting: CMOS _____

W6 Setting: Ethernet EEPROM _____

W7 Setting: MITE User/Factory Configuration _____

W10 Setting: MITE Self-Configuration _____

W12 Setting: External Trigger Input Termination _____

Other Products

Mainframe make and model _____

Microprocessor _____

Clock frequency or speed _____

Type of video board installed _____

Operating system version _____

Operating system mode _____

Programming language _____

Programming language version _____

Other boards in system _____

Monitor (manufacturer, model) _____

Mouse (manufacturer, model) _____

Keyboard (manufacturer, model) _____

Other peripherals (manufacturer, model) _____

_____

_____

_____

Documentation Comment Form

National Instruments encourages you to comment on the documentation supplied with our products. This information helps us provide quality products to meet your needs.

Title: *VXIpc™ 800/700 Series User Manual*

Edition Date: April 1997

Part Number: 321124C-01

Please comment on the completeness, clarity, and organization of the manual.

_____

_____

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If you find errors in the manual, please record the page numbers and describe the errors.

_____

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_____

Thank you for your help.

Name _____

Title _____

Company _____

Address _____

_____

Phone (___)_____ Fax (___) _____

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Glossary

Prefix	Meaning	Value
n-	nano-	10^{-9}
µ-	micro-	10^{-6}
m-	milli-	10^{-3}
K-	kilo-	10^{3}
M-	mega-	10^{6}
G-	giga-	10^{9}

A

A	Amperes
address	Character code that identifies a specific location (or series of locations) in memory
address space	A set of 2^n memory locations differentiated from other such sets in VXI/VMEbus systems by six addressing lines known as address modifiers. *n* is the number of address lines required to uniquely specify a byte location in a given space. Valid numbers for *n* are 16, 24, and 32. In VME/VXI, because there are six address modifiers, there are 64 possible address spaces.
ANSI	American National Standards Institute
ASIC	application-specific integrated circuit

B

b	bits
B	bytes
backplane	An assembly, typically a printed circuit board, with 96-pin connectors and signal paths that bus the connector pins. A C-size VXIbus system will have two sets of bused connectors called J1 and J2. A D-size VXIbus system will have three sets of bused connectors called J1, J2, and J3.
BERR*	Bus error signal
BIOS	Basic Input/Output System. BIOS functions are the fundamental level of any PC or compatible computer. BIOS functions embody the basic operations needed for successful use of the computer's hardware resources.

C

CLK10	A 10 MHz, ±100 ppm, individually buffered (to each module slot), differential ECL system clock that is sourced from Slot 0 of a VXIbus mainframe and distributed to Slots 1 through 12 on P2. It is distributed to each slot as a single-source, single-destination signal with a matched delay of under 8 ns.
CMOS	Complementary Metal Oxide Semiconductor; a process used in making chips

D

DIN	Deutsches Institut für Normung—German Standards Institute
DMA	Direct Memory Access; a method by which data is transferred between devices and internal memory without intervention of the central processing unit
DRAM	Dynamic RAM (Random Access Memory); storage that the computer must refresh at frequent intervals

E

ECL	Emitter-Coupled Logic
EDO	Extended Data Out; a DRAM architecture that shortens overall access latency, improving performance
EEPROM	Electronically Erasable Programmable Read Only Memory
embedded controller	An intelligent CPU (controller) interface plugged directly into the VXI backplane, giving it direct access to the VXIbus. It must have all of its required VXI interface capabilities built in.
EMC	Electromagnetic Compliance

F

fair requester	A VXIbus device that will not arbitrate for the VXIbus after releasing it until it detects the bus request signal inactive. This ensures that all requesting devices will be granted use of the bus.
FPERR	Floating Point Error

G

GPIB	General Purpose Interface Bus (IEEE 488)

H

hex	hexadecimal; the numbering system with base 16, using the digits 0 to 9 and letters A to F
Hz	hertz; cycles per second

I

IDE	Integrated Drive Electronics. Denotes the most common interface to the hard drive on PCs.
IEEE	Institute of Electrical and Electronics Engineers

in.	inches
I/O	input/output; the techniques, media, and devices used to achieve communication between machines and users
INT	interrupt
INTA	PCI interrupt A
INTB	PCI interrupt B
INTC	PCI interrupt C
INTD	PCI interrupt D
interrupt	A means for a device to request service from another device
interrupt handler	A VMEbus functional module that detects interrupt requests generated by interrupters and responds to those requests by requesting status and identify information
interrupt level	The relative priority at which a device can interrupt
IRQ*	Interrupt signal
ISA	Industry Standard Architecture denotes a common expansion bus used in PCs

K

KB	kilobytes of memory

L

LED	Light-emitting diode

M

m	meters
master	A functional part of a VME/VXIbus device that initiates data transfers on the backplane. A transfer can be either a read or a write.

MB	megabytes of memory
MITE	A National Instruments custom ASIC, a sophisticated dual-channel DMA controller that incorporates the Synchronous MXI and VME64 protocols to achieve high-performance block transfer rates
MODID	Module ID Lines. Used in VXI to geographically locate boards and to dynamically configure boards
MTBF	Mean Time Between Failure

N

NI-VXI	The National Instruments bus interface software for VME/VXIbus systems
NMI	NonMaskable Interrupt
Non-Slot 0 device	A device configured for installation in any slot in a VXIbus mainframe other than Slot 0. Installing such a device into Slot 0 can damage the device, the VXIbus backplane, or both.

P

PCI	Peripheral Component Interconnect. The PCI bus is a high-performance 32-bit or 64-bit bus with multiplexed address and data lines.
PCMCIA	Personal Computer Memory Card International Association
PEM	Penn Engineering Manufacturing Corporation. A manufacturer of nuts pressed into metal
POSC	Power-On Self Configuration. A process by which the MITE chip programs its own registers from EEPROMs at power up
PWB	Printed Wire Board

R

Resource Manager	A message-based Commander located at Logical Address 0, which provides configuration management services such as address map configuration, Commander and Servant mappings, and self-test and diagnostic management

S

s	seconds
SCSI	Small Computer System Interface
SIMM	Single In-line Memory Module
slave	A functional part of a VME/VXIbus device that detects data transfer cycles initiated by a VMEbus master and responds to the transfers when the address specifies one of the device's registers
Slot 0 device	A device configured for installation in Slot 0 of a VXIbus mainframe. This device is unique in the VXIbus system in that it performs the VMEbus System Controller functions, including clock sourcing and arbitration for data transfers across the backplane. Installing such a device into any other slot can damage the device, the VXIbus backplane, or both.
SMB	Sub Miniature Type B connector that features a snap coupling for fast connection

T

trigger	Either TTL or ECL lines used for intermodule communication
TTL	Transistor-Transistor Logic

V

V	Volts
VME	Versa Module Eurocard or IEEE 1014

VMEbus System Controller	A device configured for installation in Slot 0 of a VXIbus mainframe or Slot 1 of a VMEbus chassis. This device is unique in the VMEbus system in that it performs the VMEbus System Controller functions, including clock sourcing and arbitration for data transfers across the backplane. Installing such a device into any other slot can damage the device, the VMEbus/VXIbus backplane, or both.
VXIbus	VMEbus Extensions for Instrumentation
VXIedit	VXI Resource Editor program, a part of the NI-VXI bus interface software. Used to configure the system, edit the manufacturer name and ID numbers, edit the model names of VXI and non-VXI devices in the system, as well as the system interrupt configuration information, and display the system configuration information generated by the Resource Manager.
VXIinit	A program in the NI-VXI bus interface software package that initializes the board interrupts, shared RAM, VXI register configurations, and bus configurations

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