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CONFIGURING WITH POWER PMAC

Quick Review: Nodes and Addressing

Two different types of MACRO interfaces are available for Power PMAC: Gate2-style and Gate3-style.

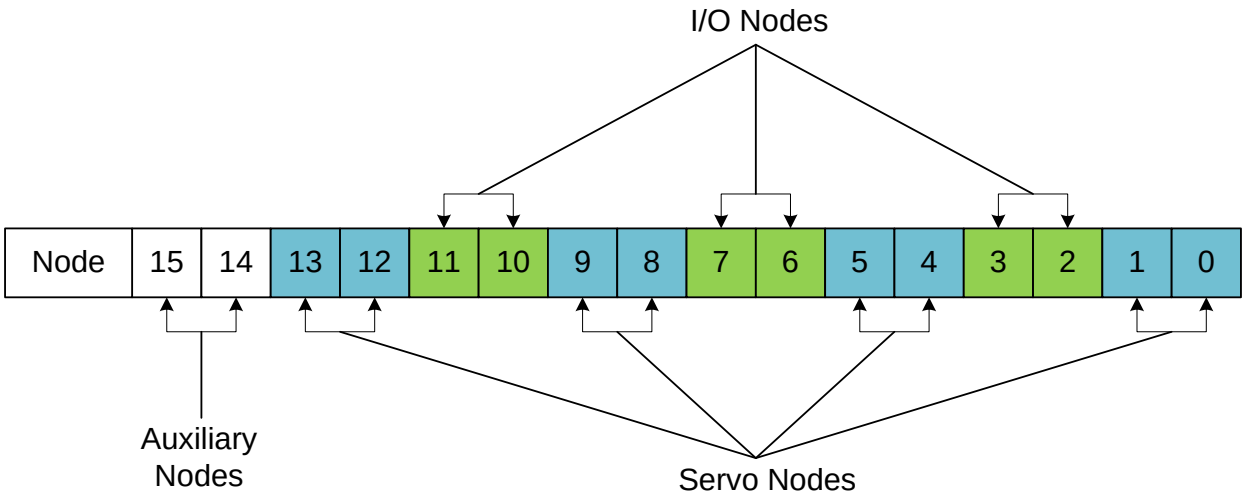
Gate3-style MACRO interfaces have two "banks" of MACRO registers, "Bank A" and "Bank B." As of the date this manual was written, ACC-5E3 is the only Gate3-style MACRO interface for Power PMAC.

Gate2-style MACRO interfaces have up to two ICs, each possessing its own registers for MACRO settings. As of the date this manual was written, ACC-5E is the only Gate2-style MACRO interface for Power PMAC.

Each MACRO IC (for Gate2-style MACRO interfaces) or each MACRO bank (Bank A and Bank B, for Gate3-style MACRO interfaces) consists of 16 nodes: 2 auxiliary, 8 servo, and 6 I/O nodes:

- Auxiliary nodes are Master/Control registers and are for internal firmware use.
- Servo nodes carry information such as feedback, commands, and flags for motor control.
- I/O nodes are by default unoccupied and are user configurable for transferring miscellaneous data.

Each motor that the ring controller controls requires one servo node, and therefore one ACC-5E3 or ACC-5E can control a maximum of 16 motors. The number of I/O nodes used depends on what I/O devices ACC-5E3 or ACC-5E is controlling over the MACRO ring. A visual representation of the nodes' individual functionality is given below:



With Gate3-style MACRO, each node consists of 8 registers: four 32-bit "Input" registers, which can be accessed by the structure **Gate3[*i*].MacroInA[*j*][*k*]** for bank A and **Gate3[*i*].MacroInB[*j*][*k*]** for bank B, and four 32-bit "Output" registers, which can be accessed by the Power PMAC structures **Gate3[*i*].MacroOutA[*j*][*k*]** for bank A and **Gate3[*i*].MacroOutB[*j*][*k*]** for bank B. Gate2-style MACRO interfaces has 4 registers for each node, wherein the input and output data share the registers; they are all grouped into this structure: **Gate2[*i*].Macro[*j*][*k*]**.

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