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ACR8000 ORDERING MATRIX

EXAMPLE: 8-Axis PC-Bus Servo Controller with Serial Ports

1-8 AXIS PC-BUS CONTROLLER

EXAMPLE: ACR8000 / PC / E8 / D4 / D4 / A0 / 1 / 0

PC=PC/ISA Bus Card
SA=Standalone Card

E0 = 0 Encoder inputs
E2 = 2 Encoder inputs
E4 = 4 Encoder inputs
E6 = 6 Encoder inputs
E8 = 8 Encoder inputs

D2/00 = 2 Digital-Analog Outputs
D4/00 = 4 Digital-Analog Outputs
D4/D2 = 6 Digital-Analog Outputs
D4/D4 = 8 Digital-Analog Outputs
S2/00 = 2 Stepper Outputs
S4/00 = 4 Stepper Outputs
S4/S2 = 6 Stepper Outputs
S4/S4 = 8 Stepper Outputs
D2/S2 = 2 Analog and 2 Stepper
D4/S2 = 4 Analog and 2 Stepper
D2/S4 = 2 Analog and 4 Stepper
D4/S4 = 4 Analog and 4 Stepper

0 - No I/O Expansion
1 - Add 64 I/O
2 - Add 128 I/O
3 - Add 196 I/O
4 - Add 256 Digital I/O

	COM1	COM2
0	NONE	NONE
1	RS-232	RS-232
2	RS-232	RS-422
3	N/A	N/A
4	RS-422	RS-232
5	RS-422	RS-422

A0 = No Analog Inputs
A8 = 12-bit Analog-Digital Inputs
AA = 16-bit Analog-Digital Inputs

Note: Maximum of 8 Single-Ended or 4 Differential Analog-Digital Inputs.

Standard ACR8000 Includes:

- 32/64 bit Floating Point DSP CPU @ 27MHz
- Pre-Emptive Multi-Tasker
- Quadrature Encoder channels (0.1 Hz to 8MHz)
- 16-bit DAC resolution(+/- 10 VDC output standard, programmable)
- 4 MHz maximum stepper velocity
- 125 nsec - 16 usec stepper pulse width range
- 128K x 8 Zero Wait State RAM (64K x 8 System Memory / 64K x 8 bytes User Memory)
- PC-Bus or Standalone configuration
- 64 Optically Isolated 24VDC I/O (32 inputs, 32 outputs)
- 32K x 8 EEPROM (Stores critical parameters)
- Two 128K x 16 EPROM's
- Two High Speed Communications FIFO's (512 x 8)
- Free Support Tools including AcroVIEW, C++, Visual Basic, Visual C, and DOS, Windows, or Windows NT Drivers from Acroloop website.
- User's Guide, Hardware, and Training Manuals



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