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MPV920/MPV921/MPV922 High Channel Density Isolated Digital I/O Boards

Features

- Very high density of digital I/O channels
- 16 Inputs COS
- Field to bus isolation up to 1500Vrms
- Outputs can drive up to 150mA
- +5V and 24V versions
- Four MC68230 parallel interface timers
- Debounce protection

Overview

The MPV920, MPV921 and MPV922 are designed for applications requiring a large number and mixture of optoisolated digital I/O. All inputs are extremely well protected against voltage surges, and each input is capable of withstanding at least 1500Vrms for 1 minute without damage. The outputs are able to drive up to 150mA while providing high isolation. Interrupts on any 7 VME interrupt levels may be generated by all boards in response to a change-of-state of any of sixteen designated channels. All boards are available in +24 and +5 volt versions.



MPV920 (64 Inputs)

- Digital Inputs = 64 inputs arranged in 8 blocks of 8 channels.
- Input Isolation Voltage = 1500Vrms for 1 minute
- Input On/Off Threshold Voltage =
  Vin(low)=+7.0V,Vin(high)=+9.5V (+24V version)
  Vin(low)=+2.2V,Vin(high)=+3.2V (+5V version)
- Hysteresis = +0.5V (+24V version) +0.1V (+5V version)
- Input Current Drawn On One Line = +2.4mA (max)

MPV921 (16 Inputs/64 Outputs)

- Digital Inputs = 16 inputs arranged in 4 blocks of 4 channels
- Input Isolation Voltage = 1500Vrms for 1 minute
- Input On/Off Threshold Voltage =
  Vin(low)=+7.0V,Vin(high)=+9.5V (+24V version)
  Vin(low)=+2.2V,Vin(high)=+3.2V (+5V version)
- Hysteresis = +0.5V (+24V version) +0.1V (+5V version)
- Input Current Drawn On One Line = +2.4mA (max)
- Digital Outputs = 64 outputs arranged in 8 blocks of 8 lines
- Output Current (on state) = 150mA (max)

MPV922 (40 Inputs/32 Outputs)

- Digital Inputs = 40 inputs arranged in 2 blocks of 4 channels and 4 blocks of 8 channels.
- Input Isolation Voltage = 1500Vrms for 1 minute
- Input On/Off Threshold Voltage =
  Vin(low)=+7.0V,Vin(high)=+9.5V (+24V version)
  Vin(low)=+2.2V,Vin(high)=+3.2V (+5V version)
- Hysteresis = +0.5V (+24V version) +0.1V (+5V version)
- Input Current Drawn On One Line = +2.4mA (max)
- Digital Outputs = 32 outputs arranged in 4 blocks of 8 lines.
- Output Current (on state) = 150mA (max)

System

- VMEbus Compliance = A16: D08 (O) Slave
- Bus Interrupter = IRQ (1-7)
- Mechanical Format = 6U; Single Width
- Power Requirements = +5V ±5% at 1.5A max. (VME)
  External = +5V or +24V (isolated digital logic)
- Operating Temperature = 0°C to 60°C

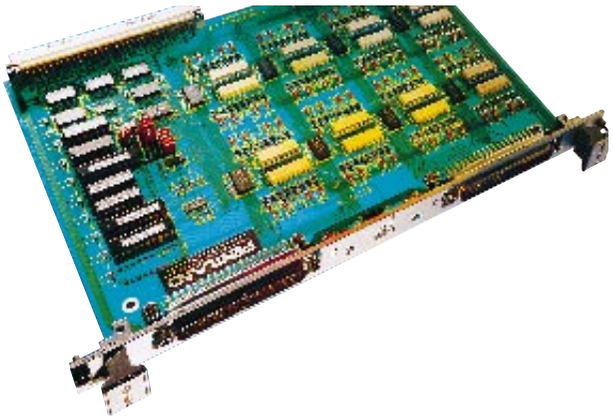
Ordering Information

MPV920A = +24V Optoisolated Digital Input Board
MPV920C = +5V Optoisolated Digital Input Board
MPV921A = +24V Optoisolated Digital I/O Board
MPV921C = +5V Optoisolated Digital I/O Board
MPV922A = +24V Optoisolated Digital I/O Board
MPV922C = +5V Optoisolated Digital I/O Board

MPV925 64 Channels Isolated Digital Input

Overview

The logic level on each channel is obtained using word/byte access to onboard channel input registers. No set-up or board initialisation is required. Three front panel status LED indicators are provided, one of which is user selectable. The board incorporates reverse bias and over voltage protection on all inputs to prevent fatal system damage in event of an overvoltage accident at any of the inputs.



Input

- Channels = 64
- Max Input Without Damage = +35VDC, -70VDC
- Isolation (Field to Bus) = 500 VDC
- Switching Thresholds
  MPV925A 0=4VDC, 1=17VDC
  MPV925B 0=3VDC, 1=9VDC
  MPV925C 0=0.8VDC, 1=3.4VDC
  MPV925D 0=6VDC, 1=14VDC

System

- VMEbus = A24/A16: D16/D08/Slave
- Mechanical Format = 6U; Single Width
- Typical VMEbus Transfer Rate = 4.35 MBytes/s
- Power Requirements = +5V ±5% @ 1A Max.(VME)
- Front panel status indicators & user LEDs
- User configurable ID and Type PROMs

Ordering Information

MPV925A: 64 Ch, Opto-Isolated, +24V Digital Input
MPV925B: 64 Ch, Opto-Isolated, +12V Digital Input
MPV925C: 64 Ch, Opto-Isolated, +5V Digital Input
MPV925D: 64 Ch, Opto-Isolated, +24V Digital Input

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