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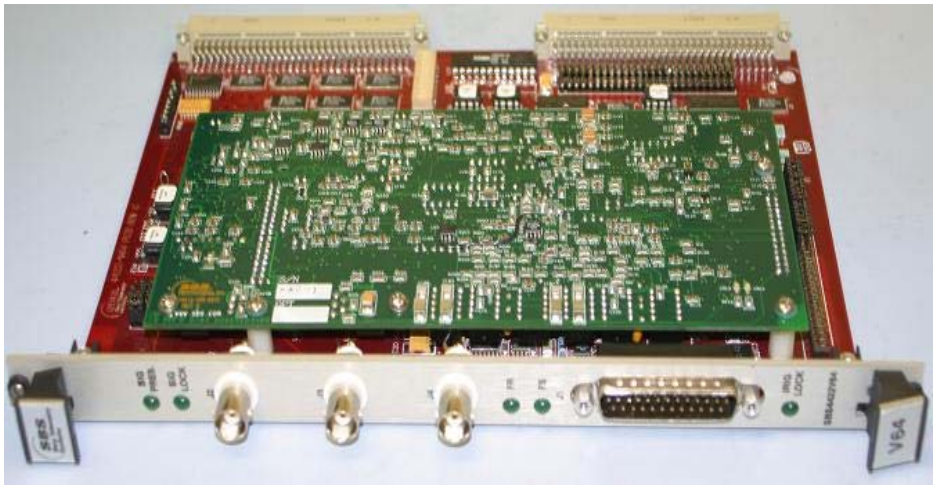
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Find the *Philips 4422-V64* at our website: ***Click HERE***

4422 - V64



4422 - V64
Telemetry Module

Features

- Supports up to 20 Mbps PCM Decommutation
- 3-16 bits of variable word length processing
- VME-64 Bus-Interface for High-Speed Data Transfers
- Subframe Methods such as: SFID, FAC, & URC
- Up to 10 Mbps of Full-fidelity PCM Simulation

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Summary

Phillips' 4422-V64 is a complete, system level telemetry module for the VME bus. The module supports all of the required data acquisition for the input data streams at rate up to 20 Mbps.

The 4422-V64 contains an optical bit synchronizer, PCM decommutator, optional IRIG time code Reader, and dynamic PCM simulator. The optional bit synchronizer accepts all PCM codes and includes a tape driver output. The decommutator supports variable frame and word length formats. The simulator features dynamic time code reader accepts IRIG (A or B) time codes. You can program it to utilize system time in the absence of an IRIG time source.

The 4422's VME-64 interface allows high rate block to mode transfers across the VME backplane. This makes the optimal platform for the multi-system configurations. It is the perfect choice for ground station requiring premium performance in the most compact form factor.

Specifications

Performance Decommutator:

Input data rate:
10 bps to 20 Mbps

Word Length:
Variable (VWL), programmable 3 to 16 bits on a word-by-word basis

Minor Frame Length:
2 to 8,191 words per minor frame

Bit order:
MSN-first or LSB-first (programmable word-by-word unless PDD operation selected

Specifications Continued

Frame sync pattern:
Up to 64 bits
Frame sync location:
Leading or trailing the frame
Frame sync strategy:
Adaptive mode (search-verify-lock) & burst mode (search-lock)
Sync error tolerance:
Programmable 0 15 bits
Sync slip window:
Programmable 1- or 3-bit window
Data polarity:
Normal, inverted, or automatic
Major frame length:
Up to 250 frames
Subframe sync:
FCC, SFID or URC (optional)
URC Location:
May be any 64- bit window within the first frame
SFID Location:
Any contiguous bits not including the last bit in the minor frame
Parallel output:
TTL-compatible. May be patched to VME bus P2 User I/O pins
System output:
Buffered PCM data blocks up to 256 frames

Simulator:

Output levels:
TTL-compatible RS-422 except Bipolar PCM Output (1 V p-p into 75 ohms)
Outputs:
0° & 90° clocks, minor frame strobe, NRZ-L data, and encoded PCM data (TTL and bipolar)
PCM codes:
PCM Outputs may be NRZ-L/M/S, BIØ-L/M/S, D,-M/S, M^2, M^2-S, RZ RNRZ11, RNRZ15, INVERTED NRZ-L, BIØ-L, or RZ
Output data rate:
1.0 bps to 10.0 Mbps (NRZ codes) 1.0 bps to 5.0 Mbps (BIØ, DM, RZ codes)
TTL- Compatible external clock:
1Hz-10MHz
Word Length:
Variable (VWL), programmable 3 to 16 bits on a word-by-word basis
Bit order:
MSB/LSB-first
Minor frame length:
2 to 4,095 words per minor frame
Frame sync pattern:
Up to 256
Major frame length:
Up to 256 frames
Subframe sync:
FCC, FAC, or SFID
SFID location:
Any contiguous bits in any word in the minor frame
Common words:
Based on word location, up to 2,048 values
Unique words:
Up to 7 programmable
Dynamic words:
Up to 5 programmable via look-up tables

Irig Time Code Reader:

Input format:
IRIG A or B
Carrier (non-real time):
250 Hz to 20 kHz
Input level:
1 V (peak-to-peak) nominal
Latency:
2 µs (maximum)
Outputs:
Automatic time tags for PCM data blocks
Alternate Source:
System Clock

Mechanical:

Dimensions:
9.187”H x 6.299”W
Form Factor:
Single 6U x 160mm VME card
Connectors:
3 BNCs
DB25
50-pin parralel output header

Environmental:

Operating:
Temperature: 0° to 50° C
Humidity: 0% to 90% non-condensing
Non-Operating (in proper packing)
Temperature: -25° to +70° C
Humidity: Packing must prevent contact with moisture and containments
Special Handling:
Standard ESD handling methods are recommended

Ordering Information

Part Number: 4422 – V64

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