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Find the *Philips PMC120* at our website: *Click HERE*

PHILLIPS

PRELIMINARY

PMC120

PMC120
TTL I/O PMC

Features

- 64 Channel TTL interface
- 50MHz transfer rate
- User loadable FPGA for automated data processing
- PCI mezzanine card (PMC) form factor
- 32-bit 33 Mhz PCI bus interface
- Integrated over-voltage protection on all lines

Phillips Aerospace
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Summary

Phillips' PMC120 TTL I/O module interface card is an intelligent digital interface capable of supporting development and validation testing. The PMC120 has an on-board field programmable gate array (FPGA) to support bus emulation, protocol simulation, test algorithm generation and other data conversion functions.

The PMC120's Spartan 3 has capacity and memory to support complex operations decoupled from the PCI bus. This allows the PMC120 to operate at high speed without being interrupted by the host controller.

The user can program the FPGA through the PMC interface, or via on-board EPROM so the board is automatically loaded. Example FPGA code and driver source code are provided with the PMC120.

Custom versions of the PMC120 are available to our customers. Please contact the factory for further information.

Specifications

Channels:	64 user-defined I/O, direction control for each line
Clock Rate:	50MHz max
Output:	H>2V into 50Ω, L<0.2V Sink 64mA Source 32mA
Input:	H>2V, L<0.8V
Termination:	120Ω
OVP:	Transient suppression on all lines
Clocks:	On-board crystal
FPGA:	Spartan 3
Connector:	VHDCI 68-pin receptacle
Power:	+5V @ 3.2A

Ordering Information

Hardware

748-1120
PMC120 64 channel TTL

Software

PMC-API-INT
Integrity® software support package
PMC-API-VXW
VxWorks® software support package
PMC-API-QNX
QNX® software support package
PMC-API-LNX
Linux™ software support package
PMC-API-WIN
Windows® DLL software package

Accessories

TP-DIN68S
Termination panel, VHDCI receptacle
CBSCSI-6803MM
Cable, shielded, 3-ft, VHDCI plug at both ends

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