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Find the *Philips PMC130* at our website: *Click HERE*

PHILLIPS

PRELIMINARY

PMC130

PMC130
LVDS I/O PMC

Features

- 32 Channel Low Voltage Differential Signaling (LVDS) interface
- 50MHz I/O clock rate
- User loadable FPGA for automated data processing
- PCI mezzanine card (PMC) form factor
- 32-bit 33 Mhz PCI bus interface
- Integrated over-voltage protection on all lines

Phillips Aerospace
16125 East Gale Ave.
Industry Ca 91745
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Summary

Phillips' PMC130 module interface card is an intelligent digital interface capable of supporting development and validation testing. The PMC130 has an on-board field programmable gate array (FPGA) to support bus emulation, protocol simulation, test algorithm generation and other data conversion functions.

The PMC130's Spartan 3 has capacity and memory to support complex operations decoupled from the PCI bus. This allows the PMC130 to operated at high speed without being interrupted by the host controller.

The user can program the FPGA through the PMC interface, or via on-board EPROM so the board is automatically loaded. Example FPGA code and driver source code are provided with the PMC130.

Custom versions of the PMC130 are available to our customers. Please contact the factory for further information.

Specifications

Channels:	32 user defined bi-directional I/O pairs
Clock Rate:	50MHz max
Out Levels	+/-140mV-360mV differential on +1V-1.65V offset voltage
Input:	+/-100mV
OVP:	Transient suppression on all lines
Clock:	On-board crystal driven
FPGA:	Spartan 3
Connector:	VHDCI 68-pin receptacle
Power:	+5V @ 2.6A

Ordering Information

Hardware

748-1130
PMC130 32 channel LVDS

Software

PMC-API-INT
Integrity® software support package
PMC-API-VXW
VxWorks® software support package
PMC-API-QNX
QNX® software support package
PMC-API-LNX
Linux™ software support package
PMC-API-WIN
Windows® DLL software package

Accessories

TP-DIN68S
Termination panel, VHDCI receptacle
CBSCSI-6803MM
Cable, shielded, 3-ft, VHDCI plug at both ends

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