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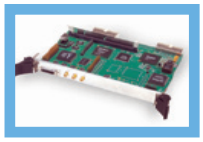
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Find the *Pulse Instruments PI-4001* at our website: *Click HERE*







PI-4000 Series Low-Noise Clock Driver and DC Bias System

Features:

- Low-Noise Design
- Modular and Upgradable
- High Precision and Accuracy
- Programmable Voltages, T_R/T_F, and Voltage Limits
- Bias Voltages from -25 to +36 V
- Clock Rates up to 100 MHz
- Voltage-sense and Current-sense outputs
- Backward Compatible

Applications:

- CCD Characterization and Test
- FPA Characterization and Test
- CMOS Imager Characterization and Test

Introduction:
The PI-4000 Series represents a no-compromises approach to providing low-noise, flexible stimulus for the test and characterization of high-performance CCDs, IR FPAs, and CMOS Image Sensors. In conjunction with Pulse Instruments or third-party pattern generators, the 4000 Series can drive a wide variety of imaging devices in standard or non-standard operating modes.

This flexibility enables device designers, manufacturers, and integrators to test and optimize device performance under a wide range of operating conditions.

Instrument Description:
The PI-4000 Series comprises one or more PI-4002 or PI-4002A Instrument Mainframes, one or more Clock Driver or DC Bias plug-in cards, and one PI-4003 DC Power Mainframe. It also requires a system controller—either a USB-equipped PC or a CompactPCI mainframe (6U) with a PI-31001 PI-Bus Controller Card and CPU card.

Each PI-4002/PI-4002A mainframe has 8 instrument card slots that can be populated with various combinations of clock driver and/or DC Bias cards.

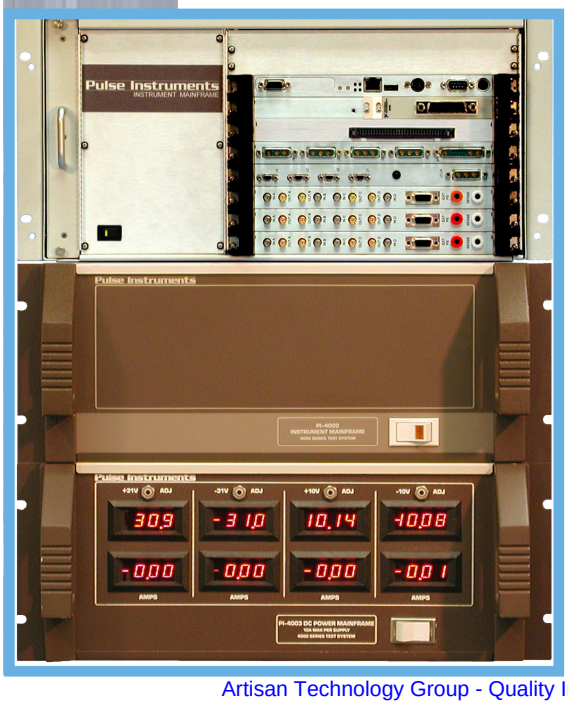
Low-noise, linear DC power is supplied by the PI-4003 DC Power Mainframe, which can power up to 3 PI-4002/PI-4002A Instrument Mainframes, depending on the specific card configuration.

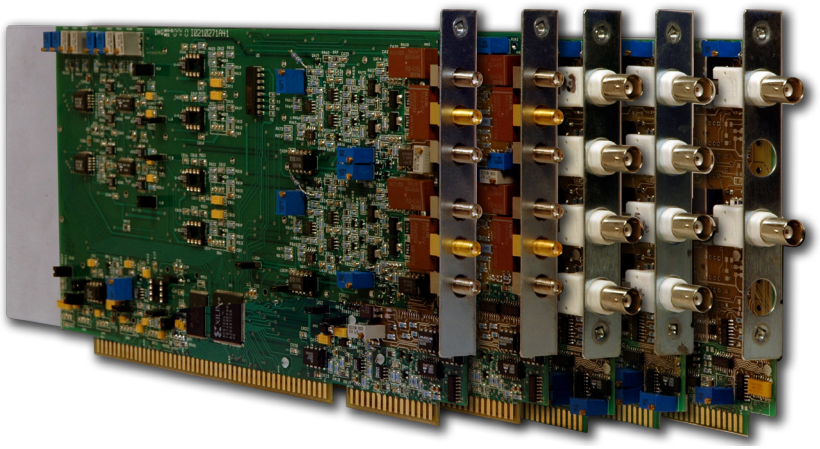
Optically-isolated digital control is provided either via USB or via the PI-Bus from the PI-Bus Controller Card in a separate mainframe. The PI-4000 mainframes are floating with respect to the control mainframe. This permits separation of the analog and digital domains in the system for maximum noise immunity.

The PI-4000 Series provides a host of device protection features, such as a Disconnect mode, separate programmable voltage on/off sequencing, programmable voltage limits, and programmable current limits (selected models). There is also protection against transients that could damage the DUT. When the power is turned on or off, the output voltages on all drivers and bias supplies are held within ± 3 V. A reset feature also sets all outputs voltages to 0 V in the event that any channel loses power.

Software Description:
The PI-4000 Series can be programmed via PI-Controller, PI-DATS, or by a custom application using the fully-documented ASCII command set.

PI-Controller and PI-DATS are Pulse Instruments' GUI-based test and characterization applications. Custom applications can program the instrument cards by passing ASCII commands to the controller by DLL or via GPIB. The ASCII command set is backward compatible with that used to program the older PI-4001 Control Mainframe.





Pulse Instruments

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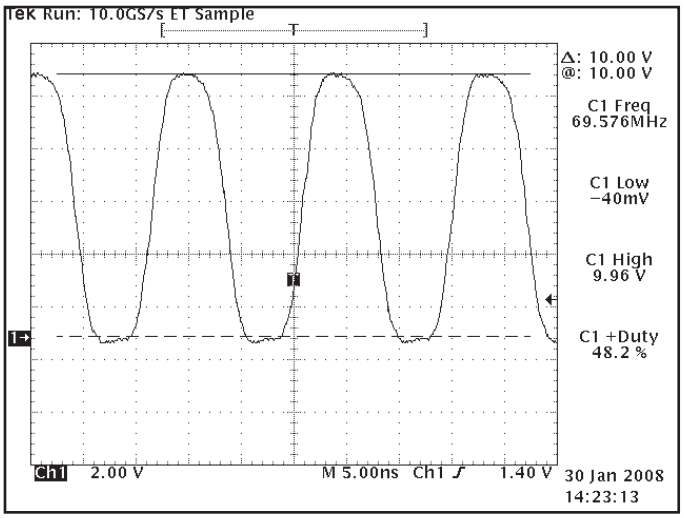
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Compatibility:

Older 4000 Series equipment may contain a PI-4001 Control Mainframe, which is no longer available or supported. The PI-4001 can be replaced either by a PI-Bus Controller Card (for CompactPCI-based systems) or by a USB-equipped PC.

Existing PI-4002 mainframes with Rev. 2 Opto cards are compatible with the PI-Bus Controller card. Older PI-4002 mainframes can be made compatible with the PI-Bus Controller card or with a USB controller by installing new Opto cards. Nearly all existing driver and bias cards are supported by the new controllers, with the exception of the 40480 Clock Driver Card and 40770 Four-Quadrant Bias Card.

40xxx cards must be installed in PI-4002 mainframes. 42xxx may be installed in PI-4002A mainframes or in modified PI-4002 mainframes with extra cooling.



PI-42490 Clock Driver, 10 V_{PP} at 70 MHz into 1 MΩ

SPECIFICATIONS:

PI-4002/4002A INSTRUMENT MAINFRAME

Card Slots: Eight. Maximum load determined by card mix.
DC Input: +31 VDC, -31 VDC, +10 VDC.
Dimensions: 7.0" H x 17.5" W x 19.625" D.
Weight: 15 lbs.
Temperature: 0° C to 50° C.
I/V Sense Output: 2 BNC connectors isolated from chassis ground

PI-4003 DC POWER MAINFRAME

Output Voltages/Current:
+31 VDC: 10 A
-31 VDC: 10 A
+10 VDC: 10 A
-10 VDC: 10 A (Not implemented; reserved for future applications)
Load Regulation: ±0.1% (0 to 100% full load)
Line Regulation: 0.2% (20% line change)
Noise and Ripple: 0.1% peak to peak
Transient Response: less than 50 msec for 50% load change
Temperature Coefficient: ±0.01%/°C maximum
Stability: ±0.1% maximum for 24 hours after warm-up

40460 2 CHANNEL DRIVER CARD

Output Repetition Rate (into 1 MΩ)
20 V Amplitude 0-8 MHz
10 V Amplitude 0-16 MHz
Output High Level: -19.48 V to +20.47 V into 1 MΩ
Output Low Level: -20.47 V to + 19.47 V into 1 MΩ
Resolution: 10 mV (11 bits plus sign bit)
Accuracy: 0.1% of programmed value ±100 mV for positive going transition time set to min. and may increase to 125 mV max. for transition times > minimum value (into 1 MΩ)
Output Amplitude: 1 V to 25 V into 1 MΩ
Rise And Fall Times/Accuracy (@ 20 V_{PP}, 40 ns-40 µs in 3 Ranges):

40 ns-400 ns	15% ±5 ns, except T_R/T_F ±40 ns
400 ns-4,000 ns	5% ±20 ns
4,000 ns-40,000 ns	5% ±200 ns

Output Polarity: Normal or inverted, programmable
Output Resistance: 50 Ω ±10%.
Output Noise:

1 Hz	3.5 µV/√Hz
10 Hz	900 nV/√Hz
100 HZ	400 nV/√Hz
1 kHz	300 nV/√Hz

Clock Output: BNC Connector, isolated from chassis ground.
TTL Input: BNC Connector, optically isolated from clock output and chassis ground.
Current Sense Output/Accuracy: 3 Ranges:

1 mA/V	2% of reading ±3 µA for ±1 mA
10 mA/V	2% of reading ±30 µA for ±10 mA
25 mA/V	2% of reading ±300 µA

Voltage Sense Output/Accuracy: 2 Ranges:

1 V/V	0.2% reading ±4 mV for ±2 V
0.1 V/V	0.2% reading ±40 mV

40465 2 CHANNEL TRI-LEVEL DRIVER CARD (1)

Output Repetition Rate (into 1 MΩ)
20 V Amplitude: 0-10 MHz (2-Level Mode)
5 MHz (4-Level or Tri-Level Mode)
Operating Conditions: HL-LL>2 V, MHL-LL>2 V, HL-MLL>2 V, MH>MLL

	1 MΩ load// 100 pf	50 Ω load// 100 pf
High Level:	-18.00 V to +20.00 V	-9.0 V to +10 V
Low Level:	-20.00 V to + 18.00 V	-10 V to +9.0 V
Resolution:	10 mV	5 mV
Accuracy:	0.1% of Prog. value ±100 mV	5% of Prog. value ±50 mV
MHL & MLL:	-18.00 V to +18.00 V	-9.0 V to + 9.0 V
Resolution:	10 mV	5 mV
Accuracy:	0.1% of Prog. value ±100 mV	5% of Prog. value ±50 mV
In Tri-Level Mode:		
MHL=MLL=ML:	Accuracy of Setting	
MHL=MLL:	1% of Prog. value ±300 mV	5% of Prog. value ±50 mV
Output Ampl:	1 V to 23 V	0.5 V to 11.5 V

Output/Input Connectors: BNC, isolated from chassis ground
Tri-Level Input: 2 TTL Timing Channels, BNC connectors
Rise And Fall Times/Accuracy (@ 20 V_{PP}, 30 ns-30 µs in 3 Ranges):

30 ns-300 ns	10% of Prog. value ±5 ns
300 ns-3,000 ns	5% of Prog. value ±30 ns
3,000 ns-30,000 ns	5% of Prog. value ±200 ns

Output aberrations:
2-Level Mode: 5% of amplitude ±200 mV
4-Level or Tri-Level Mode: 5% of amplitude ±350 mV
Output Polarity: Invert/Noninvert, programmable
Output Resistance: 50 Ω ± 10% or 10 Ω ± 10%, programmable
Output Noise:

1 Hz	3 µV/√Hz
10 Hz	800 nV/√Hz
100 Hz	400 nV/√Hz
1 kHz	300 nV/√Hz

External Source Input:
Input Resistance: 500 Ω ±10%
Input Voltage Range: 0 to 5 V max
Optically Isolated Input:
300 Ω in series with LED
Input Voltage Range 3 V to 6 V
Jitter in Opto mode 4 ns max
Current Sense Output:

1 mA/V	2% of reading ±3 µA for ±1 mA
10 mA/V	2% reading ±30 µA for ±10 mA
25 mA/V	2.5% reading ±300 µA

Voltage Sense Output:

1 V/V	0.2% of reading ±4 mV for ±2 V
0.1 V/V	0.2% of reading ±40 mV

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42460 2 CHANNEL DRIVER CARD

Output Repetition Rate (into 1 MΩ)
20 V Amplitude 0-16 MHz
10 V Amplitude 0-32 MHz
Output High Level: -18 V to +20 V into 1 MΩ
Output Low Level: -20 V to + 18 V into 1 MΩ
Resolution: 10 mV (11 bits plus sign bit)
Accuracy: 0.1% of programmed value ±100 mV for positive-going transition time set to min. and may increase to 125 mV max. for transition times > minimum value (into 1 MΩ)
Operating Conditions: HLV-LLV > 0.5 V
Output Amplitude: 0.5 V to 22 V into 1 MΩ
Rise And Fall Times/Accuracy @ 20 V_{PP}: 20 ns-200 ns, 10% ±5 ns
Output Polarity: Normal or inverted, programmable
Output Resistance: 50 Ω ±10%.
Output Noise:

1 Hz	3.5 μV/√Hz
10 Hz	900 nV/√Hz
100 HZ	400 nV/√Hz
1 kHz	300 nV/√Hz

Clock Output: SMA Connector, isolated from chassis ground.
TTL Input:
Connector: SMA, isolated from chassis ground
Input Resistance: 50 Ω or 500 Ω ±10%, programmable
Input V_{HI}: 3 to 5 V
Optically Isolated Input:
Connector: SMA, isolated from chassis ground
Input Impedance: 300 Ω in series with LED
Input V_{HI}: 3 V to 5 V
Minimum Pulse Width: 25 ns
Max Clock Rate: 20 MHz
Current Sense Output/Accuracy: 3 Ranges:

1 mA/V	2.5% of reading ±3 μA ±1 mA
10 mA/V	2.5% of reading ±30 μA ±10 mA
25 mA/V	2.5% of reading ±300 μA

Voltage Sense Output/Accuracy: 2 Ranges:

1 V/V	0.5% reading ±4 mV for ±2 V
0.1 V/V	0.5% reading ±30 mV

42490 HIGH SPEED DRIVER CARD

Output Characteristics:	1 MΩ load//15 pf	50 Ω Load
Max. Repetition Rate 1: (Input Duty Cycle ≠ 50%)	100 MHz @16 V_{PP}	100 MHz @ 8 V_{PP}
Max. Repetition Rate 2: (Input Duty Cycle = 50%)	70 MHz @ 10 V_{PP}	70 MHz @ 5 V_{PP}
High Level:	-7 V to +17 V	-3.5 V to +7.5 V
Low Level:	-17 V to +7 V	-7.5 V to +3.5 V
Resolution:	10 mV	5 mV
Accuracy:	0.1 % of Prog. value ±100 mV	5% of Prog. value ±50 mV
Amplitude:	1 V to 18 V	0.5 V to 9.5 V
Rise and Fall Times:	<5 ns @ 10 V_{PP} <7 ns @ 15 V_{PP}	<3.5 ns @ 5 V_{PP}

Operating Conditions: HLV-LLV > 1 V
Output Aberrations: 5% of V_{PP} +300 mV for V_{PP}> 2 V
Output Resistance: 50 Ω ±10%
Output Polarity: Normal or Inverted, programmable
TTL Input:
Connector: SMA, isolated from chassis ground
Input Resistance: 50 Ω or 500 Ω ±10%, programmable
Input V_{HI}: 3 to 5 V
Optically Isolated Input:
Connector: SMA, isolated from chassis ground
Input Impedance: 300 Ω in series with LED
Input V_{HI}: 3 V to 5 V
Minimum Pulse Width: 25 ns
Max Clock Rate: 20 MHz
Current Sense Output/Accuracy: 3 Ranges:

1 mA/V	2% of reading ±3 μA for ±1 mA
10 mA/V	2% of reading ±30 μA for ±10 mA
100 mA/V	2.5% of reading ±300 μA

Voltage Sense Output:

1 V/V	0.2% reading ±20 mV for ±2 V
0.1 V/V	0.5% reading ±200 mV

40750 4 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD

	Range 1	Range 2
Output Voltage:	-10.24 V to +10.235 V	-20.48 V to +20.47 V
Resolution:		
Normal Mode:	5 mV	10 mV
Hi Resolution:	0.25 mV	0.50 mV
Output Noise:		
1 Hz	250 nV/√Hz	350 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	75 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	50 nV/√Hz	50 nV/√Hz
Accuracy:	0.1% of programmed value ±20 mV	
Output Current:	±25 mA max. for -20 V ≤ V_O ≤ +20 V.	
Output Resistance:	75 mΩ max. for I_O ≤ +25 mA.	
Current Sense:		
1 mA/V	0.2% reading ±1 μA for ±1 mA	
10 mA/V	0.2% reading ±10 μA ±10 mA	
100 mA/V	0.25% reading ±100 μA	
Voltage Sense:		
1 V/V	0.2% reading ±0.5 mV for ±2 V	
0.1 V/V	0.2% reading ±5 mV	

40752 4 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD

	Range 1	Range 2
Output Voltage:	-10.24 V to +10.235 V	-20.48 V to +20.47 V
Resolution:		
Normal Mode:	5 mV	10 mV
Hi Resolution:	0.25 mV	0.50 mV
Output Noise:		
1 Hz	300 nV/√Hz	400 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	75 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	50 nV/√Hz	50 nV/√Hz
Accuracy:	0.1% of programmed value ±20 mV	
Output Current:	±100 mA max. for -20 V ≤ V_O ≤ +20 V.	
Output Resistance:	75 mΩ max. for I_O ≤ +100 mA.	
Current Sense:		
1 mA/V	0.2% reading ±1 μA for ±1 mA	
10 mA/V	0.2% reading ±10 μA for ±10 mA	
100 mA/V	0.25% reading ±100 μA	
Voltage Sense:		
1 V/V	0.2% reading ±0.5 mV for ±2 V	
0.1 V/V	0.2% reading ±5 mV	

40755 2 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD

	Range 1	Range 2
Output Voltage:	-10.24 V to +10.235 V	-20.48 V to +20.47 V
Resolution:		
Normal Mode:	5 mV	10 mV
Hi Resolution:	0.25 mV	0.50 mV
Output Noise:		
1 Hz	350 nV/√Hz	450 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	75 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	50 nV/√Hz	50 nV/√Hz
Accuracy:	0.1% of programmed value ±20 mV	
Output Current:	±250 mA max. for -20 V ≤ V_O ≤ +20 V.	
Output Resistance:	75 mΩ max. for I_O ≤ +250 mA.	
Current Sense:		
1 mA/V	0.2% reading ±1 μA for ±1 mA	
10 mA/V	0.2% reading ±10 μA for ±10 mA	
100 mA/V	0.25% reading ± 100 μA	
Voltage Sense:		
1 V/V	0.2% reading ±0.5 mV for ±2 V	
0.1 V/V	0.2% reading ±5 mV	

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40757 4 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD [2]

	Range 1	Range 2
Output Voltage:	-10 V to +10 V	-20 V to +36 V
Resolution:		
Normal Mode:	5 mV	20 mV
Output Noise:		
1 Hz	600 nV/√Hz	800 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	75 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	50 nV/√Hz	50 nV/√Hz
Accuracy:	0.1% of programmed value ±40 mV	
Output Current:	±25 mA max. for -20 V ≤ V_O ≤ +36 V.	
Output Resistance:	75 mΩ max. for I_O ≤ +25 mA.	
Current Sense:		
1 mA/V	0.2% reading ±1 µA for ±1 mA	
10 mA/V	0.2% reading ±10 µA for ±10 mA	
100 mA/V	0.25% reading ± 100 µA	
Voltage Sense:		
1 V/V	0.2% reading ±0.5 mV for ±2 V	
0.1 V/V	0.2% reading ±5 mV	

40758 4 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD [2]

	Range 1	Range 2
Output Voltage:	-10 V to +10 V	-20 V to +36 V
Resolution:		
Normal Mode:	5 mV	20 mV
Output Noise:		
1 Hz	600 nV/√Hz	800 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	75 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	50 nV/√Hz	50 nV/√Hz
Accuracy:	0.1% of programmed value ±40 mV	
Output Current:	±100 mA max. for -20 V ≤ V_O ≤ +36 V.	
Output Resistance:	75 mΩ max. for I_O ≤ +100 mA.	
Current Sense:		
1 mA/V	0.2% reading ±1 µA for ±1 mA	
10 mA/V	0.2% reading ±10 µA for ±10 mA	
100 mA/V	0.25% reading ±100 µA	
Voltage Sense:		
1 V/V	0.2% reading ±0.5 mV for ±2 V	
0.1 V/V	0.2% reading ±5 mV	

42750 4 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD

	Range 1	Range 2
Output Voltage:	-10.24 V to +10.235 V	-20.48 V to +20.47 V
Resolution:	1.25 mV	2.50 mV
Output Noise:		
1 Hz	250 nV/√Hz	350 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	60 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	40 nV/√Hz	40 nV/√Hz
Accuracy:	0.1% of programmed value ±20 mV	
Output Current:	±100 mA max. for -20 V ≤ V_O ≤ +20 V, 300 mA per card	
Output Resistance:	75 mΩ max. for I_O ≤ +75 mA.	
Current Sense:		
1 mA/V	0.25% reading 1 µA for ±1 mA	
10 mA/V	0.25% reading 10 µA. for ±10 mA	
100 mA/V	0.25% reading ± 120 µA	
Voltage Sense:		
1 V/V	0.25% reading ±3 mV for ±2 V	
0.1 V/V	0.25% reading ±6 mV	

42751 4 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD WITH PROGRAMMABLE CURRENT LIMITS

	Range 1	Range 2
Output Voltage:	-10.24 V to +10.235 V	-20.48 V to +20.47 V
Resolution:	1.25 mV	2.50 mV
Output Noise:		
1 Hz	TBD nV/√Hz	TBD nV/√Hz
10 Hz	TBD nV/√Hz	TBD nV/√Hz
100 Hz	TBD nV/√Hz	TBD nV/√Hz
1 kHz	TBD nV/√Hz	TBD nV/√Hz
10 kHz	TBD nV/√Hz	TBD nV/√Hz
Accuracy:	0.1% of programmed value ±20 mV	
Current Limits:	TBD	
Output Current:	±100 mA max. for -20 V ≤ V_O ≤ +20 V, 300 mA per card	
Output Resistance:	75 mΩ max. for I_O ≤ +75 mA.	
Current Sense:		
1 mA/V	0.25% reading 1 µA for ±1 mA	
10 mA/V	0.25% reading 10 µA for ±10 mA	
100 mA/V	0.25% reading ± 120 µA	
Voltage Sense:		
1 V/V	0.25% reading ±3 mV for ±2 V	
0.1 V/V	0.25% reading ±6 mV	

42755 2 CHANNEL, BIPOLAR LOW NOISE DC BIAS CARD

	Range 1	Range 2
Output Voltage:	-10.24 V to +10.235 V	-20.48 V to +20.47 V
Resolution:		
Normal Mode:	1.25 mV	2.50 mV
Output Noise:		
1 Hz	300 nV/√Hz	400 nV/√Hz
10 Hz	75 nV/√Hz	100 nV/√Hz
100 Hz	60 nV/√Hz	75 nV/√Hz
1 kHz	50 nV/√Hz	50 nV/√Hz
10 kHz	40 nV/√Hz	40 nV/√Hz
Accuracy:	0.1% of programmed value ±20 mV	
Output Current:	±250 mA max. for -20 V ≤ V_O ≤ +20 V	
Output Resistance:	75 mΩ max. for I_O ≤ +75 mA.	
Current Sense:		
1 mA/V	0.3% reading 1 µA for ±1 mA	
10 mA/V	0.3% reading 10 µA for ±10 mA	
100 mA/V	0.3% reading ± 200 µA	
Voltage Sense:		
1 V/V	0.3% reading ±3 mV for ±2 V	
0.1 V/V	0.3% reading ±6 mV	

SPECIAL ORDER MODELS:

- **40461 2 Channel Clock Driver Card, ±25 V operating range**
- **40751 4 Channel Low Noise DC Bias, ±25 V, 25 mA/Ch**
- **High-Current CCD driver cards with 10 Ω output impedance**

Notes: (1) These cards have limited availability, but are supported by currently-shipping mainframes, controllers, and software
(2) Requires special-order DC power mainframe

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