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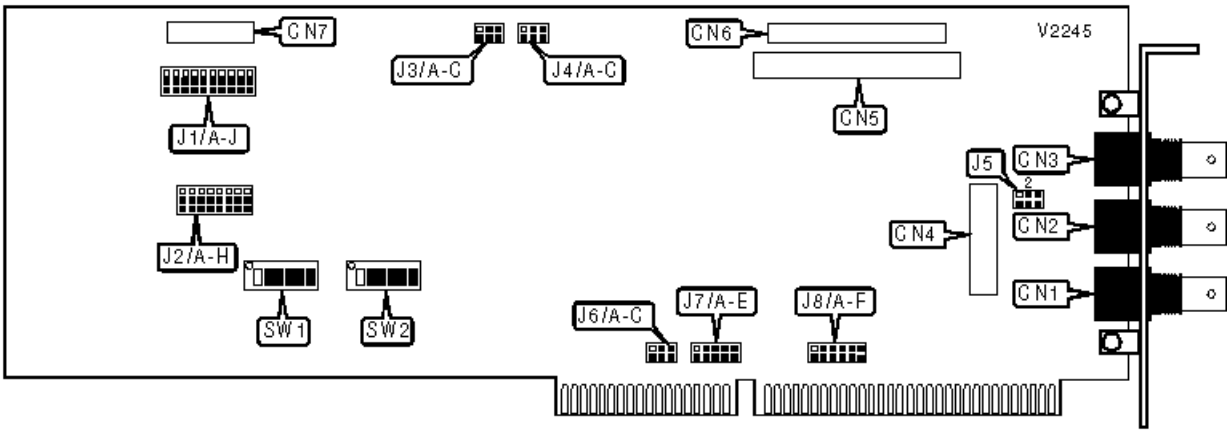
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QUATECH, INC.

WSB-100-10, WSB-100-20

Card Type Signal generator
I/O Options Analog/digital output port, Analog/digital I/O ports (2), multi-board connectors (2), output modules (2)
Data Bus 16-bit ISA



CONNECTIONS			
Function	**Label**	**Function**	**Label**
Analog/digital output port	CN1	Output module header	CN5
Analog/digital I/O port 1	CN2	Multi-board connector	CN6
Analog/digital I/O port 2	CN3	Multi-board connector	CN7
Output module header	CN4		

USER CONFIGURABLE SETTINGS			
	Function	**Label**	**Position**
»	Factory configured - do not alter	J1/A	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/B	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/C	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/D	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/E	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/F	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/G	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/H	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/I	Pins 1 & 2 closed
»	Factory configured - do not alter	J1/J	Pins 1 & 2 closed
»	Factory configured - do not alter	J2/A	Pins 1 & 2 closed
»	Factory configured - do not alter	J2/B	Pins 1 & 2 closed

»	Factory configured - do not alter	J2/C	Pins 1 & 2 closed
»	Factory configured - do not alter	J2/D	Pins 1 & 2 closed
»	Factory configured - do not alter	J2/E	Pins 1 & 2 closed
»	Card is not configured for trigger delay	J2/F	Pins 1 & 2 closed
	Card is configured for trigger delay	J2/F	Pins 2 & 3 closed
»	Factory configured - do not alter	J2/G	Pins 1 & 2 closed
»	Factory configured - do not alter	J2/H	Pins 1 & 2 closed

BASE I/O ADDRESS						
Setting		**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
	0000h	On	On	On	On	On	On
	0010h	On	On	On	On	On	On
	0020h	On	On	On	On	On	On
	0030h	On	On	On	On	On	On
	0040h	On	On	On	On	On	On
	0210h	On	On	On	On	On	On
	0280h	On	On	On	On	On	On
»	0300h	On	On	On	On	On	On
	FFB0h	Off	Off	Off	Off	Off	Off
	FFC0h	Off	Off	Off	Off	Off	Off
	FFD0h	Off	Off	Off	Off	Off	Off
	FFE0h	Off	Off	Off	Off	Off	Off
	FFF0h	Off	Off	Off	Off	Off	Off

BASE I/O ADDRESS (CON'T)							
Setting		**SW2/1**	**SW2/2**	**SW2/3**	**SW2/4**	**SW2/5**	**SW2/6**
	0000h	On	On	On	On	On	On
	0010h	On	On	On	On	On	Off
	0020h	On	On	On	On	Off	On
	0030h	On	On	On	On	Off	Off
	0040h	On	On	On	Off	On	On
	0210h	Off	On	On	On	On	Off
	0280h	Off	On	Off	On	On	On
»	0300h	Off	Off	On	On	On	On

	FFB0h	Off	Off	Off	On	Off	Off
	FFC0h	Off	Off	Off	Off	On	On
	FFD0h	Off	Off	Off	Off	On	Off
	FFE0h	Off	Off	Off	Off	Off	On
	FFF0h	Off	Off	Off	Off	Off	Off

Note: A total of 4095 base address settings are available. The switches are a binary representation of the decimal memory addresses. SW1/1 is the Most Significant Bit and switch SW2/6 is the Least Significant Bit. The switches have the following decimal values: SW1/1=32768, SW1/2=16384, SW1/3=8192, SW1/4=4096, SW1/5=2048, SW1/6=1024, SW2/1=512, SW2/2=256, SW2/3=128, SW2/4=64, SW2/5=32, SW2/6=16. Turn off the switches and add the values of the switches that are off to obtain the correct memory address. (Off=1, On=0)

ANALOG/DIGITAL I/O PORT 1 FUNCTION				
Setting	**J3/A**	**J3/B**	**J3/C**	**J5**
External clock input	Closed	Open	Open	Pins 5 & 6 closed
Trigger clock input	Open	Closed	Open	Pins 5 & 6 closed
Trigger delay input	Open	Open	Closed	Pins 5 & 6 closed
Data strobe output	Open	Open	Open	Pins 4 & 5 closed

ANALOG/DIGITAL I/O PORT 2 FUNCTION				
Setting	**J4/A**	**J4/B**	**J4/C**	**J5**
External clock input	Closed	Open	Open	Pins 2 & 3 closed
Trigger clock input	Open	Closed	Open	Pins 2 & 3 closed
Trigger delay input	Open	Open	Closed	Pins 2 & 3 closed
Data strobe output	Open	Open	Open	Pins 1 & 2 closed

DMA CHANNEL			
Setting	**J6/A**	**J6/b**	**J6/C**
DMA5	Open	Open	Closed
DMA6	Open	Closed	Open
DMA7	Closed	Open	Open

INTERRUPT					
Setting	**J7/A**	**J7/B**	**J7/C**	**J7/D**	**J7/E**
IR/FONT>	Open	Open	Open	Open	Open
IRQ3	Open	Open	Open	Open	Open
IRQ4	Open	Open	Open	Open	Open
IRQ5	Open	Open	Open	Open	Open

IRQ6	Open	Open	Open	Open	Open
IRQ7	Open	Open	Open	Open	Open
IRQ10	Open	Open	Open	Open	Closed
IRQ11	Open	Open	Open	Closed	Open
IRQ12	Open	Open	Closed	Open	Open
IRQ14	Open	Closed	Open	Open	Open
IRQ15	Closed	Open	Open	Open	Open

INTERRUPT (CON'T)						
Setting	**J8/A**	**J8/B**	**J8/C**	**J8/D**	**J8/E**	**J8/F**
IR/FONT>	Open	Open	Open	Open	Open	Closed
IRQ3	Closed	Open	Open	Open	Open	Open
IRQ4	Open	Closed	Open	Open	Open	Open
IRQ5	Open	Open	Closed	Open	Open	Open
IRQ6	Open	Open	Open	Closed	Open	Open
IRQ7	Open	Open	Open	Open	Closed	Open
IRQ10	Open	Open	Open	Open	Open	Open
IRQ11	Open	Open	Open	Open	Open	Open
IRQ12	Open	Open	Open	Open	Open	Open
IRQ14	Open	Open	Open	Open	Open	Open
IRQ15	Open	Open	Open	Open	Open	Open

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