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Find the *RadiSys EPC-3311* at our website: ***Click HERE***

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EPC-3311 DATA SHEET

PICMG2.16 COMPLIANT CPU BLADE

FEATURE SUMMARY

- Full PICMG 2.16 Compliance
- Full PICMG 2.0 R3.0 and Hot-swap
- R2.1 Compliance (operates as a host or peripheral processor)
- 1.2GHz Intel® Pentium III Processor-M
- RadiSys 82600 Chipset
- 133MHz Front Side Bus
- 512MB PC133 ECC SDRAM (max 1GB)
- Two 32-bit PMC expansion sites
- Optional 20GB Hard Drive (occupies one PMC site)
- Optional SCSI and SVGA PMC cards
- Optional Rear Transition Modules for PICMG 2.0 or PICMG 2.16
- Intelligent Platform Management Interface (IPMI)
- Front panel Reset switch and COM1 port
- 6U Compact PCI form factor, full Hot-swap capability

PRODUCT DESCRIPTION

The EPC-3311 is a high performance single slot Compact PCI® module that operates as either a host or task processor. It is fully compliant with PICMG 2.16 for use with packet-switched backplanes and the dual Gigabit Ethernet channels offer maximum bandwidth to the backplane. For legacy applications, it is also PICMG 2.0 R3.0 compliant.

The EPC-3311 utilizes the RadiSys 82600 chipset for reduced part count, fast through-put and efficient use of board real estate. It incorporates two PMC sites for flexibility allowing the use of a variety of PMC modules such as SCSI, LAN or WAN adapters. It offers a choice of two memory configurations on the main board; 512MB or 1GB.

Designed for applications that require lightning fast through-put, the EPC-3311 is powered by a 1.2GHz Intel® Pentium® III Processor-M with a 133MHz Processor System Bus speed. Include is a passive heatsink for processor cooling.

APPLICATIONS

The next generation communication infrastructure will be a packet oriented one in which applications or services such as VoIP, VPN, speech processing and a multitude of media servers will co-exist in a seamless, unified network. Additional applications include WAP servers, SS7 signaling gateways, traffic management/shaping blades, switching and advanced call center applications.

SYSTEM MANAGEMENT, RELIABILITY AND HIGH AVAILABILITY

The EPC-3311 is designed to be incorporated into High Availability (HA) solutions providing 99.999% of up time. A baseboard management controller (BMC) provides system management functionality compliant with the IPMI specification. The BMC reports the temperature of the processor, on-board voltages, and full identification of the SBC (model, serial number, etc).

CONFIGURATION FLEXIBILITY

The EPC-3311 can be configured through the use of two PMC sites, eliminating the need for an additional slot. Optional PMC modules include SCSI, SVGA, as well as other off-the shelf cards. For applications requiring mass storage, the EPC-3311 accommodates a 2.5" notebook style IDE hard disk. The installation of the hard disk occupies one PMC site. Optional Rear Transition Modules are available allowing for I/O access from the rear.

SOFTWARE SUPPORT

The EPC-3311 is compatible with Windows NT* 4.0 embedded, Linux* and VxWorks.*



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ORDERING INFORMATION

Call for pricing and availability.
Refer to the order codes below.

DESCRIPTION:

EPC-3311 with 1.2GHz Pentium III Processor-M 512MB ECC SDRAM
ORDER CODE: EPC3311-512
EPC-3311 with 1.2GHz Pentium III Processor-M 1GB ECC SDRAM
ORDER CODE: EPC3311-1G

OPTIONS:

Rear Transition Module–PICMG 2.16
For switched backplanes
ORDER CODE: EPC3311-RTM216
Rear Transition Module–PICMG 2.0
For non-switched backplanes
ORDER CODE: EPC3311-RTML
On-board 20GB IDE Hard Drive Kit
Note: occupies one PMC site
ORDER CODE: EPC3311-HDD
SCSI PMC, includes PIM for RTM
ORDER CODE: PMCSCSI
VGA PMC
ORDER CODE: PMCSVGA IIA

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SPECIFICATIONS

FEATURE	FUNCTION	DESCRIPTION	
Physical	Dimensions	6U CompactPCI, single slot	
	Compliance	PICMG 2.0 R3.0, Hot-swap R2.1, PICMG 2.16	
Processor System	CPU	1.2GHz Intel® Pentium III Processor-M	
	Cache	Integrated 512K L2	
	Chipset	RadiSys 82600	
	FSB	133MHz	
Memory	Technology	3.3V PC133, ECC	
	Capacity	512MB or 1GB	
	Sockets	One for custom memory module	
Ethernet	Interface	Dual 10/100/1000BaseTX	
	Controller	Intel® 82544EI	
Hard Drive Interface	Interface	UltraDMA/66 IDE (one channel)	
	Controller	Integrated in the 82600	
	Connector	On-board 44-pin header	
Front Panel I/O	Serial	One DB-9 connector	
	Reset	Momentary push button	
RTM I/O	-	PICMG 2.0 (non-switched)	PICMG 2.16 (switched)
	Keyboard/Mouse	One male PS/2 connector	One male PS/2 connector
	USB	One USB Type A connector	One USB Type A connector
	Ethernet	Two RJ-45 connectors with Link/Activity LED indicators	-
	Serial	One male DB-9 connector	One male DB-9 connector
	IDE	One 44-pin header	One 44-pin header
	PIM	One PIM sites	Two PIM sites
Operating System	Compatibility	Windows NT 4.0 embedded, Linux, VxWorks	
Misc	IPMI	Dual IPMB	
	PMC	Two 32-bit, 33MHz sites (optional IDE HDD occupies one site)	
	Watchdog Timer	Programmable two-stage, soft or hard reset	
	Compact Flash	On-board socket, optional 32MB, 64MB, or 64MB Compact Flash	
	BIOS	PhoenixBIOS* with 2MB Strataflash (optional 4MB)	
	RTC	Integrated in 82600, accurate to ±13 seconds per year	
Power Requirements	Power (typical)	20W (includes 1.2MHz CPU and 512MB memory)	
	Current	+3.3V +5V +12V -12V	
		2.5A 2.0A 0.1A 0.1A	
Environment	-	Operating	Non-Operating
	Temperature	5° C to 55° C (derated 2° C per 1000 ft (300 m) over 6000 ft (2000 m))	-20° C to 60° C
	Humidity	20% to 80% RHNC	5% to 85% RHNC
	Shock	5g, 11ms duration, half-sine shock pulse	70g 11ms, half-sine shock pulse
	Vibration	5Hz @ 0.01g2/Hz to 0.02g2/Hz @ 20Hz	0.06g2/Hz from 5 to 1,000Hz random, 10 min. per sweep cycle
	Airflow	300 LFM	-
	Altitude	15,000 ft (4,500 m)	40,000 ft (12,000 m)
Regulatory	Safety	UL 1950	
		CB Scheme to IEC 950	
	EMC	FCC Part 15 Class B	
		EN55022: 1998 Class B	
		EN50024: 1998 Class B	
	NEBS	Designed for NEBS compliance	
Warranty	-	Two years, parts only	

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World Headquarters
5445 NE Dawson Creek Drive
Hillsboro, OR 97124 USA
Phone: 503-615-1100
Fax: 503-615-1121
Toll-Free: 800-950-0044
www.radisys.com
info@radisys.com

International Headquarters
10 Centech Park
Fringe Meadow Road, Redditch
Worcestershire, B98 9NR
United Kingdom
Phone: +44 (0) 1527 588800
Fax: +44 (0) 1527 588801

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